Elektrobit

JOTACTIC 捷策動能



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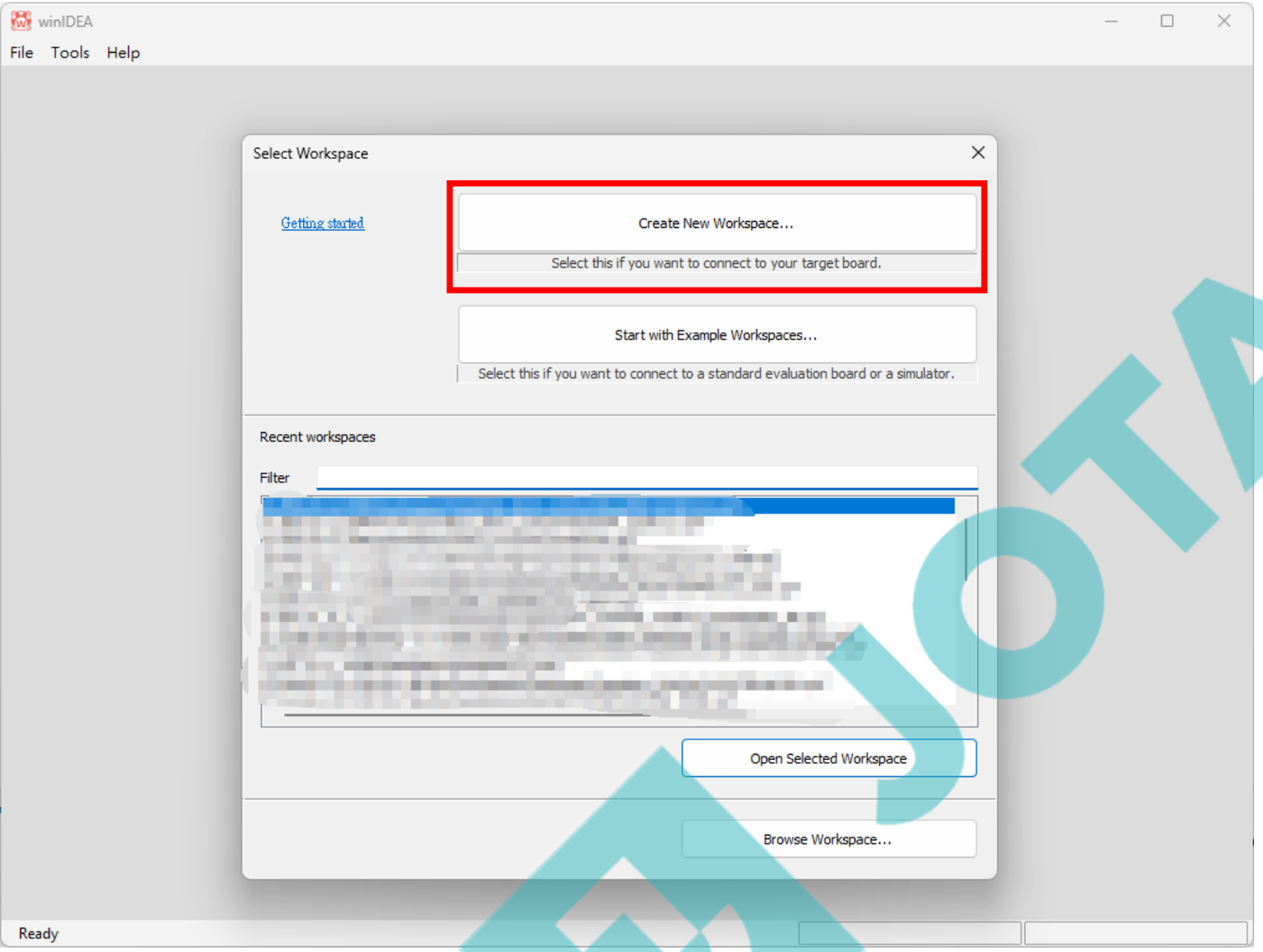




- 創建 iSYSTEM 專案
- Debug 功能介紹
- Debug 介面設定
- Architecture-specific plugins
- daqIDEA
- Tasks Window
- testIDEA

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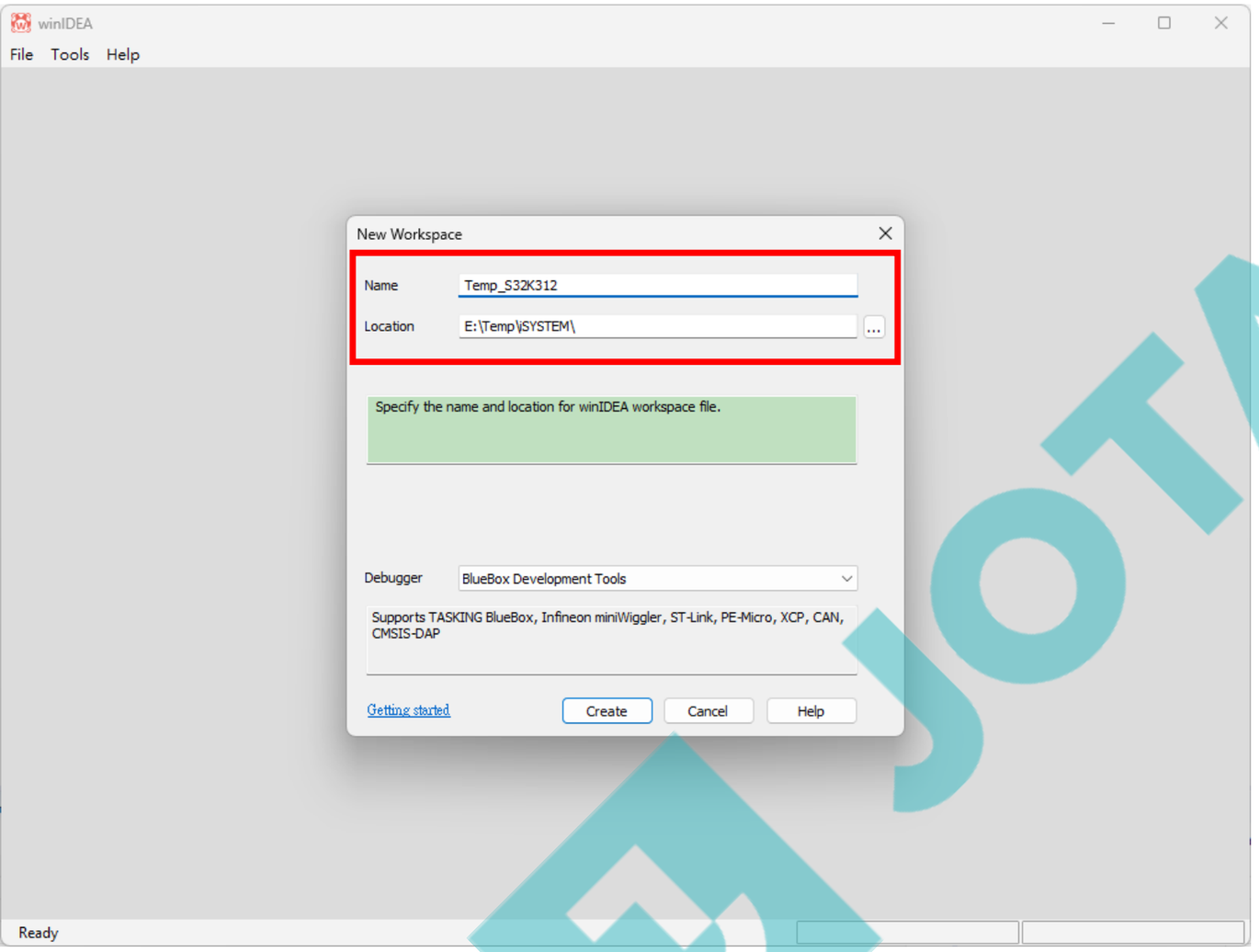
創建iSYSTEM專案



選擇創建新的Workspace用來存放debug時所產生的相關資料

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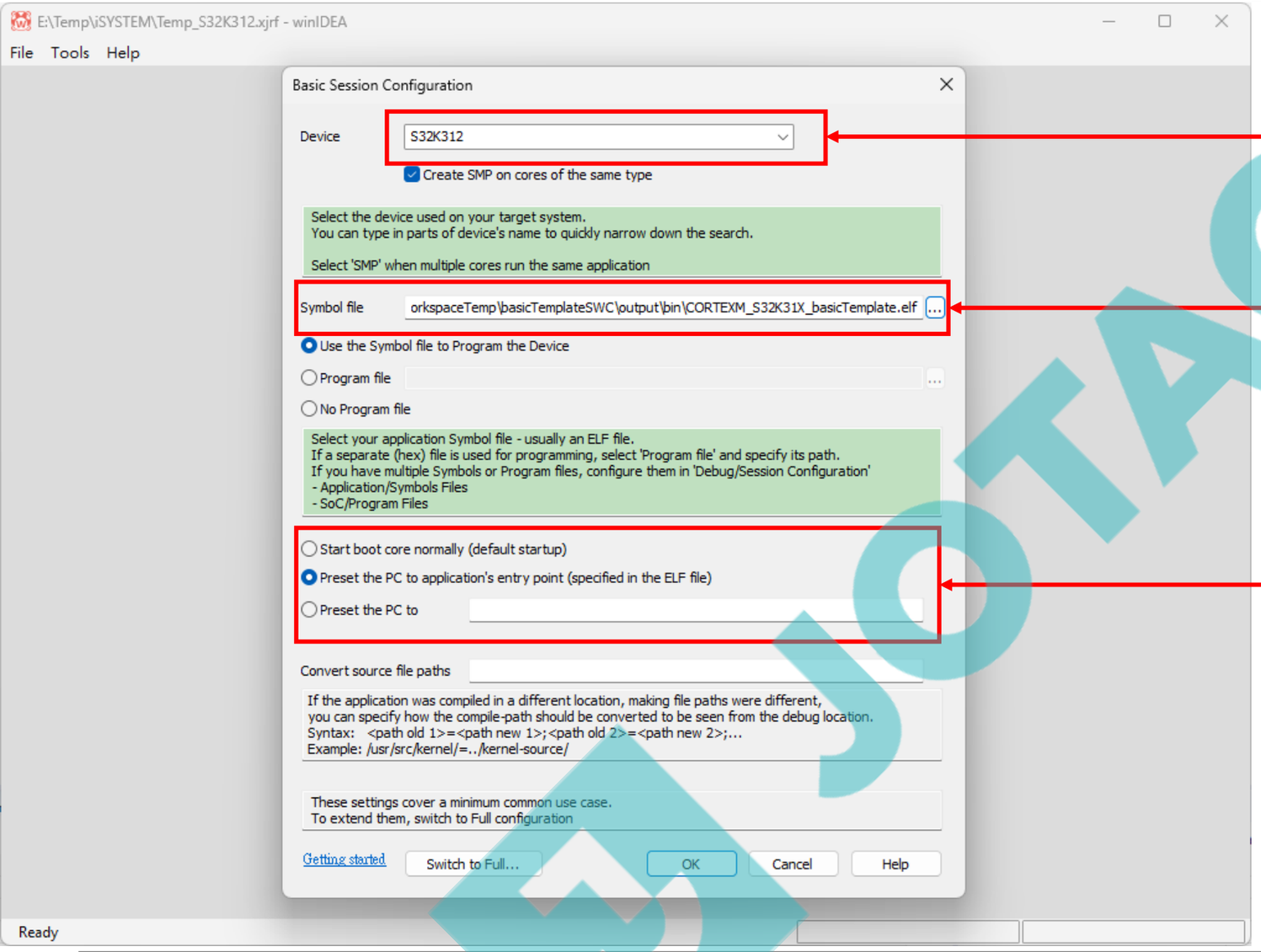
創建iSYSTEM專案



定義專案的名稱和資料存放的位置

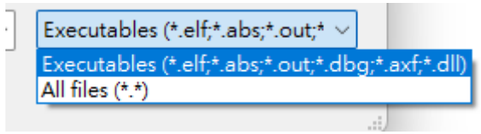
JOTACTIC

創建iSYSTEM專案



選擇目標晶片

載入要燒入到晶片上的檔案

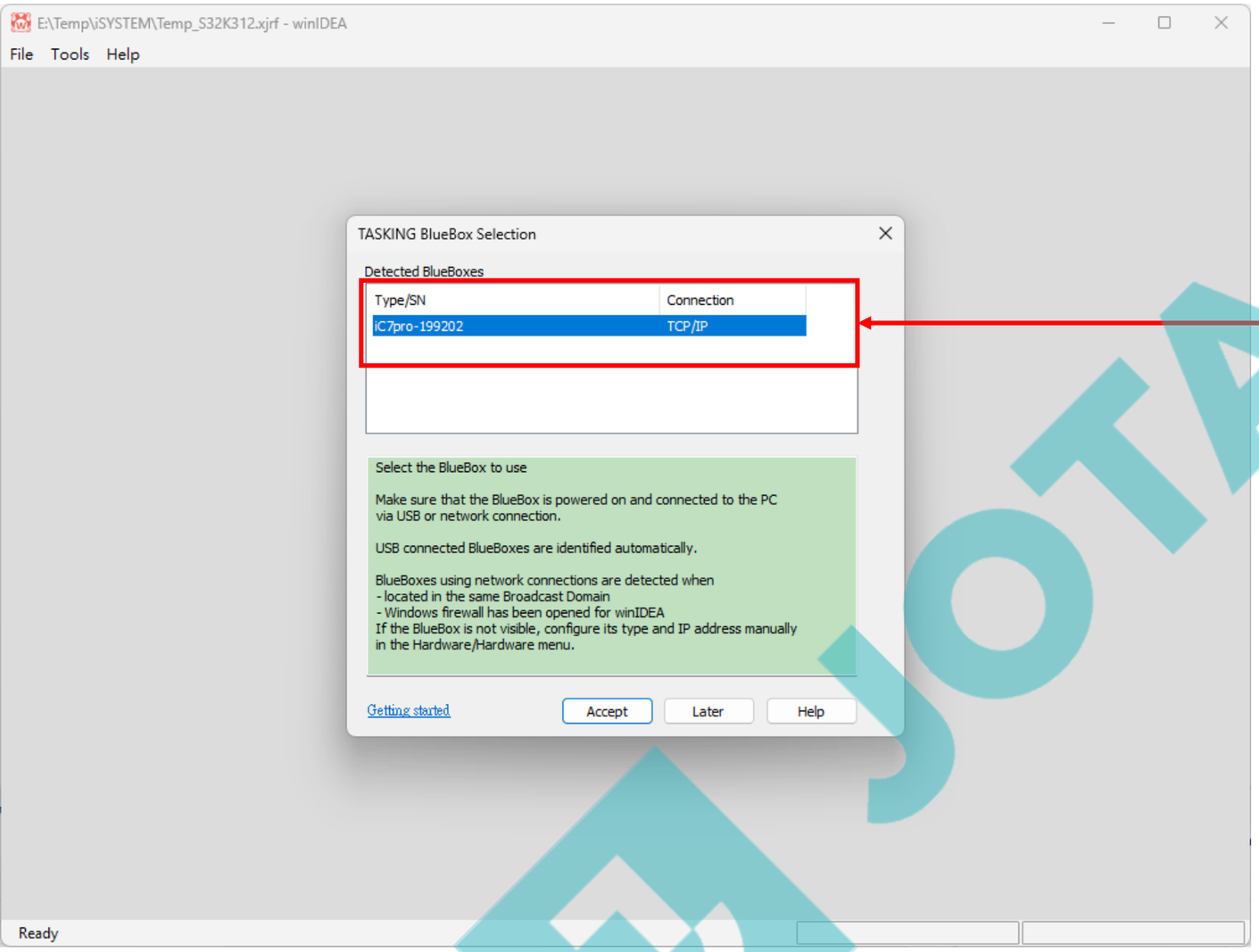


選擇啟動位址(請參考目標晶片手冊)

1. 根據晶片標準啟動流程來啟動
2. 根據燒入檔的指定的位址來啟動
3. 自訂以啟動位址

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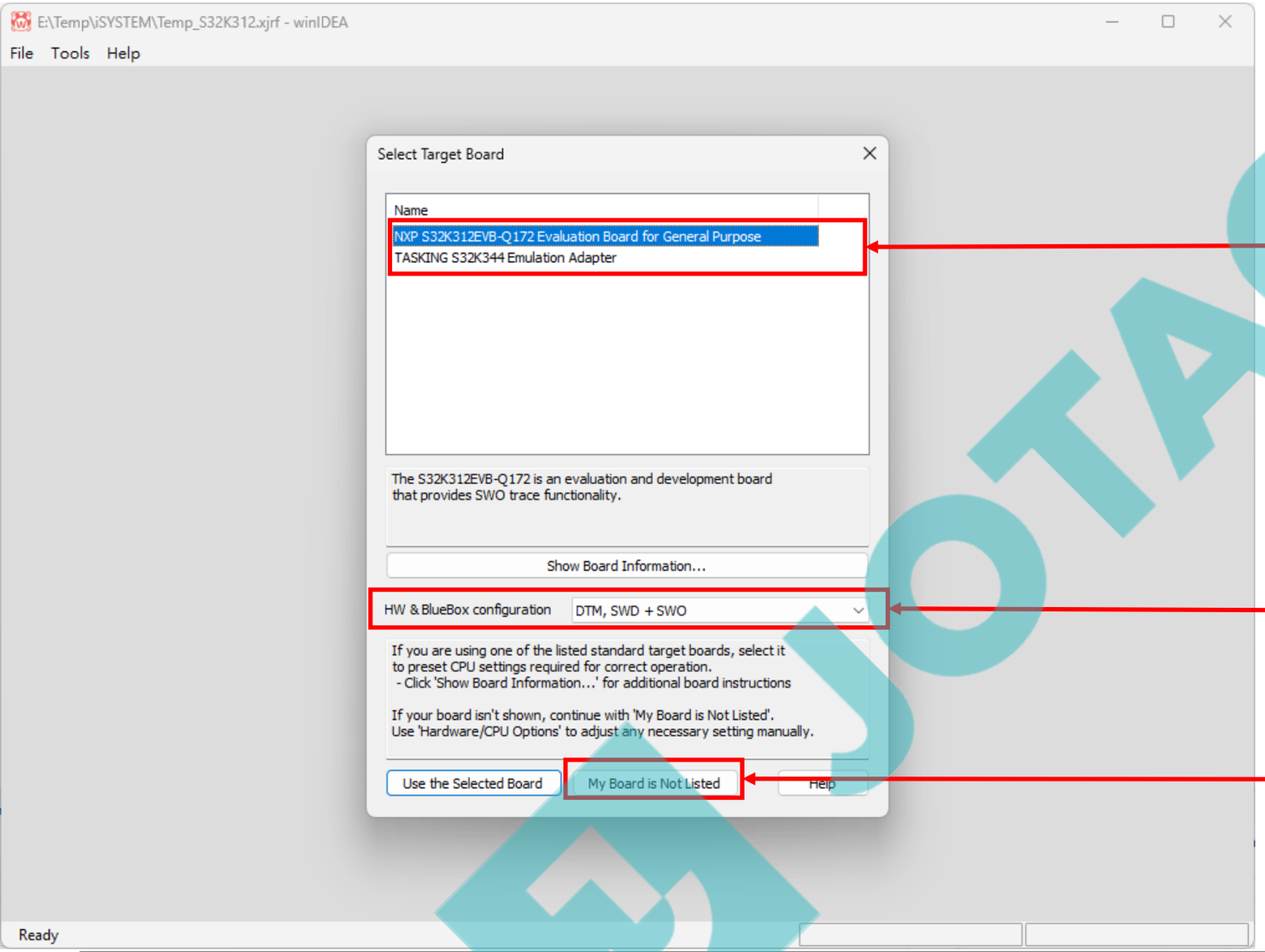
創建iSYSTEM專案



選擇連接的debug硬件設備。
建議前先將設備與電腦連接好，這邊才會正常顯示

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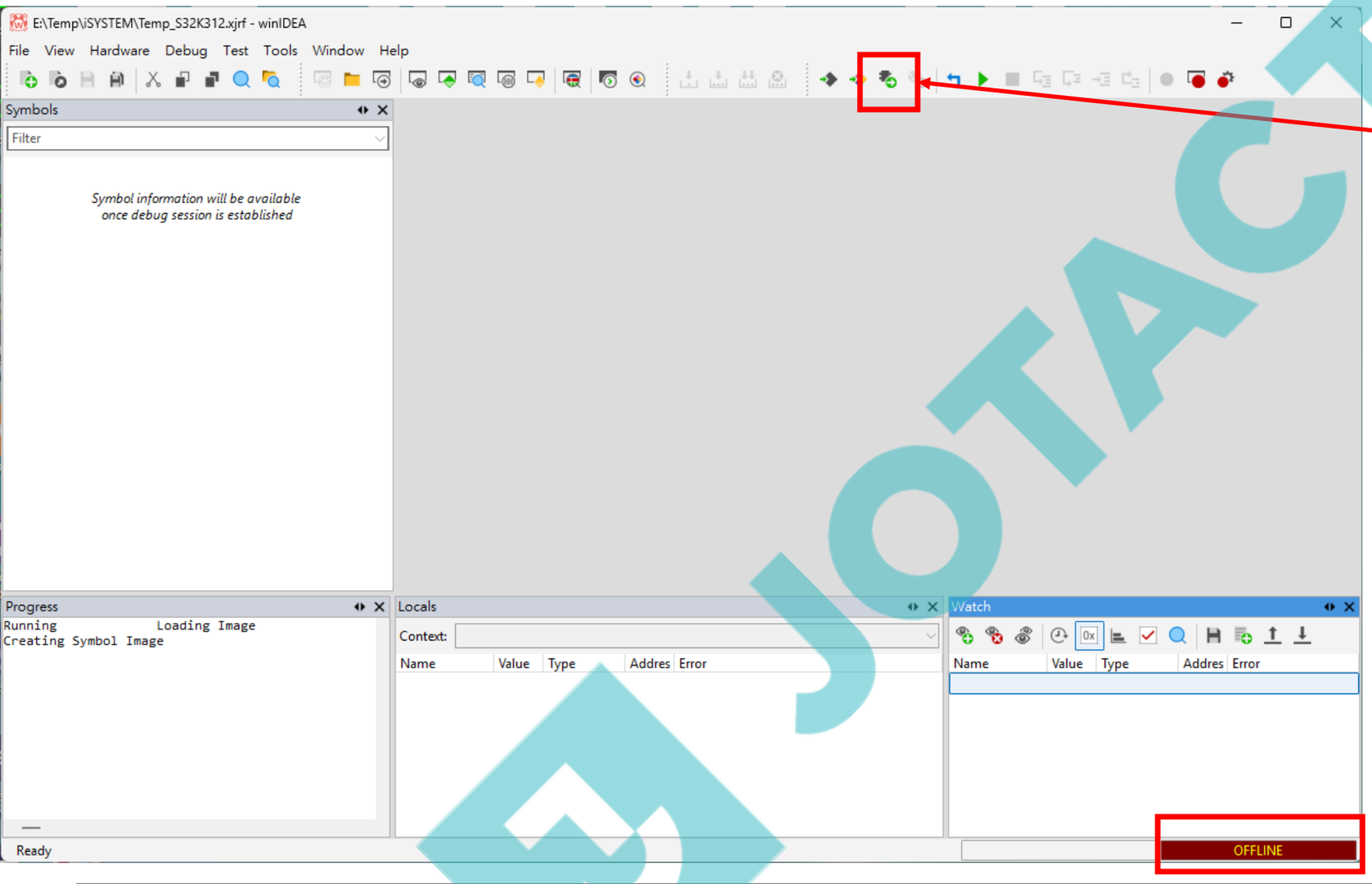
選擇目標板子，這裡大多會是晶片廠的EVB板子。

不同的EVB提供debug介面會有所不同，
iSYSTEM已經預先將相關的debug介面參數設定完成

如果自己的ECU板子不在清單當中，可選此項目。
自行設定debug介面相關參數。
(建議可直接選用EVB的配置，後續在微調即可)

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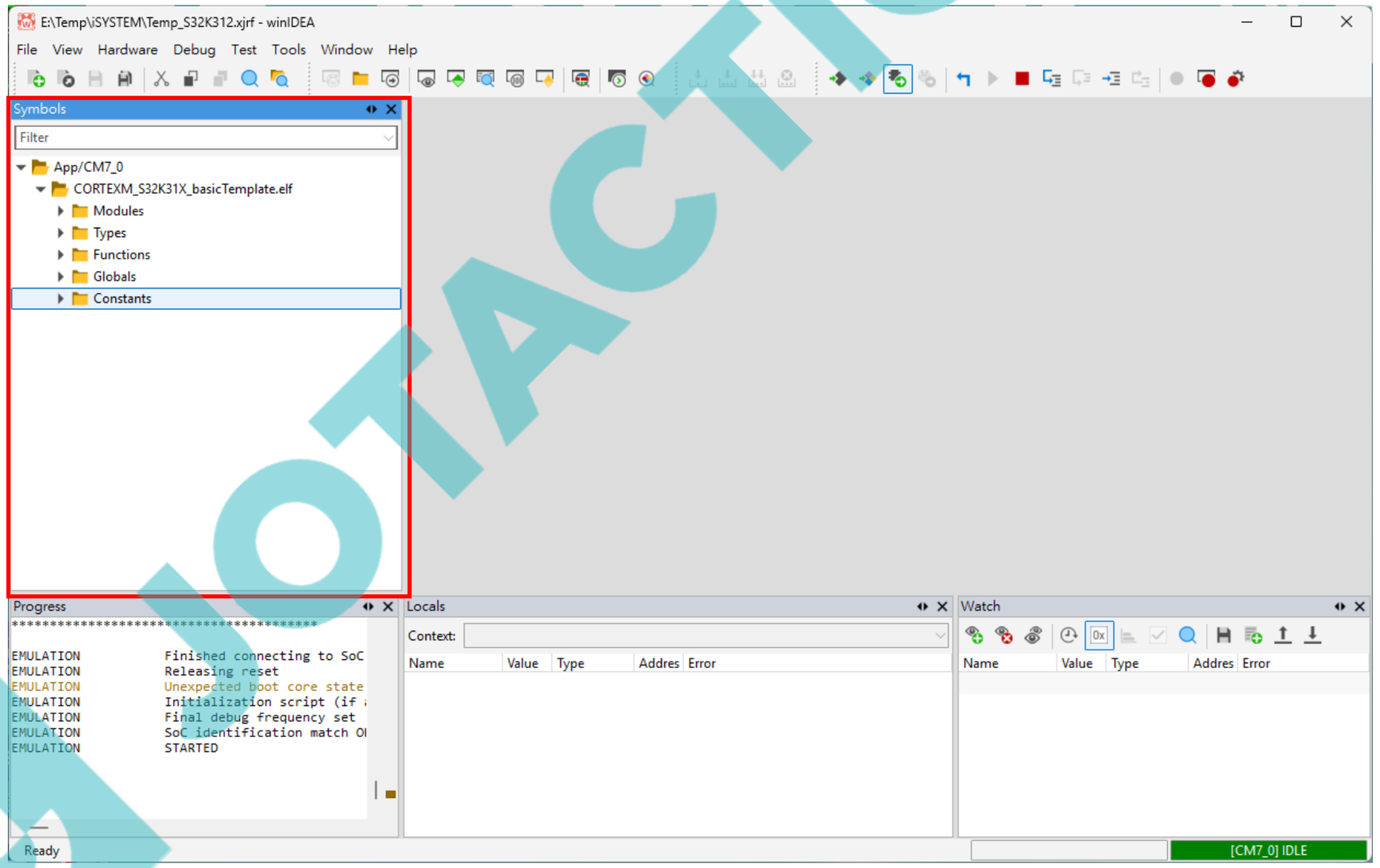


OFFLINE的情況下，點選Attach來連接晶片

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如果載入的燒入檔案包含Symbol資訊時，會在視窗顯示也可以在這裡搜尋



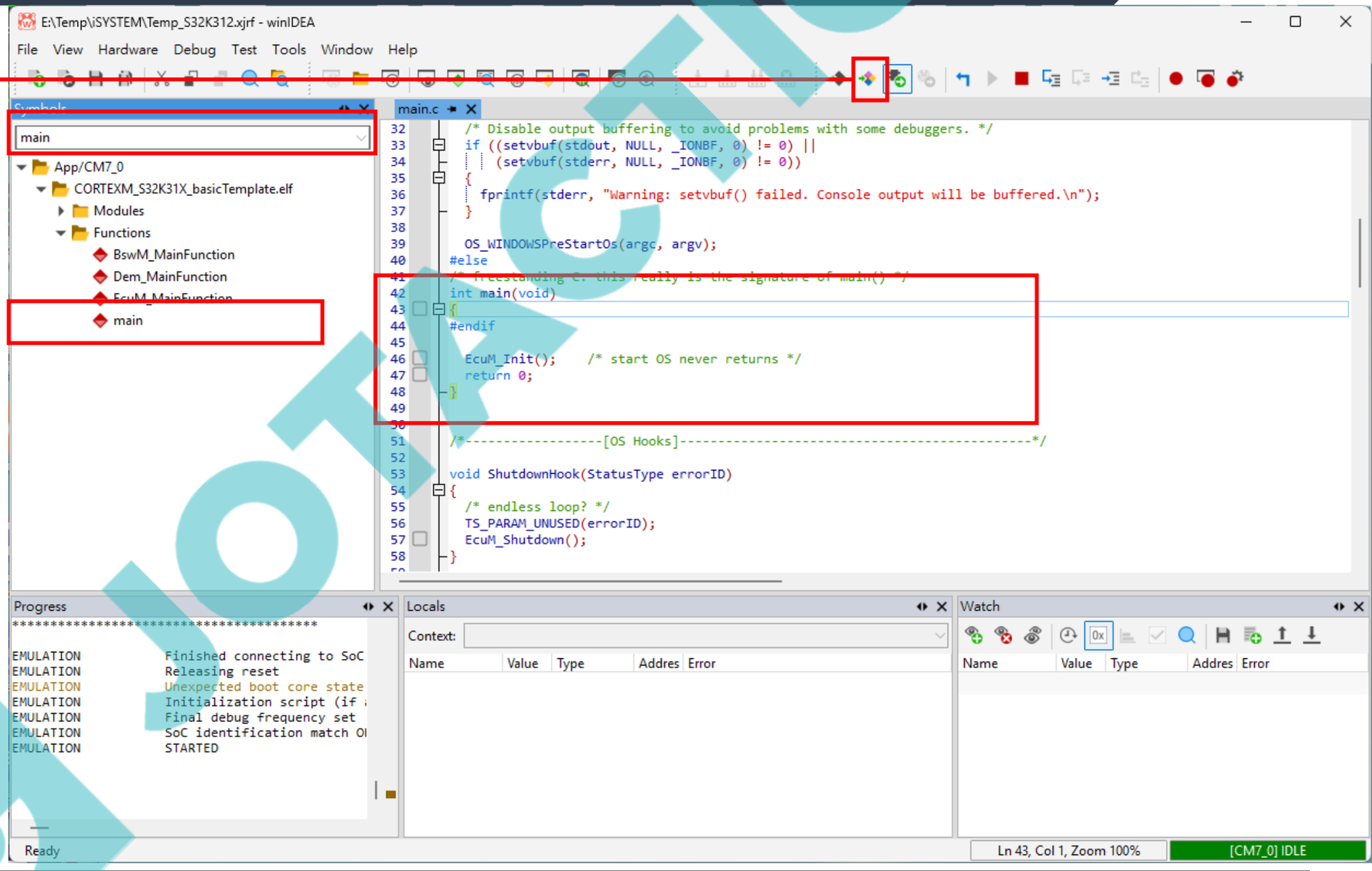
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創建iSYSTEM專案

先執行Load Symbol

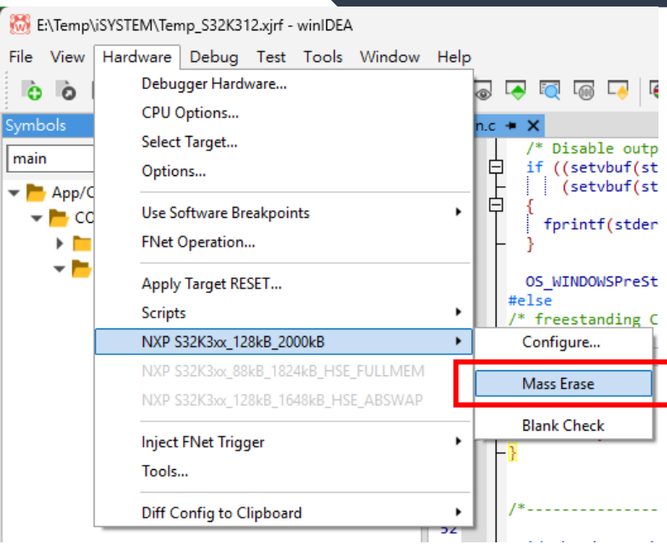
在Symbol搜尋想要查看的函式，
在目標函式上面雙擊之後，winIDEA就會在入此函式。
如未顯示可能Symbol與代碼之間的link不一致導致無法顯示(燒入檔案不再同一台電腦上編譯)，或是代碼被優化導置顯示不完整。

※ winIDEA載入的檔案要有包含Symbol，否則這裡不會有資訊



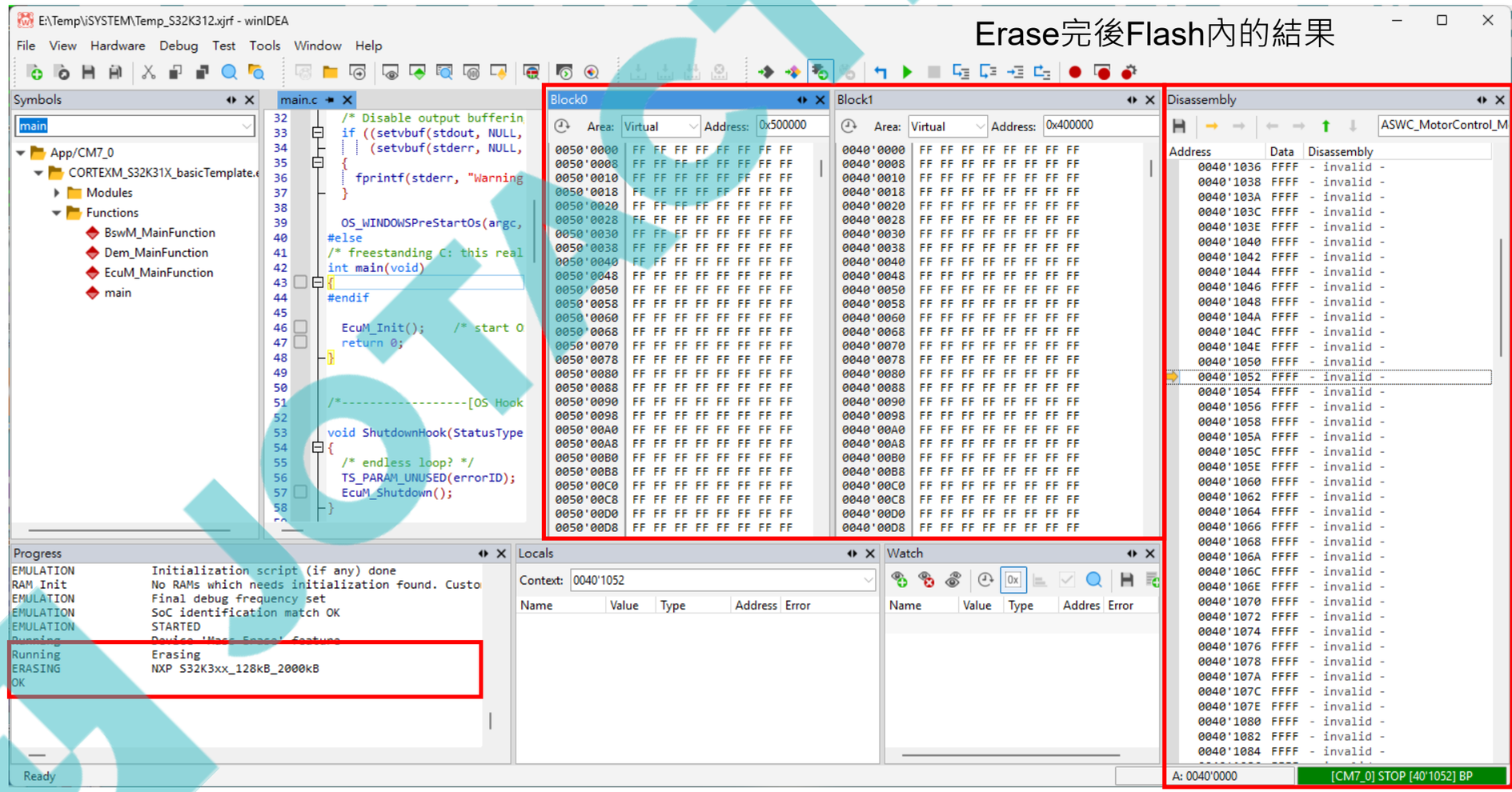
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創建iSYSTEM專案



※燒入代碼前建議先Erase晶片。
不同晶片Erase區塊會有所不同，
詳細請根據所選的晶片執行操作

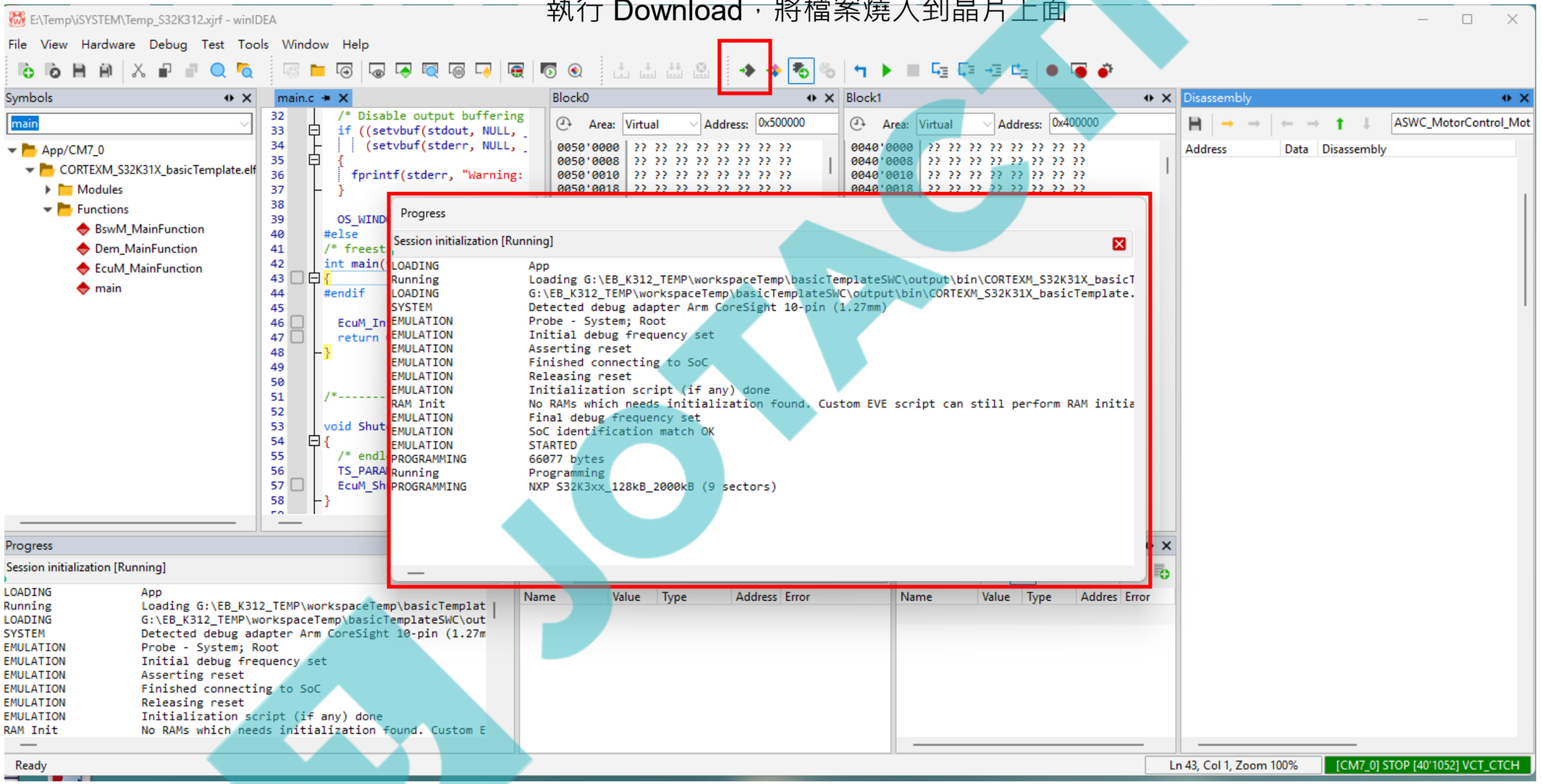
Erase完後Flash內的結果



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創建iSYSTEM專案

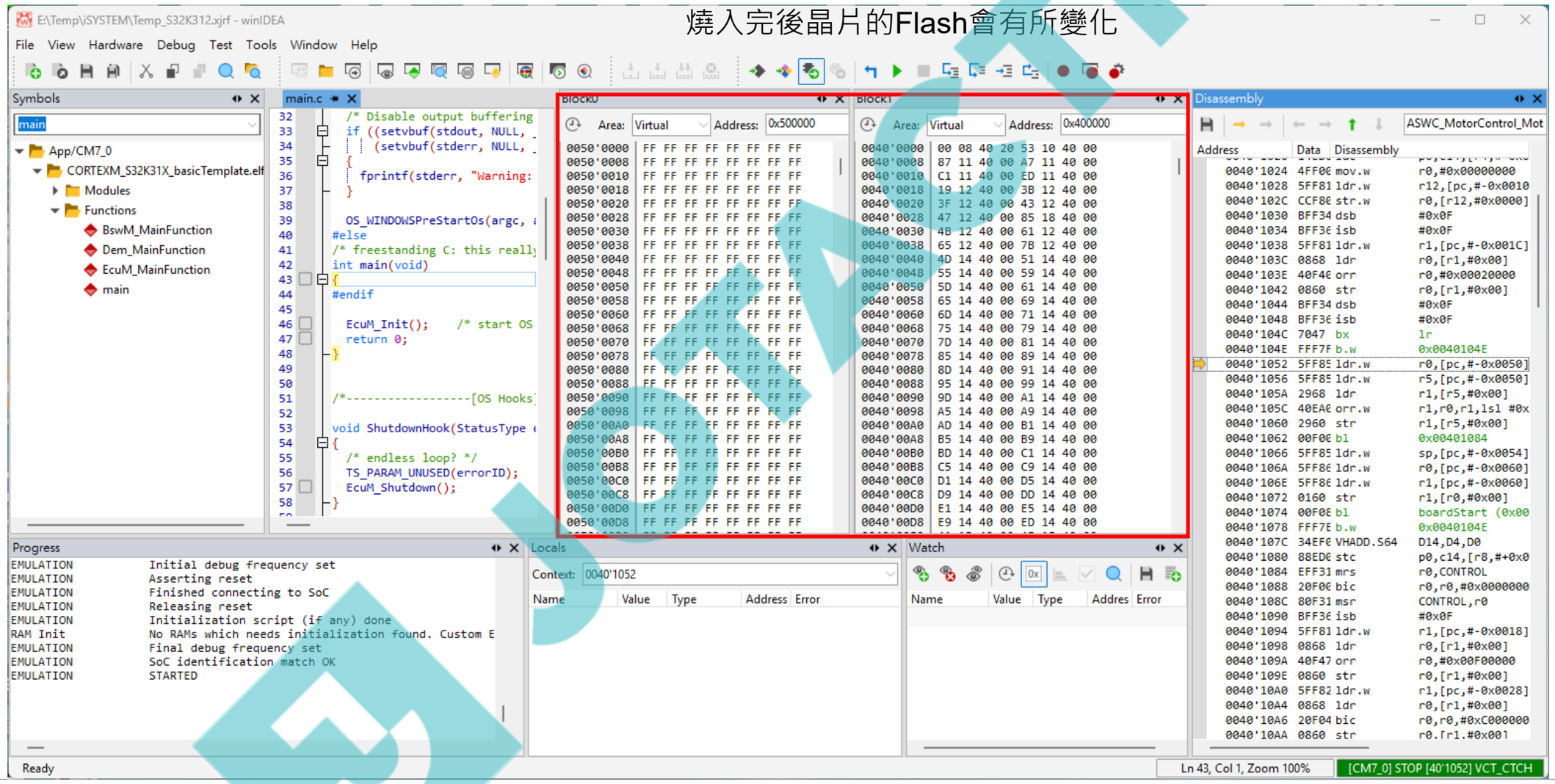
執行 Download，將檔案燒入到晶片上面



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創建iSYSTEM專案

燒入完後晶片的Flash會有所變化



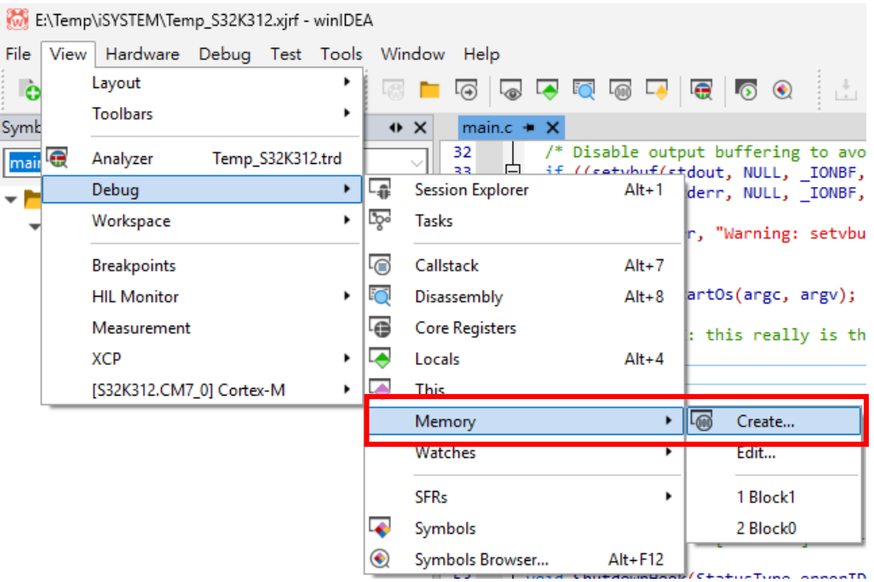
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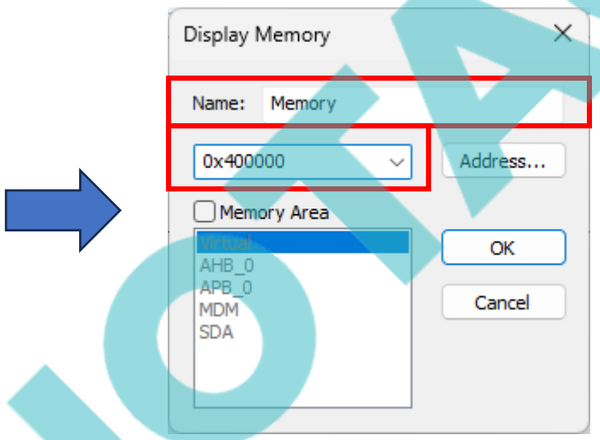
- 創建 iSYSTEM 專案
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- Debug 介面設定
- Architecture-specific plugins
- daqIDEA
- Tasks Window
- testIDEA

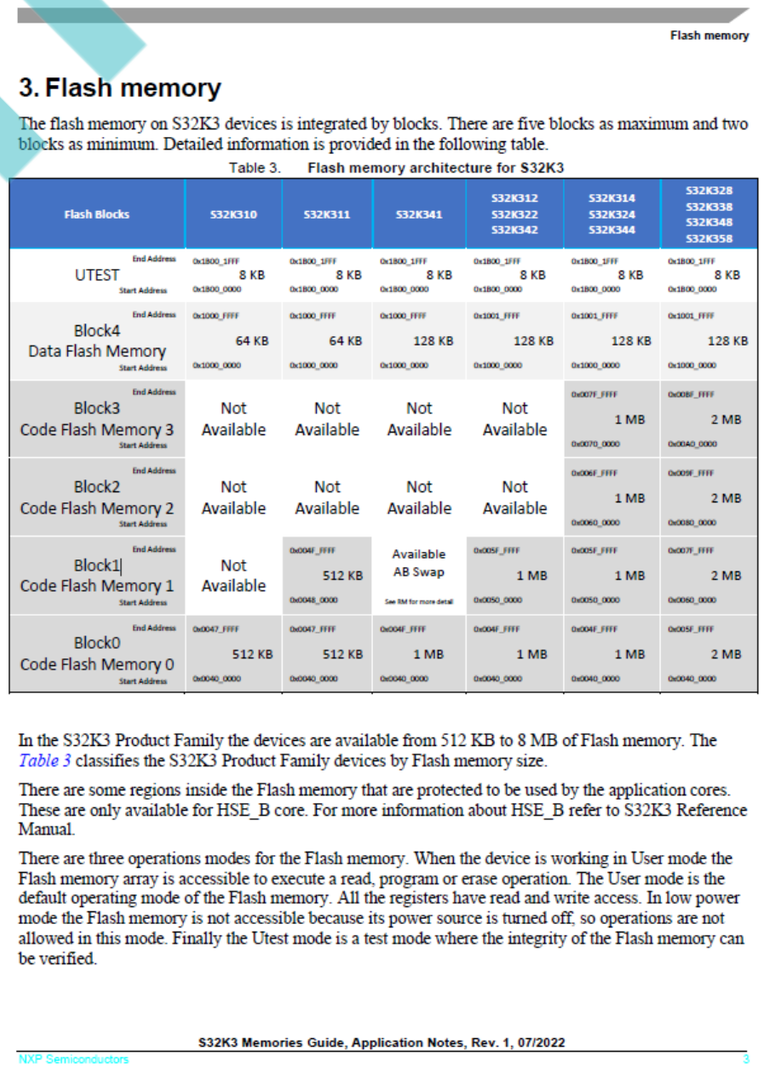
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Debug 功能介紹 – Memory



1. 自訂查看Memory視窗的名字
2. 輸入要觀察的地址(參考晶片手冊)





Flash memory

3. Flash memory

The flash memory on S32K3 devices is integrated by blocks. There are five blocks as maximum and two blocks as minimum. Detailed information is provided in the following table.

Table 3. Flash memory architecture for S32K3

Flash Blocks	S32K310	S32K311	S32K341	S32K312 S32K322 S32K342	S32K314 S32K324 S32K344	S32K328 S32K338 S32K348 S32K358
UTEST	8 KB	8 KB	8 KB	8 KB	8 KB	8 KB
Block4 Data Flash Memory	64 KB	64 KB	128 KB	128 KB	128 KB	128 KB
Block3 Code Flash Memory 3	Not Available	Not Available	Not Available	Not Available	1 MB	2 MB
Block2 Code Flash Memory 2	Not Available	Not Available	Not Available	Not Available	1 MB	2 MB
Block1 Code Flash Memory 1	Not Available	512 KB	Available AB Swap	1 MB	1 MB	2 MB
Block0 Code Flash Memory 0	512 KB	512 KB	1 MB	1 MB	1 MB	2 MB

In the S32K3 Product Family the devices are available from 512 KB to 8 MB of Flash memory. The *Table 3* classifies the S32K3 Product Family devices by Flash memory size.

There are some regions inside the Flash memory that are protected to be used by the application cores. These are only available for HSE_B core. For more information about HSE_B refer to S32K3 Reference Manual.

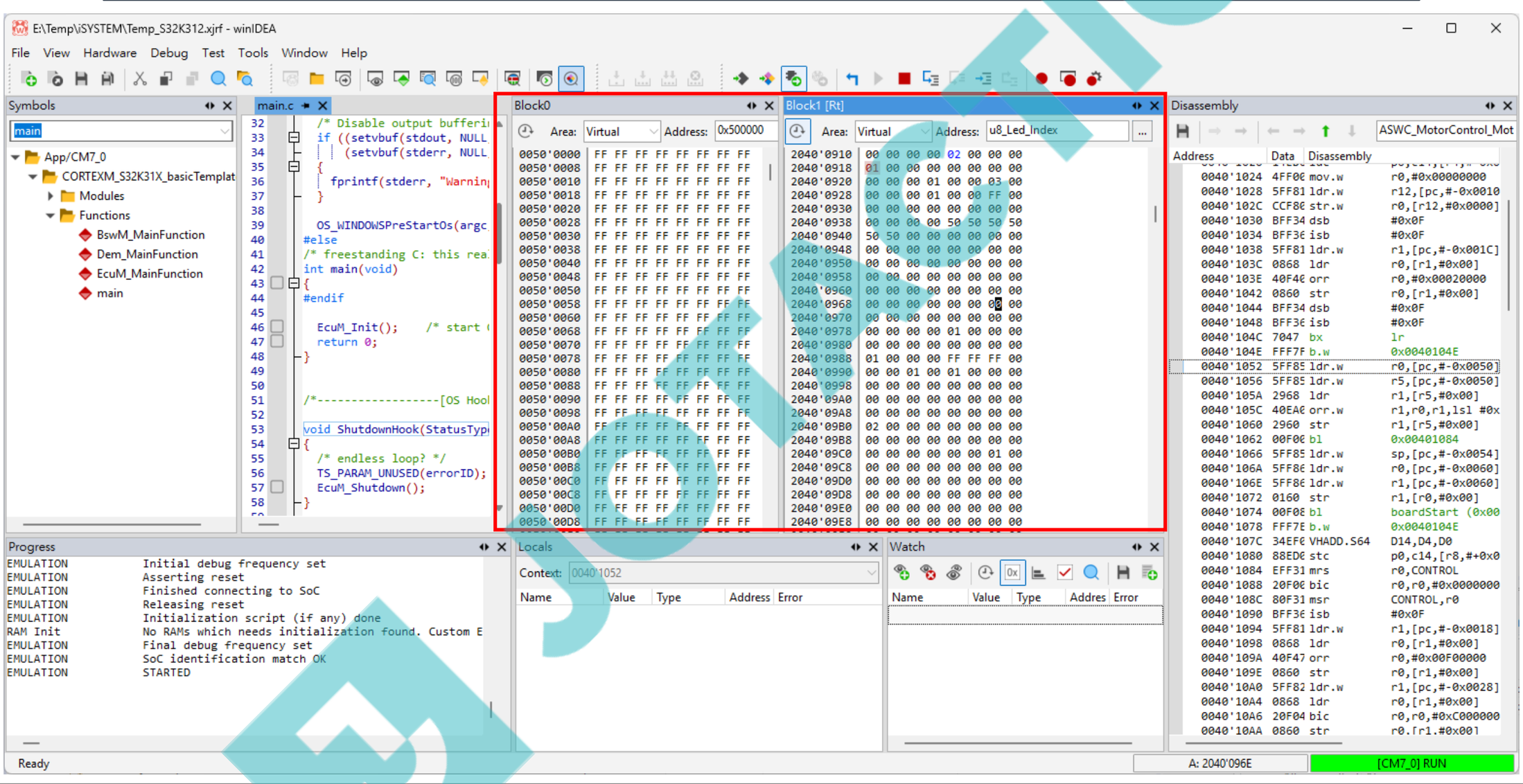
There are three operations modes for the Flash memory. When the device is working in User mode the Flash memory array is accessible to execute a read, program or erase operation. The User mode is the default operating mode of the Flash memory. All the registers have read and write access. In low power mode the Flash memory is not accessible because its power source is turned off, so operations are not allowed in this mode. Finally the Utest mode is a test mode where the integrity of the Flash memory can be verified.

S32K3 Memories Guide, Application Notes, Rev. 1, 07/2022

NXP Semiconductors 3

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Debug 功能介紹 – Memory



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Debug 功能介紹 – Memory

```
EcuASWC_DataAcquisition.c 3
source > application > C EcuASWC_DataAcquisition.c > EcuASWC_DataAcquisition_DataAcquisition_Runnable(void)
32  #include <Rte_EcuASWC_DataAcquisition.h>
34  #include "Dio.h"
35  static uint8 u8_Led_0 = STD_HIGH;
36  static uint8 u8_Led_1 = STD_HIGH;
37  static uint8 u8_Led_2 = STD_HIGH;
38  static uint8 u8_Led_3 = STD_LOW;
39  static uint32 u32_DetCunt = 0;
40  static uint8 u8_Led_Index = 0;
```

可以在Memory視窗當中尋找要查變數/函式，找到後會自動反藍

Block1

Area: Virtual Address: u8_Led_Index

2040'0910	00	00	00	00	02	0		
2040'0918	01	00	00	00	00	00	00	00
2040'0920	00	00	00	01	00	00	03	00
2040'0928	00	00	00	01	00	00	FF	00
2040'0930	00	00	00	00	00	00	00	00
2040'0938	00	00	00	00	50	50	50	50
2040'0940	50	50	00	00	00	00	00	00
2040'0948	00	00	00	00	00	00	00	00
2040'0950	00	00	00	00	00	00	00	00
2040'0958	00	00	00	00	00	00	00	00
2040'0960	00	00	00	00	00	00	00	00
2040'0968	00	00	00	00	00	00	00	00
2040'0970	00	00	00	00	00	00	00	00
2040'0978	00	00	00	00	01	00	00	00
2040'0980	00	00	00	00	00	00	00	00
2040'0988	01	00	00	00	FF	FF	FF	00
2040'0990	00	00	01	00	01	00	00	00
2040'0998	00	00	00	00	00	00	00	00
2040'09A0	00	00	00	00	00	00	00	00
2040'09A8	00	00	00	00	00	00	00	00
2040'09B0	02	00	00	00	00	00	00	00
2040'09B8	00	00	00	00	00	00	00	00
2040'09C0	00	00	00	00	00	00	01	00
2040'09C8	00	00	00	00	00	00	00	00
2040'09D0	00	00	00	00	00	00	00	00
2040'09D8	00	00	00	00	00	00	00	00
2040'09E0	00	00	00	00	00	00	00	00
2040'09E8	00	00	00	00	00	00	00	00

點擊時鐘圖案，可在Memory視窗中動態看到數值的變動

Block1 [Rt]

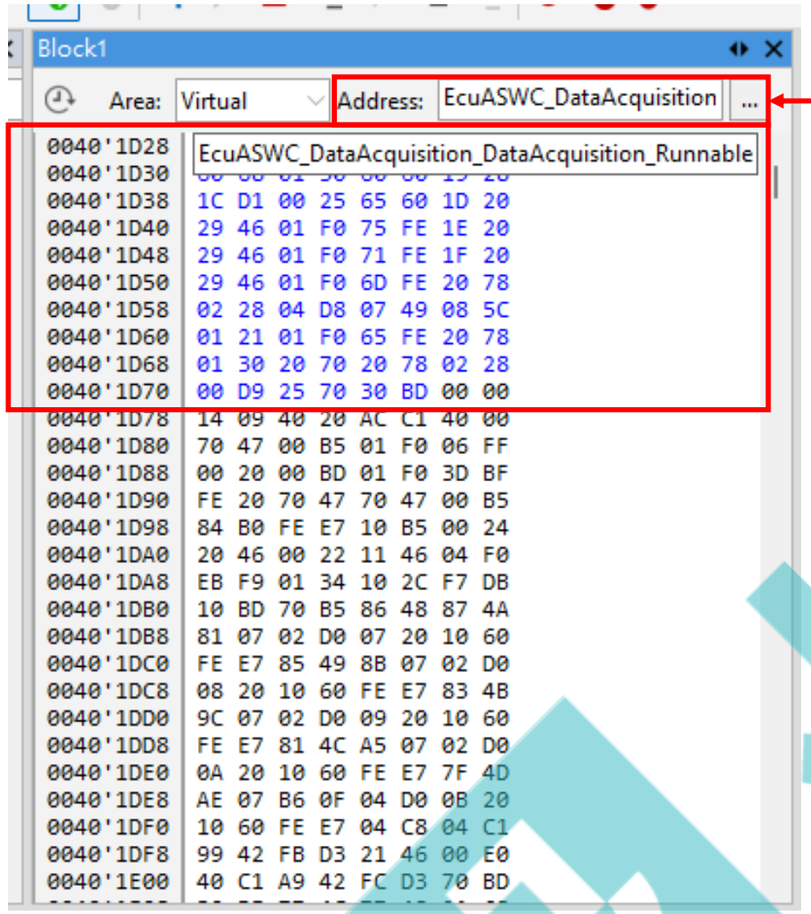
Area: Virtual Address: u8_Led_Index

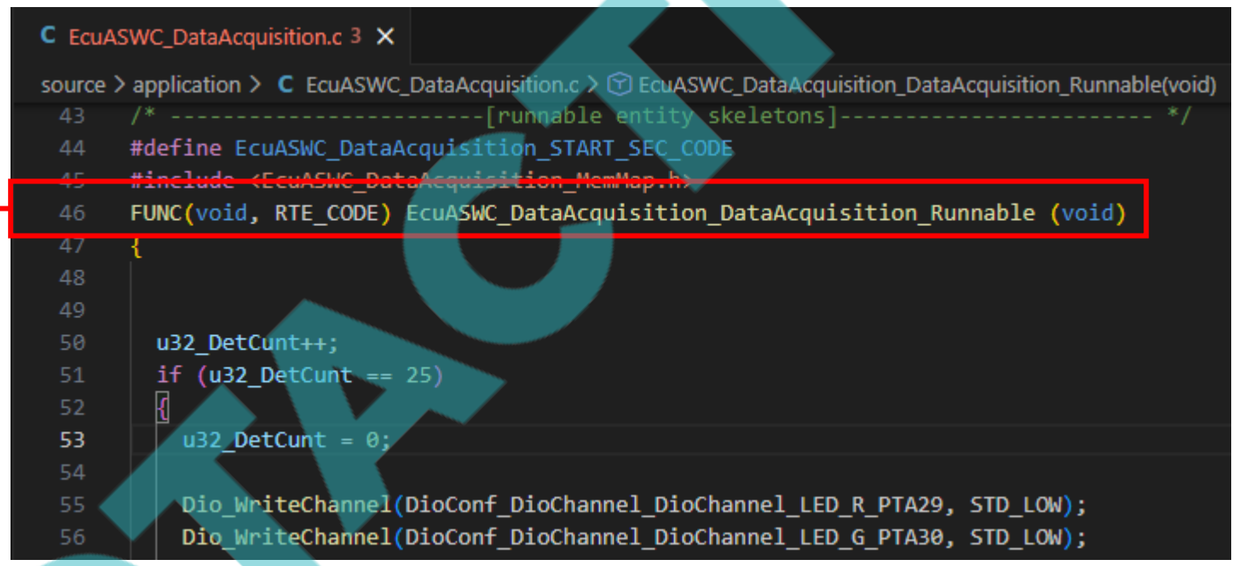
2040'0910	00	00	00	00	01	00	00	00
2040'0918	02	00	00	00	00	00	00	00
2040'0920	00	00	00	01	00	00	03	00
2040'0928	00	00	00	01	00	00	FF	00
2040'0930	00	00	00	00	00	00	00	00
2040'0938	00	00	00	00	50	50	50	50
2040'0940	50	50	00	00	00	00	00	00
2040'0948	00	00	00	00	00	00	00	00
2040'0950	00	00	00	00	00	00	00	00
2040'0958	00	00	00	00	00	00	00	00
2040'0960	00	00	00	00	00	00	00	00
2040'0968	00	00	00	00	00	00	00	00
2040'0970	00	00	00	00	00	00	00	00
2040'0978	00	00	00	00	01	00	00	00
2040'0980	00	00	00	00	00	00	00	00
2040'0988	01	00	00	00	FF	FF	FF	00
2040'0990	00	00	01	00	01	00	00	00
2040'0998	00	00	00	00	00	00	00	00
2040'09A0	00	00	00	00	00	00	00	00
2040'09A8	00	00	00	00	00	00	00	00
2040'09B0	02	00	00	00	00	00	00	00
2040'09B8	00	00	00	00	00	00	00	00
2040'09C0	00	00	00	00	00	00	01	00
2040'09C8	00	00	00	00	00	00	00	00
2040'09D0	00	00	00	00	00	00	00	00
2040'09D8	00	00	00	00	00	00	00	00
2040'09E0	00	00	00	00	00	00	00	00
2040'09E8	00	00	00	00	00	00	00	00

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Debug 功能介紹 – Memory

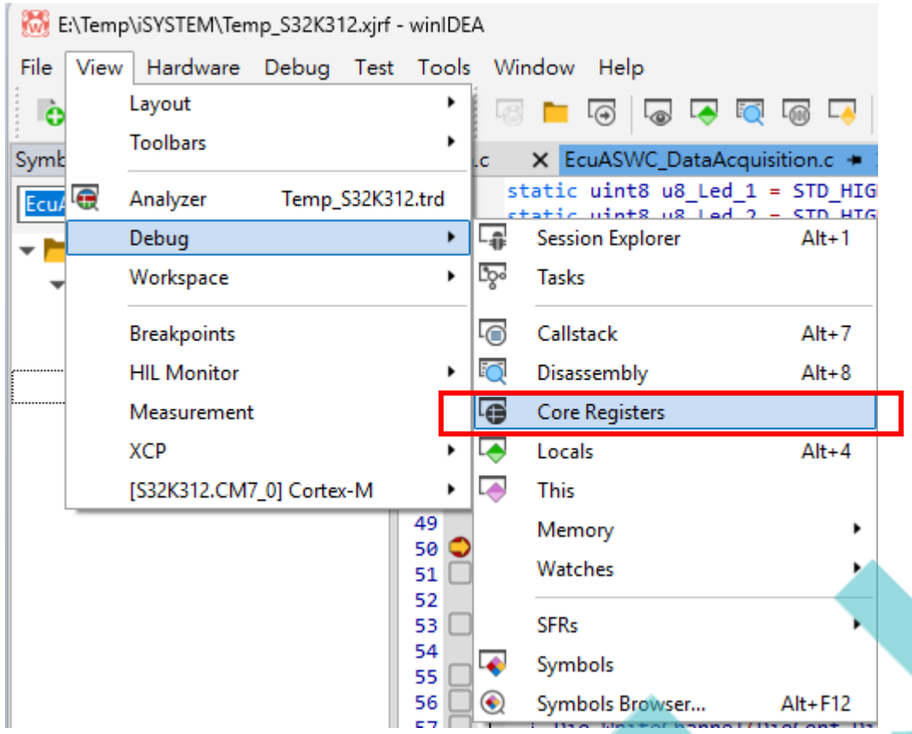
可以在Memory視窗當中尋找要查變數/函式，找到後會自動反藍

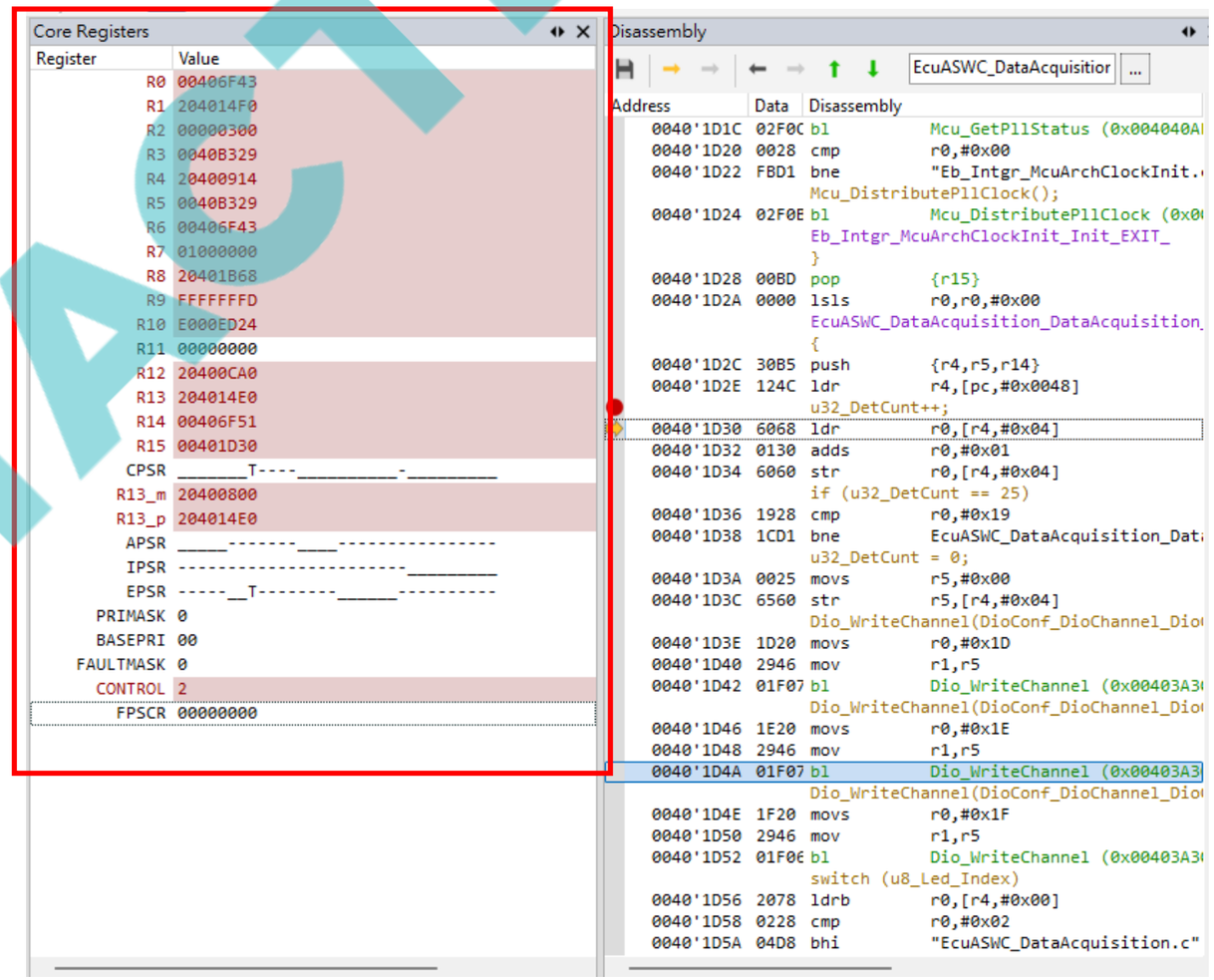




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Debug 功能介紹 – Core Registers

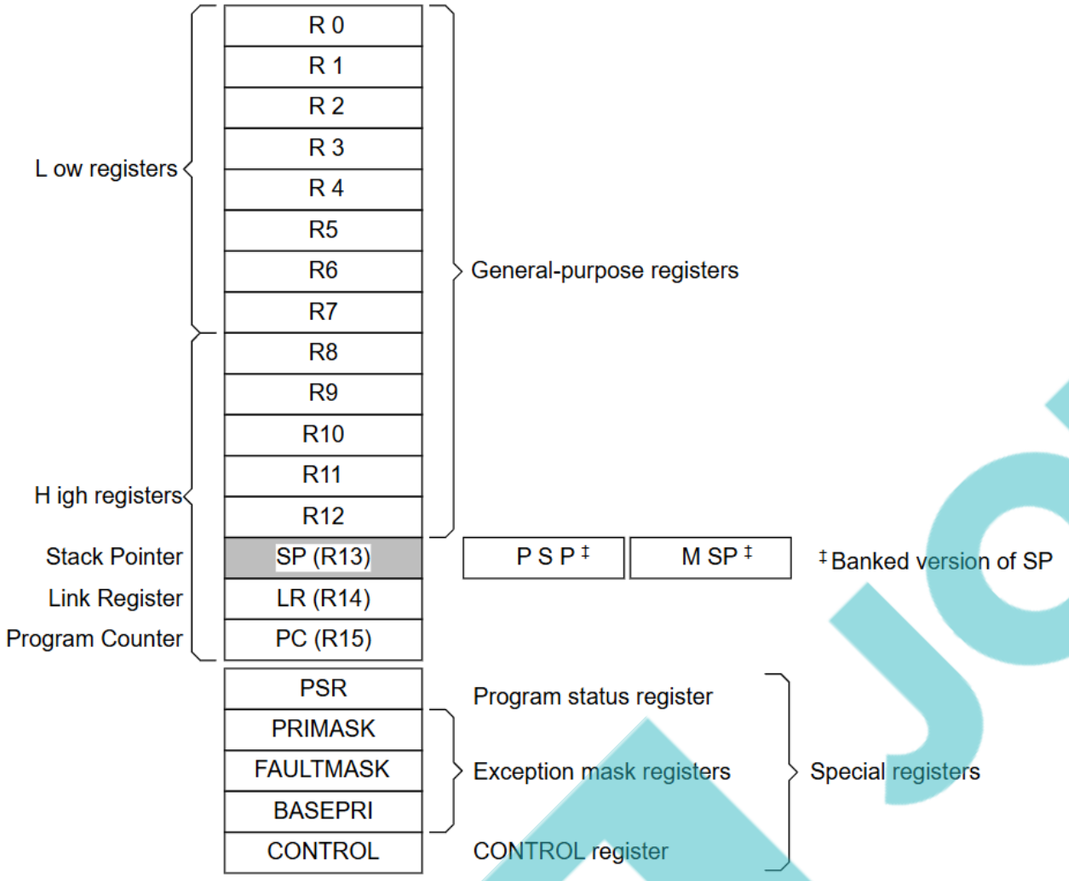


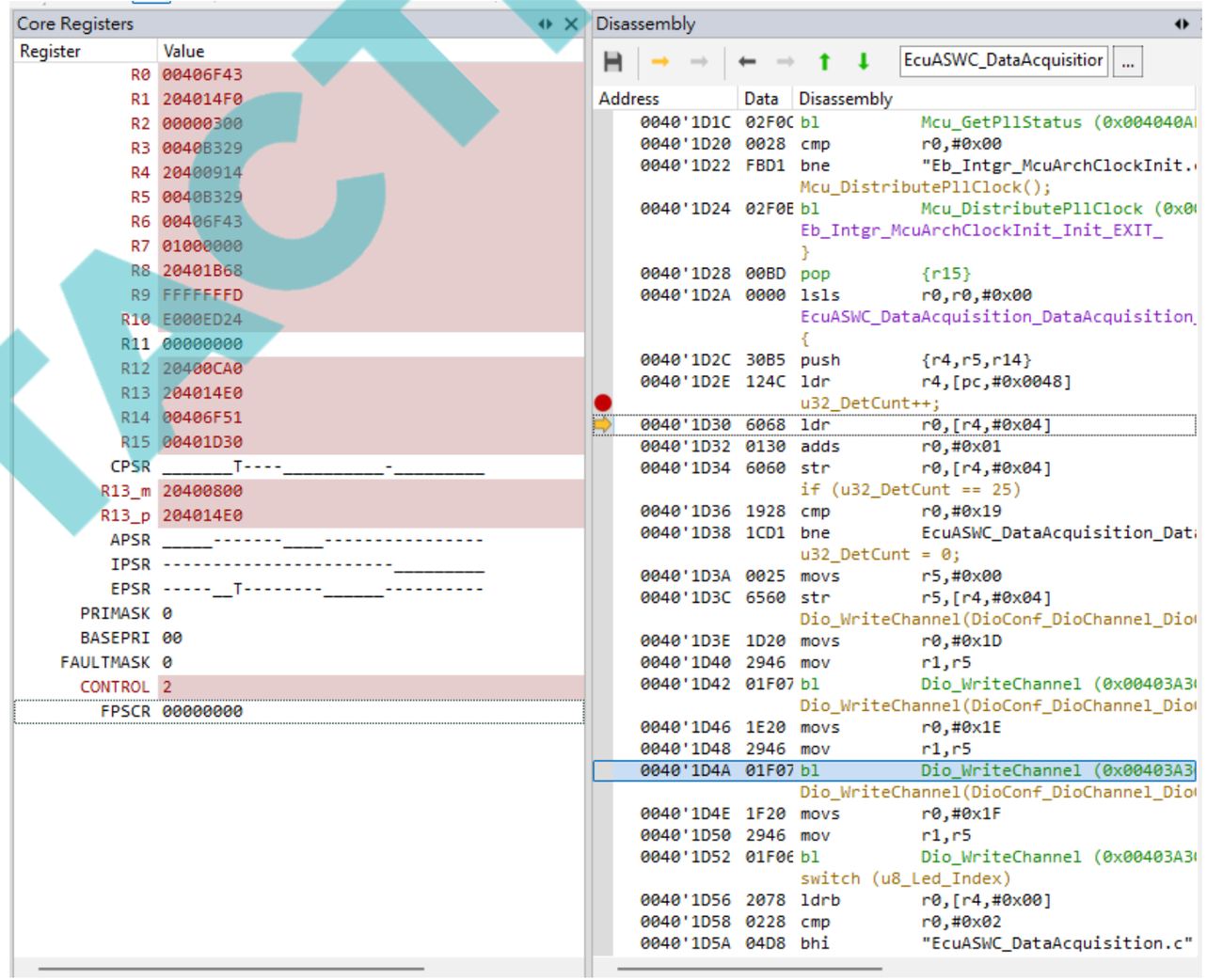


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Debug 功能介紹 – Core Registers

此處需要參考晶片手冊





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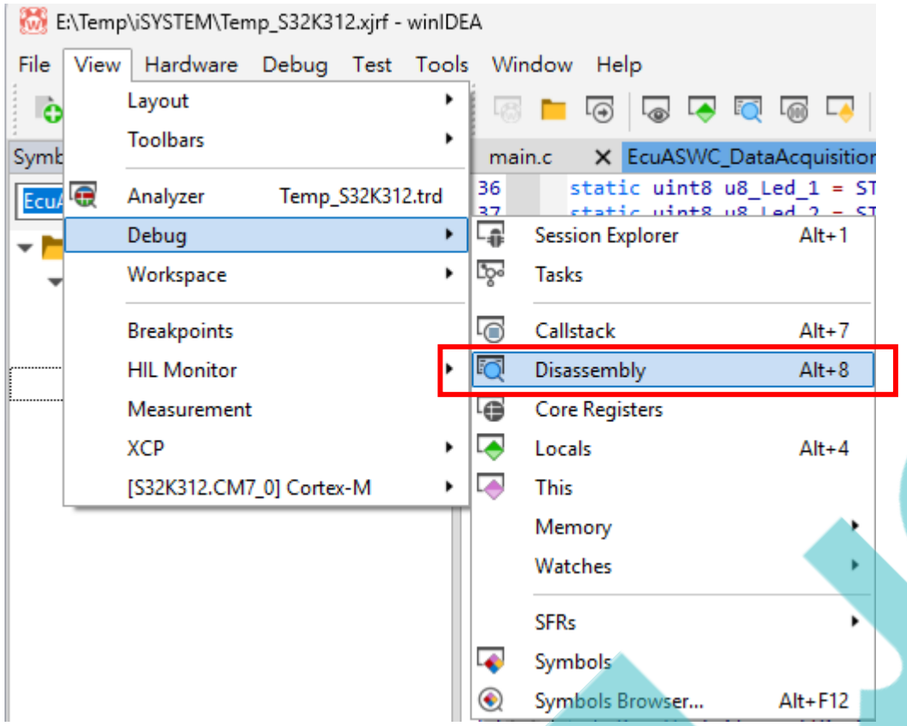
Debug 功能介紹 – Core Registers

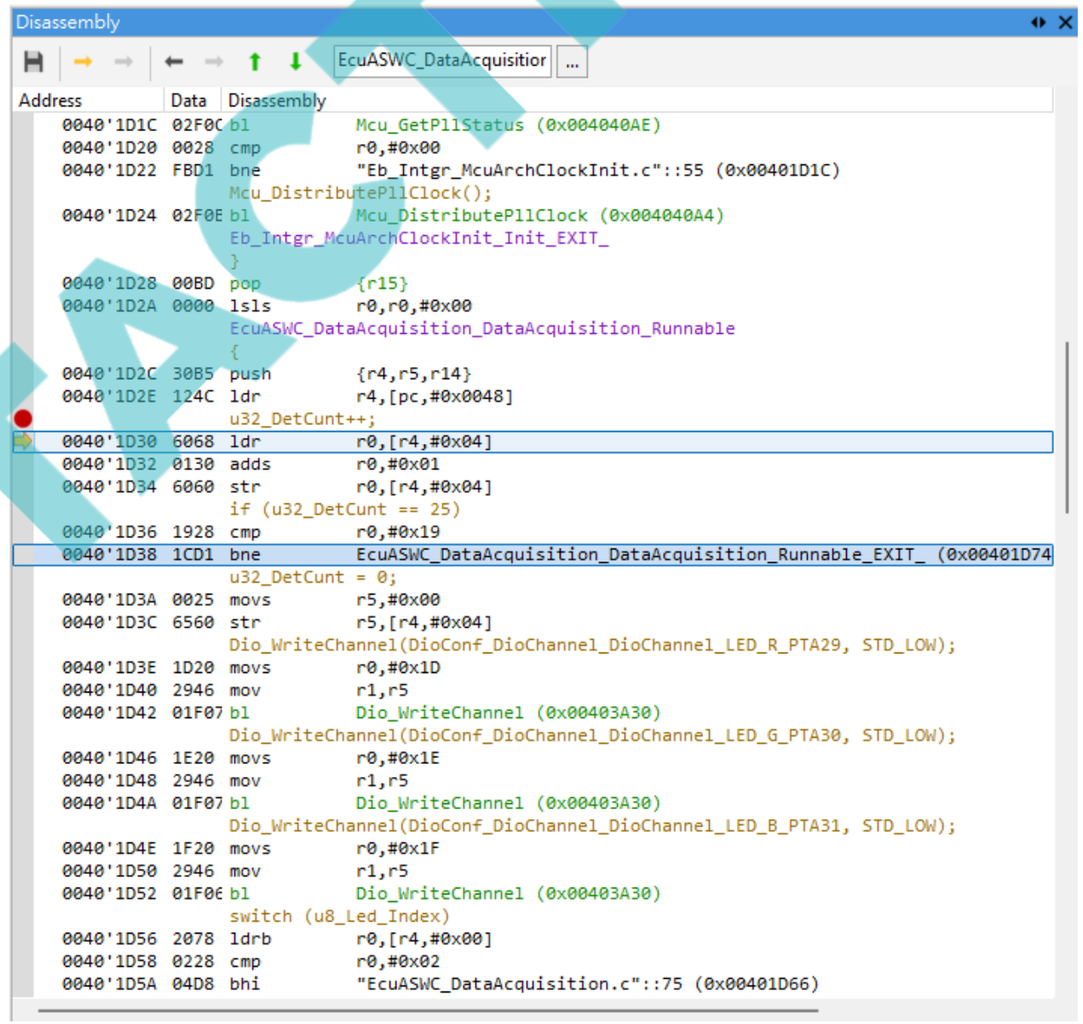
Table 2.2. Core register set summary

Name	Type [a]	Required privilege [b]	Reset value	Description
R0-R12	RW	Either	`Unknown`	*General-purpose registers*
MSP	RW	Either	See description	*Stack Pointer*
PSP	RW	Either	`Unknown`	*Stack Pointer*
LR	RW	Either	`0xFFFFFFFF`	*Link Register*
PC	RW	Either	See description	*Program Counter*
PSR	RW	Either	`0x01000000` [c]	*Program Status Register*
ASPR	RW	Either	`Unknown`	*Application Program Status Register*
IPSR	RO	Privileged	`0x00000000`	*Interrupt Program Status Register*
EPSR	RO	Privileged	`0x01000000` [c]	*Execution Program Status Register*
PRIMASK	RW	Privileged	`0x00000000`	*Priority Mask Register*
FAULTMASK	RW	Privileged	`0x00000000`	*Fault Mask Register*
BASEPRI	RW	Privileged	`0x00000000`	*Base Priority Mask Register*
CONTROL	RW	Privileged	`0x00000000`	*CONTROL register*

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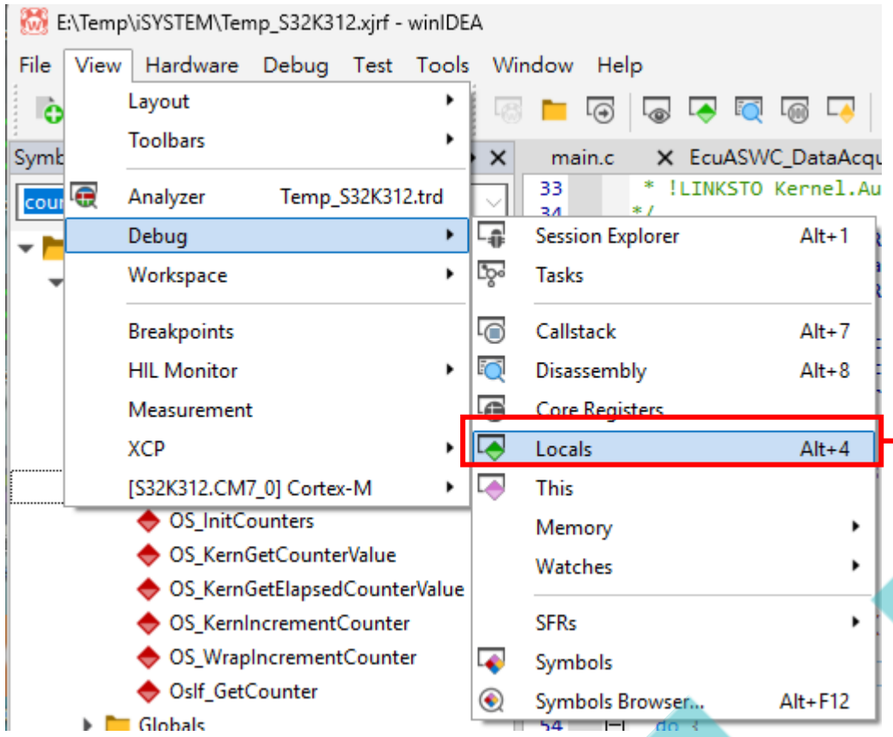
Debug 功能介紹 – Disassembly

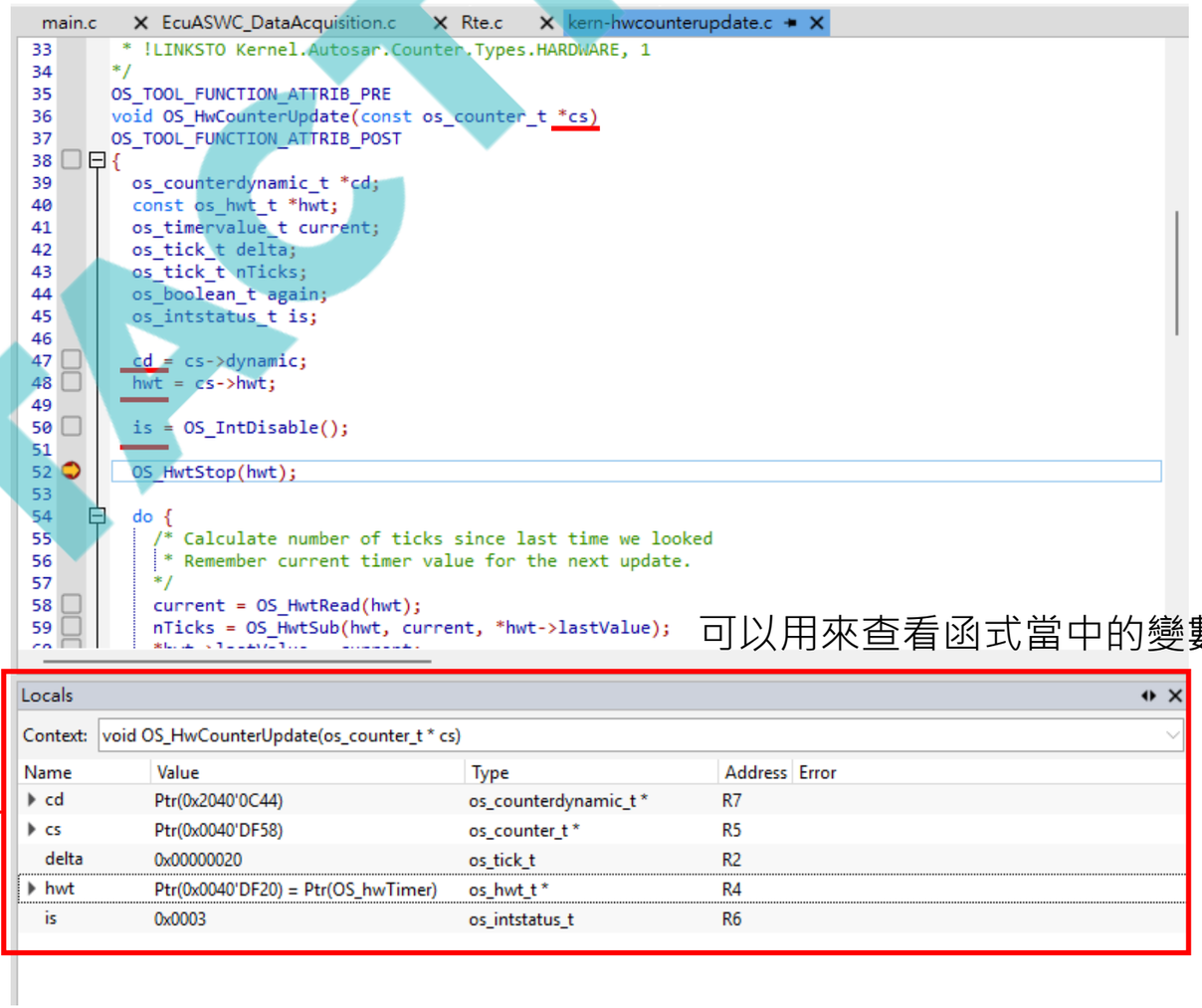




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Debug 功能介紹 – Locals

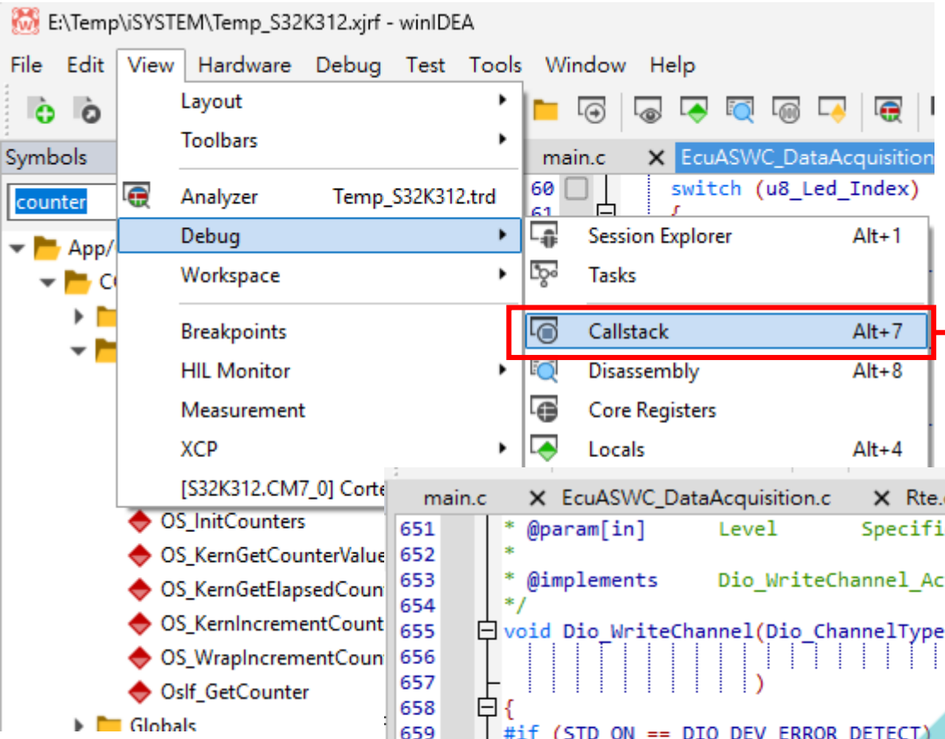




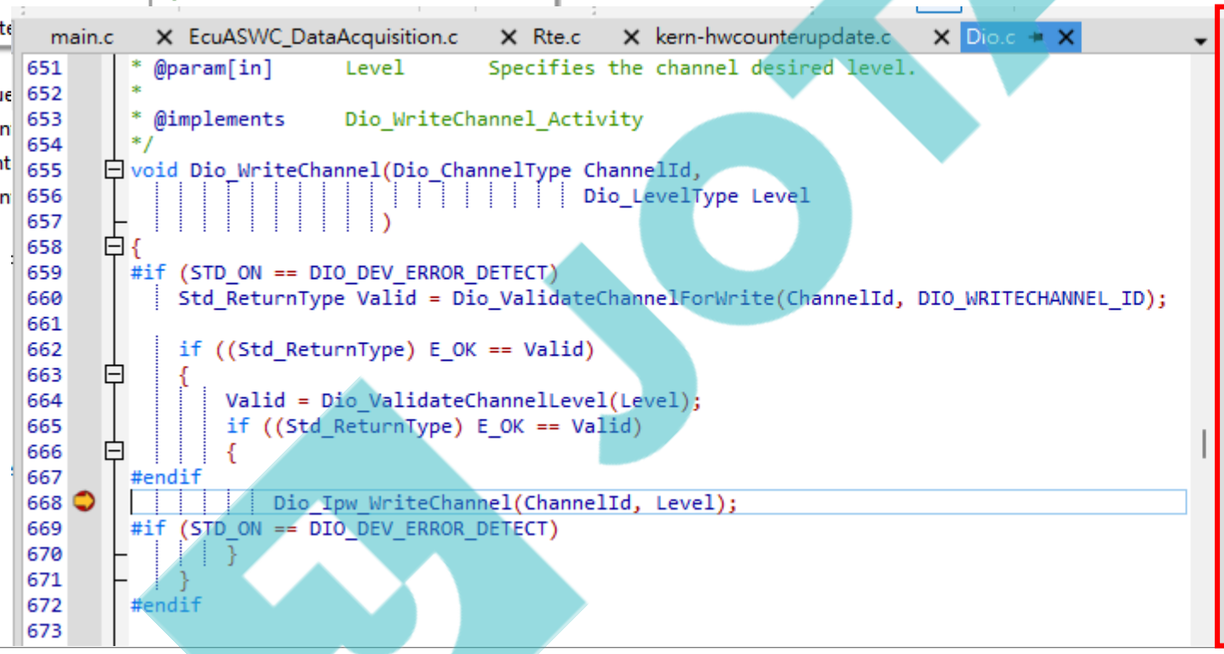
可以用來查看函式當中的變數資訊

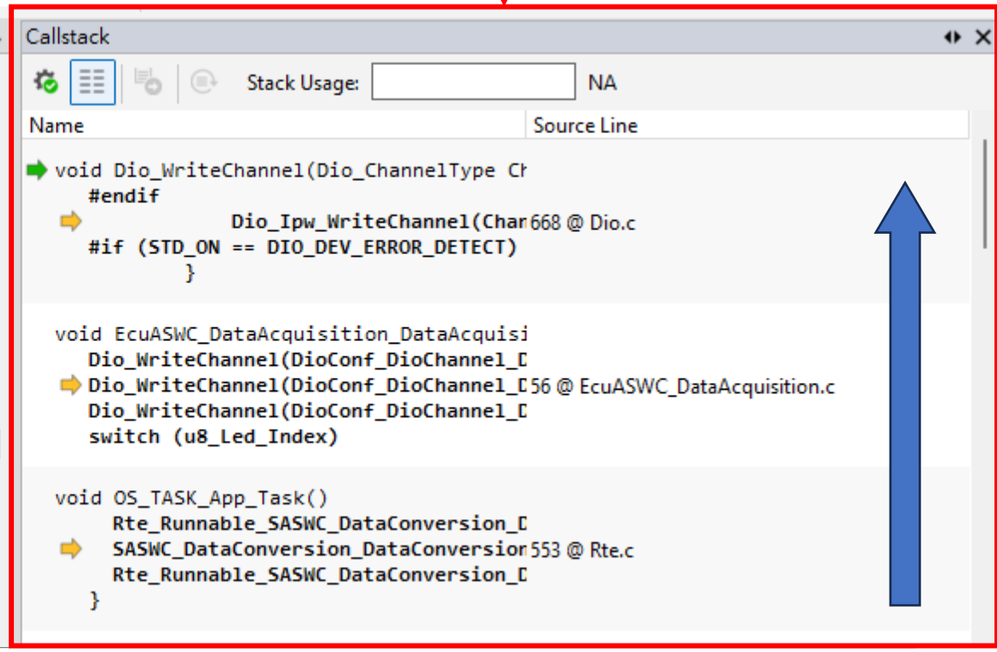
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Debug 功能介紹 – Callstack



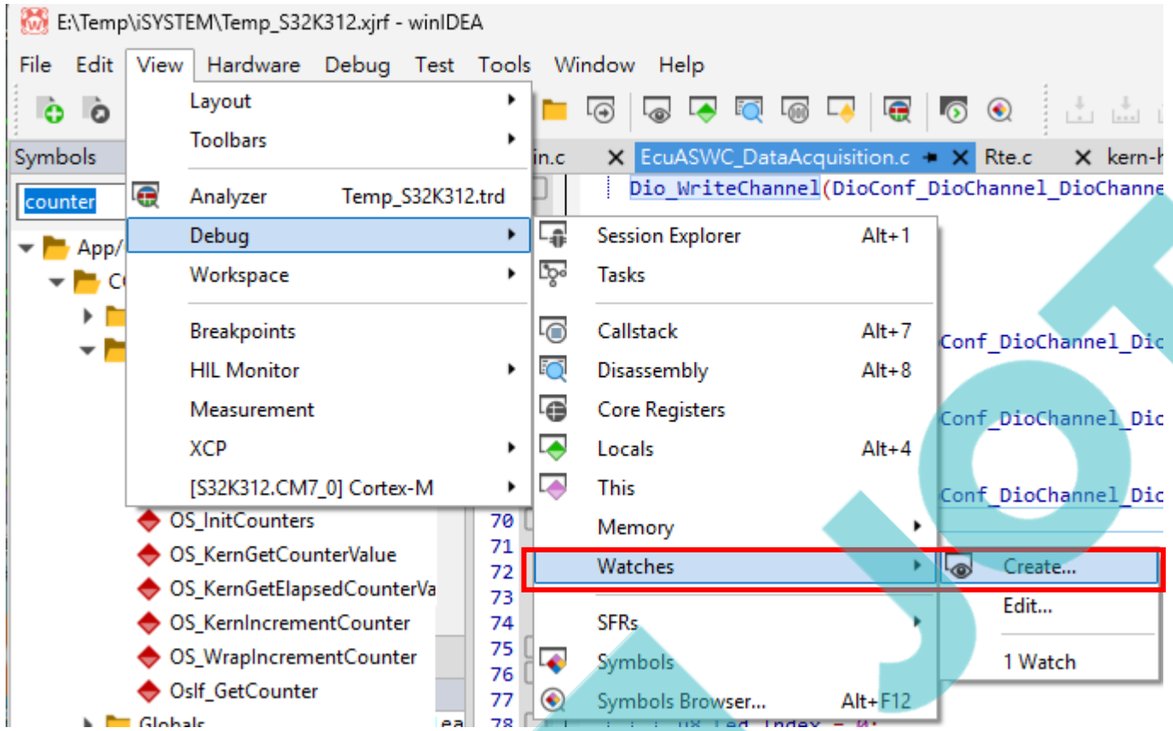
當系統執行到斷點時，Callstack視窗會顯示系統過往跑過的函式



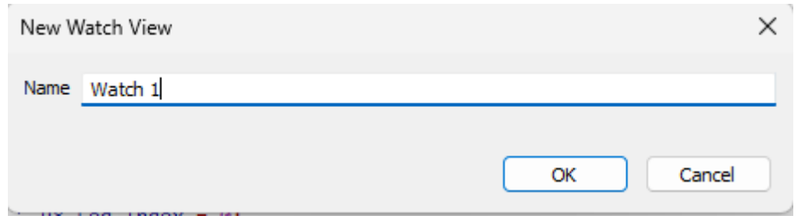


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Debug 功能介紹 – Watches

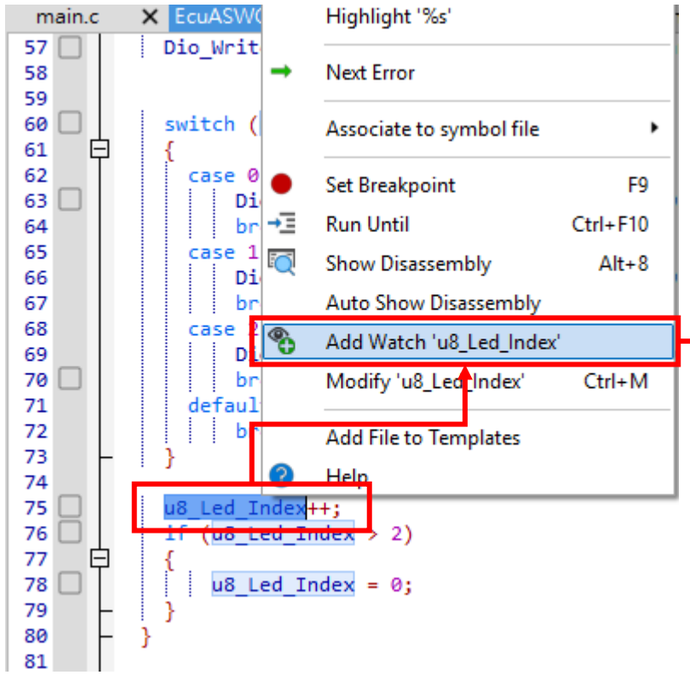


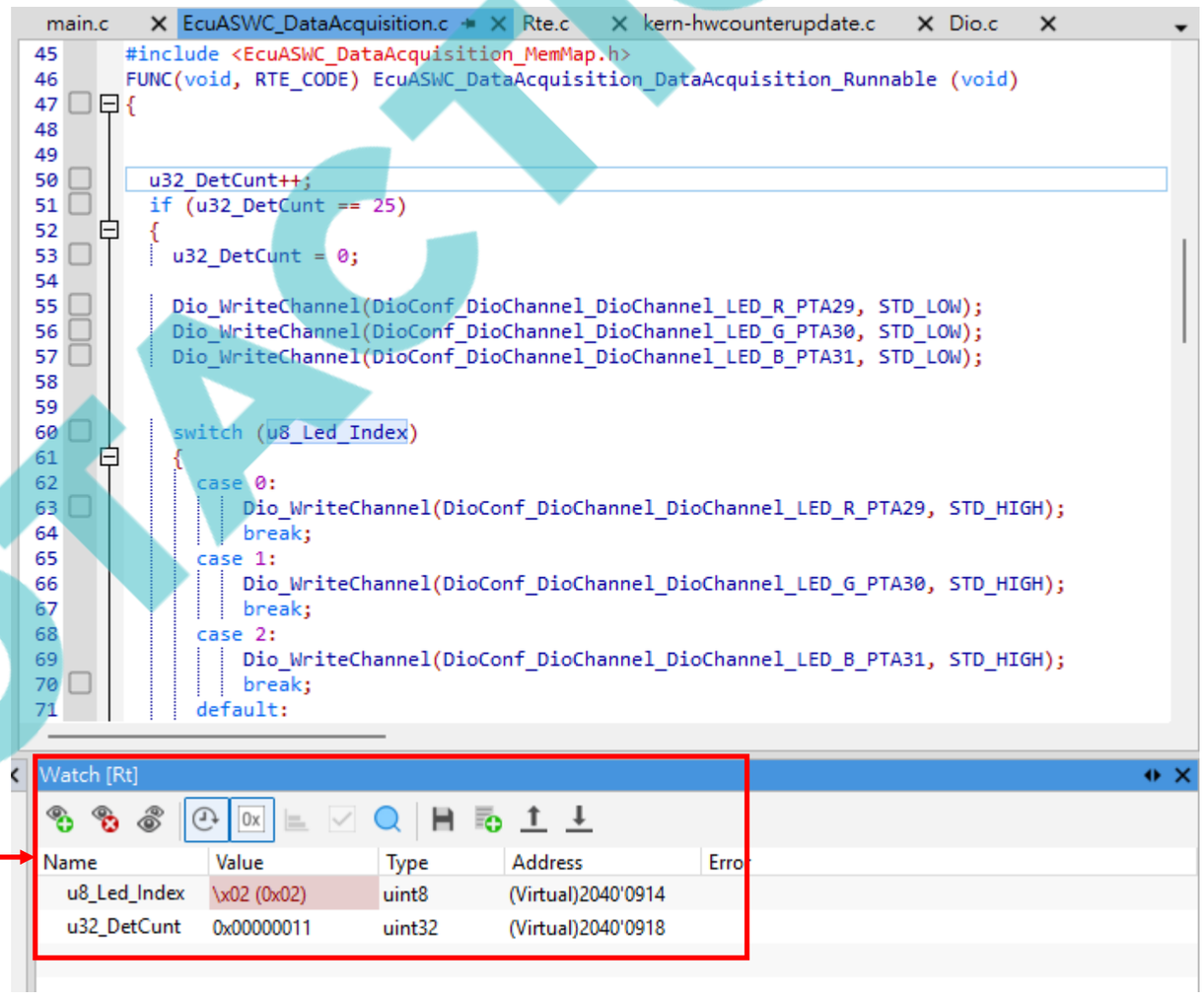
自定義Watch式窗名字



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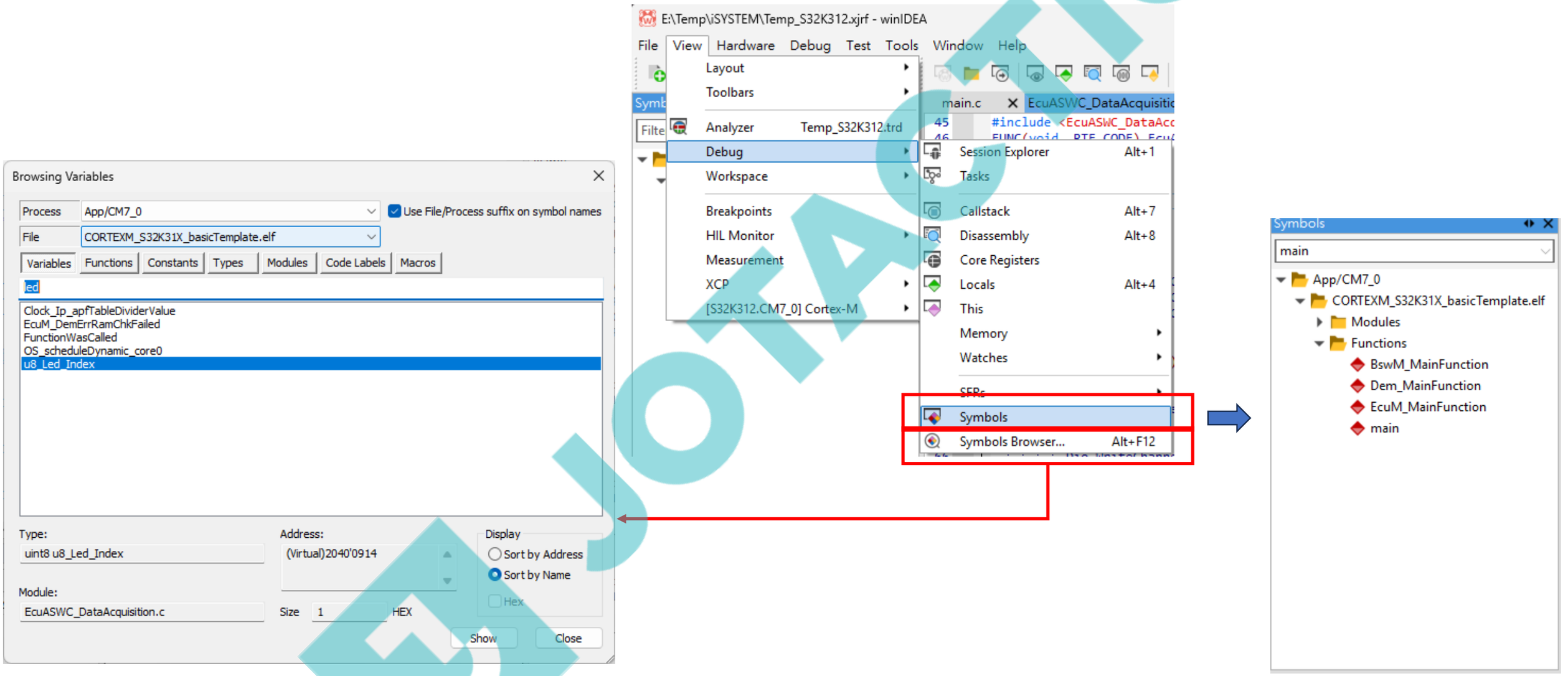
Debug 功能介紹 – Watches





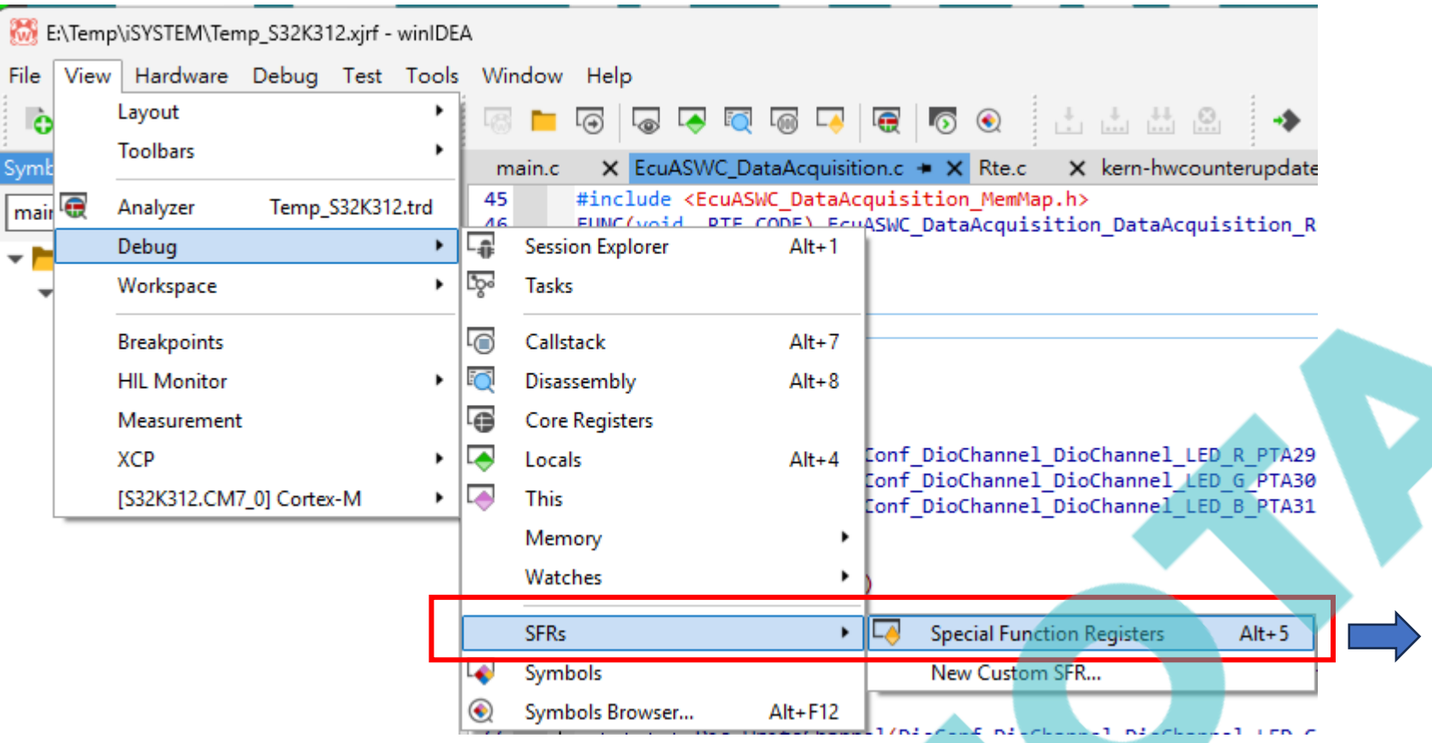
在想要觀察的變數上點擊右鍵，添加到Watch式窗當中。
Watch上窗中點擊時鐘圖案，即刻在Watch式窗中動態看到數值的變化

Debug 功能介紹 – Symbols

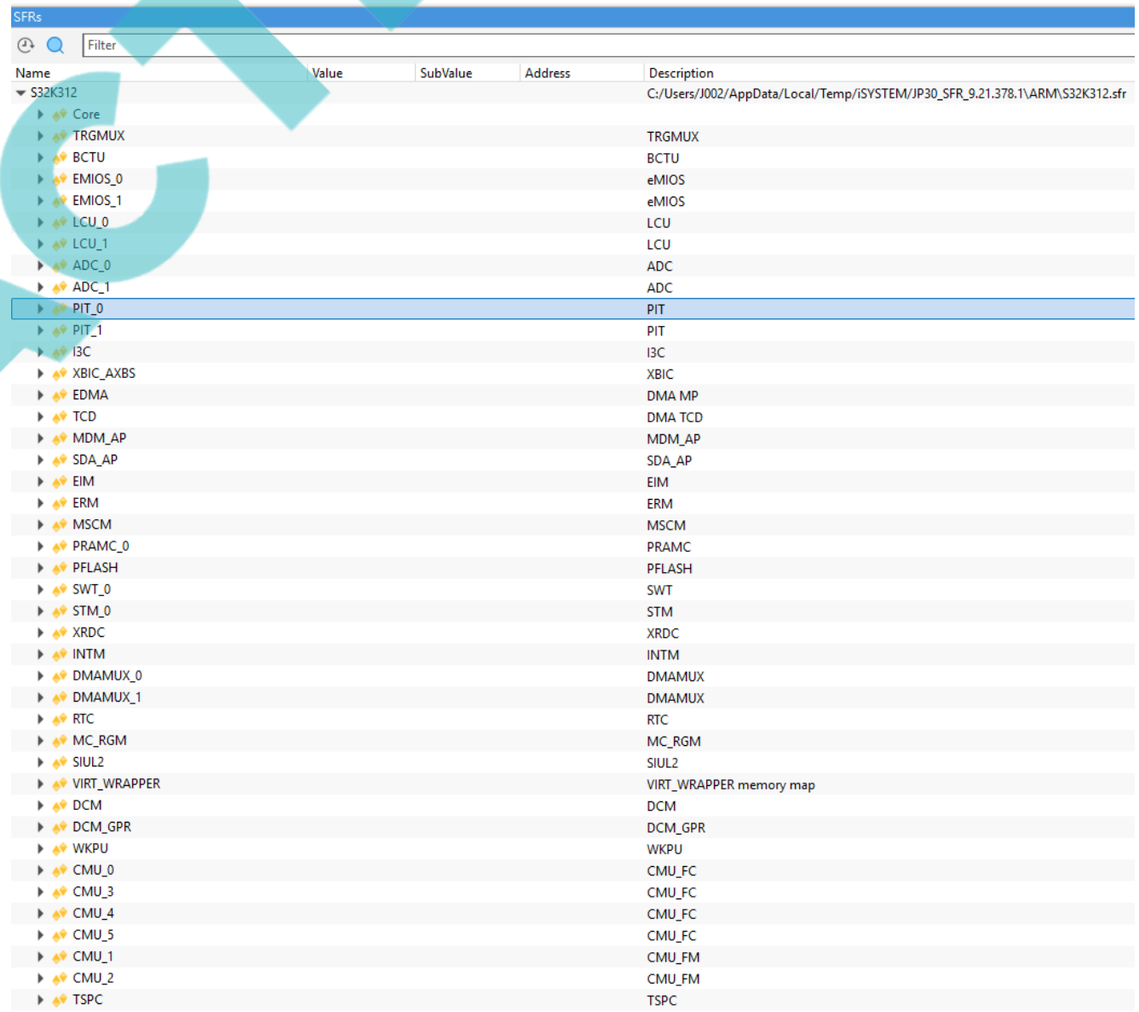


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Debug 功能介紹 – SFRs

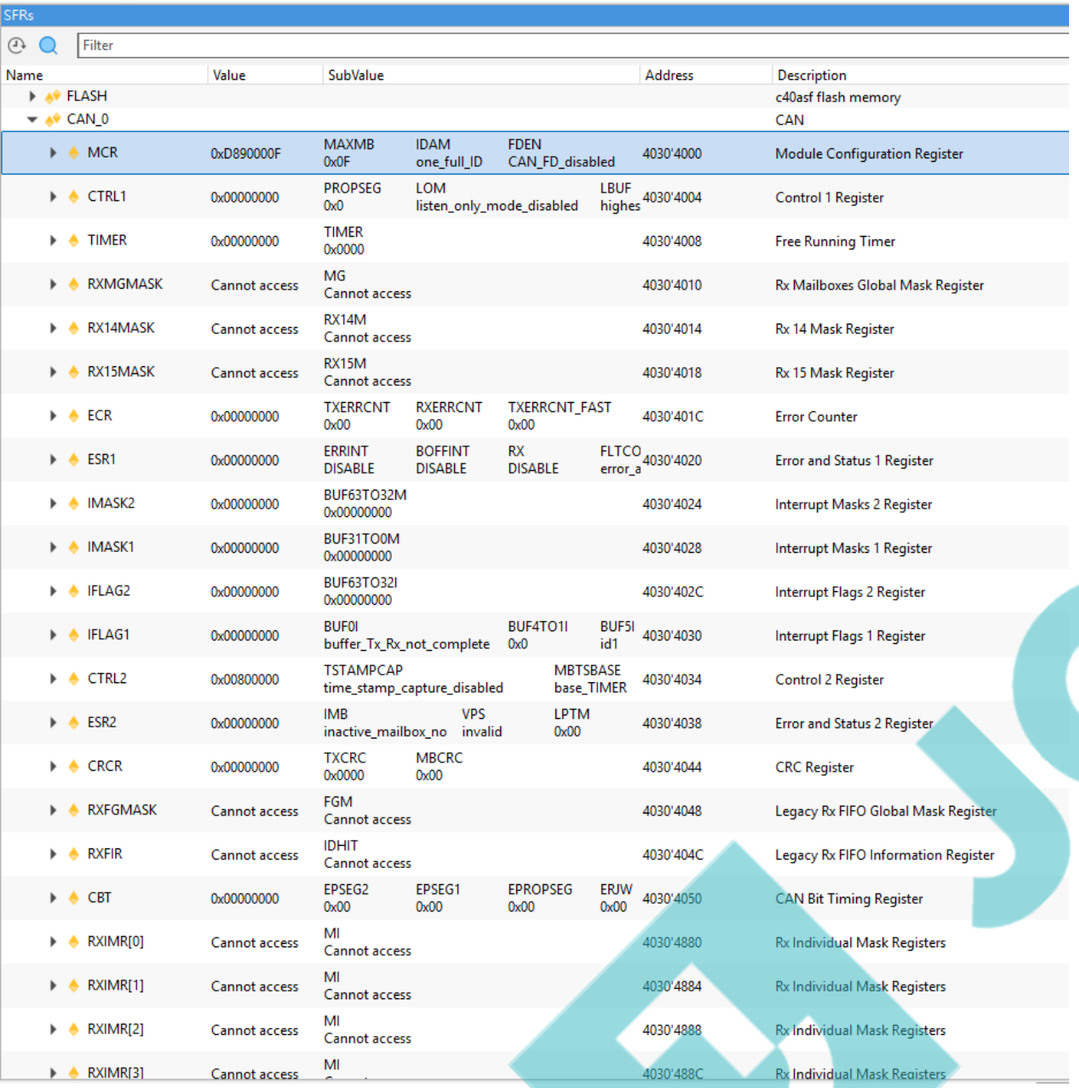


晶片暫存器相關資訊

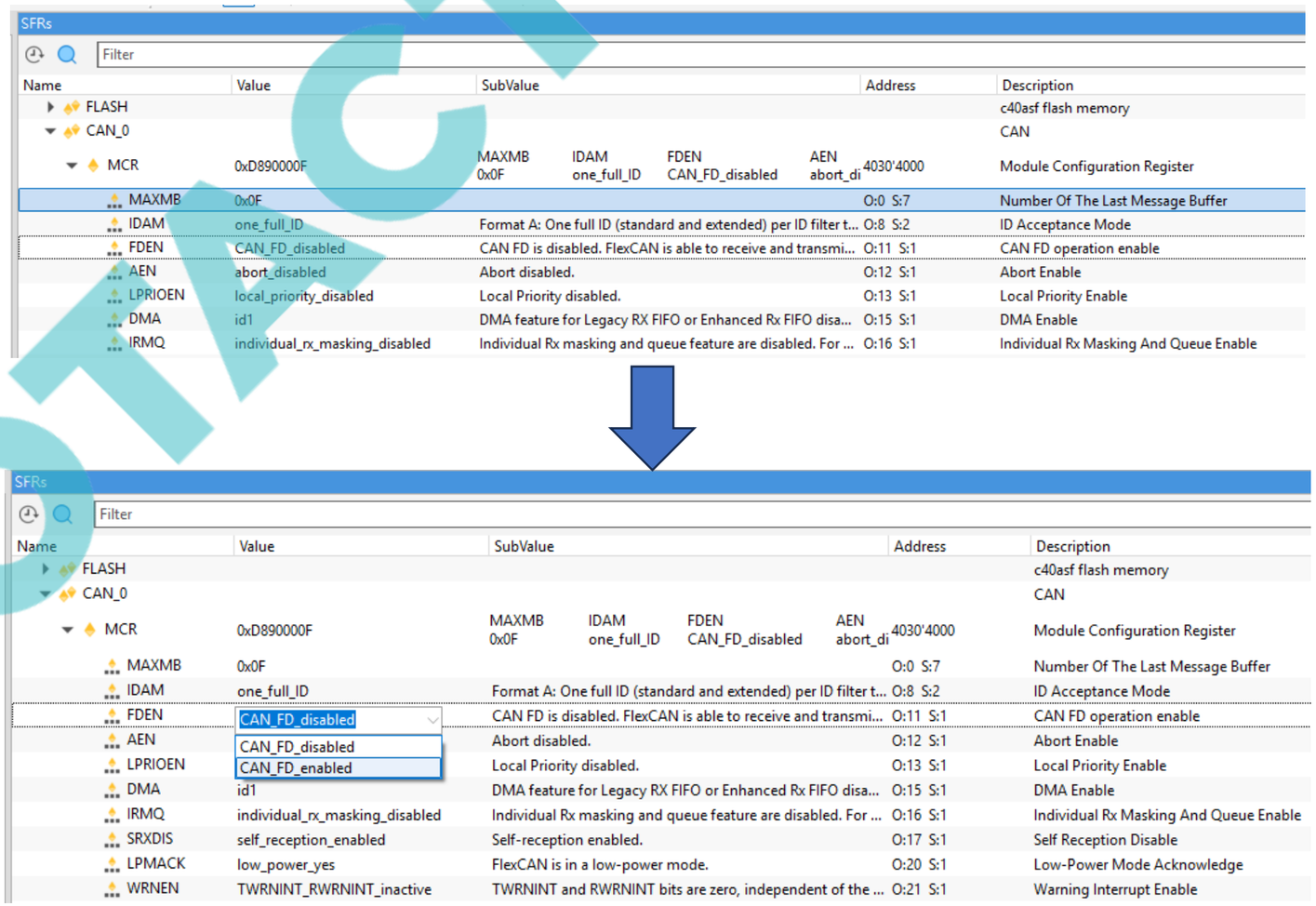


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Debug 功能介紹 – SFRs



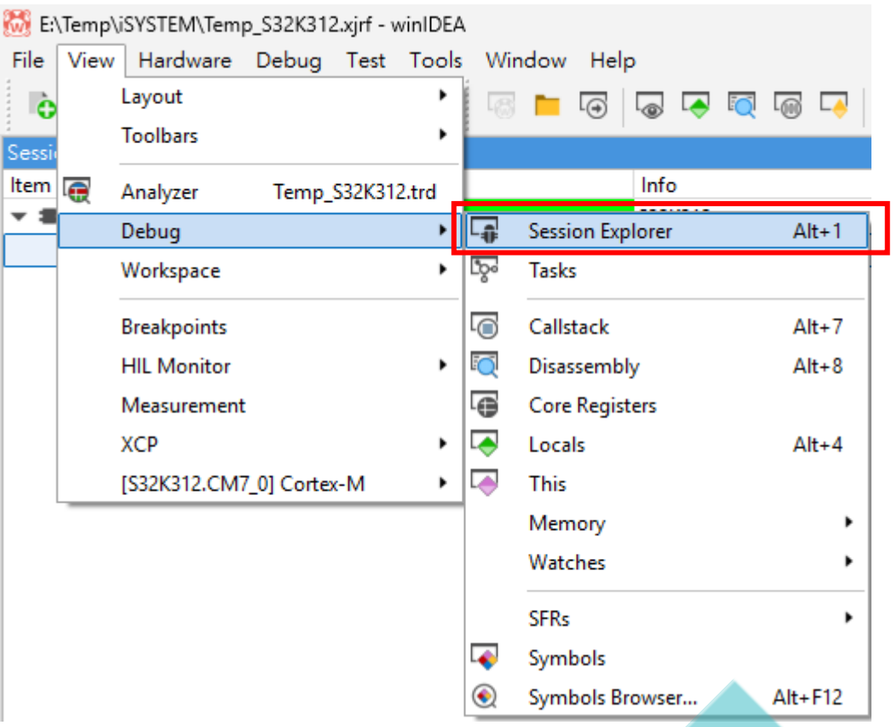
可以在線查看晶片暫存器狀態，也能線上及時修改暫存器數值(請查看晶片手冊進行操作)

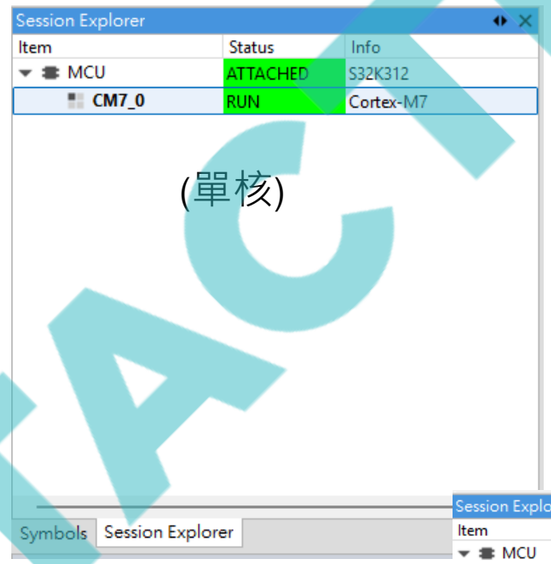


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Debug 功能介紹 – Session Explorer

可以查看晶片核心運行的情況





(單核)



(多核)

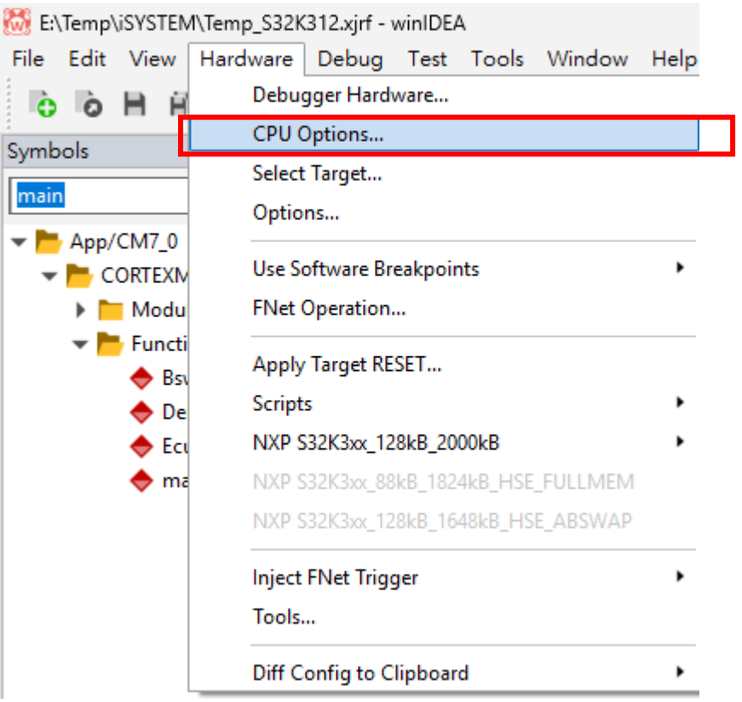
JOTACTIC

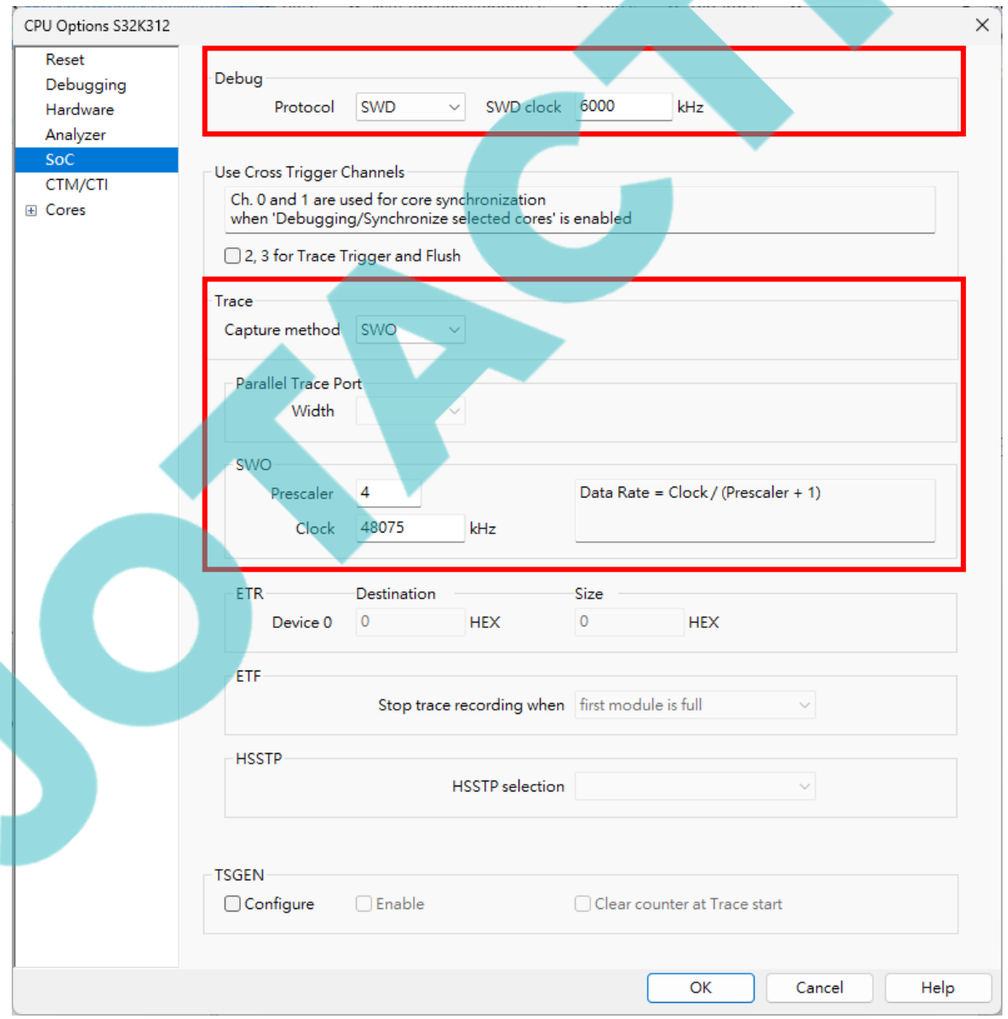


- 創建 iSYSTEM 專案
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- Tasks Window
- testIDEA

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Debug 介面設定



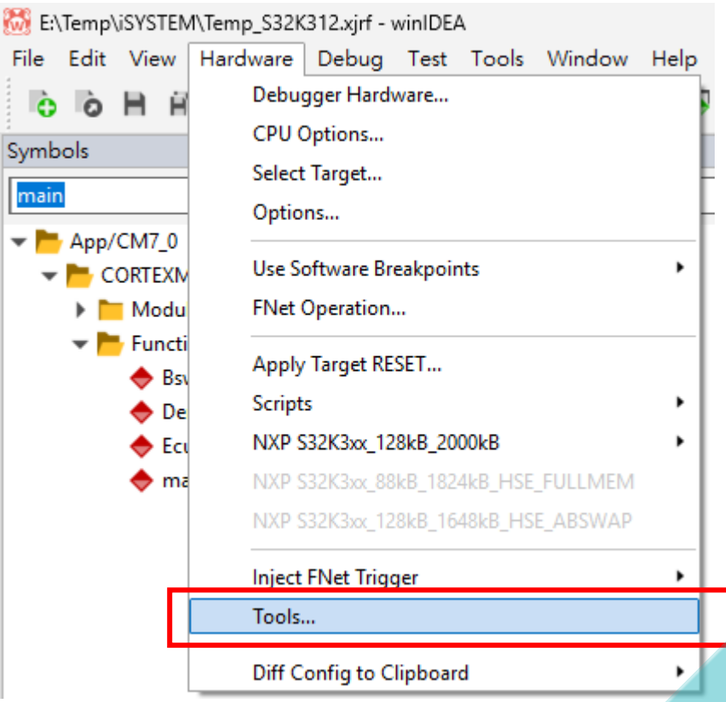


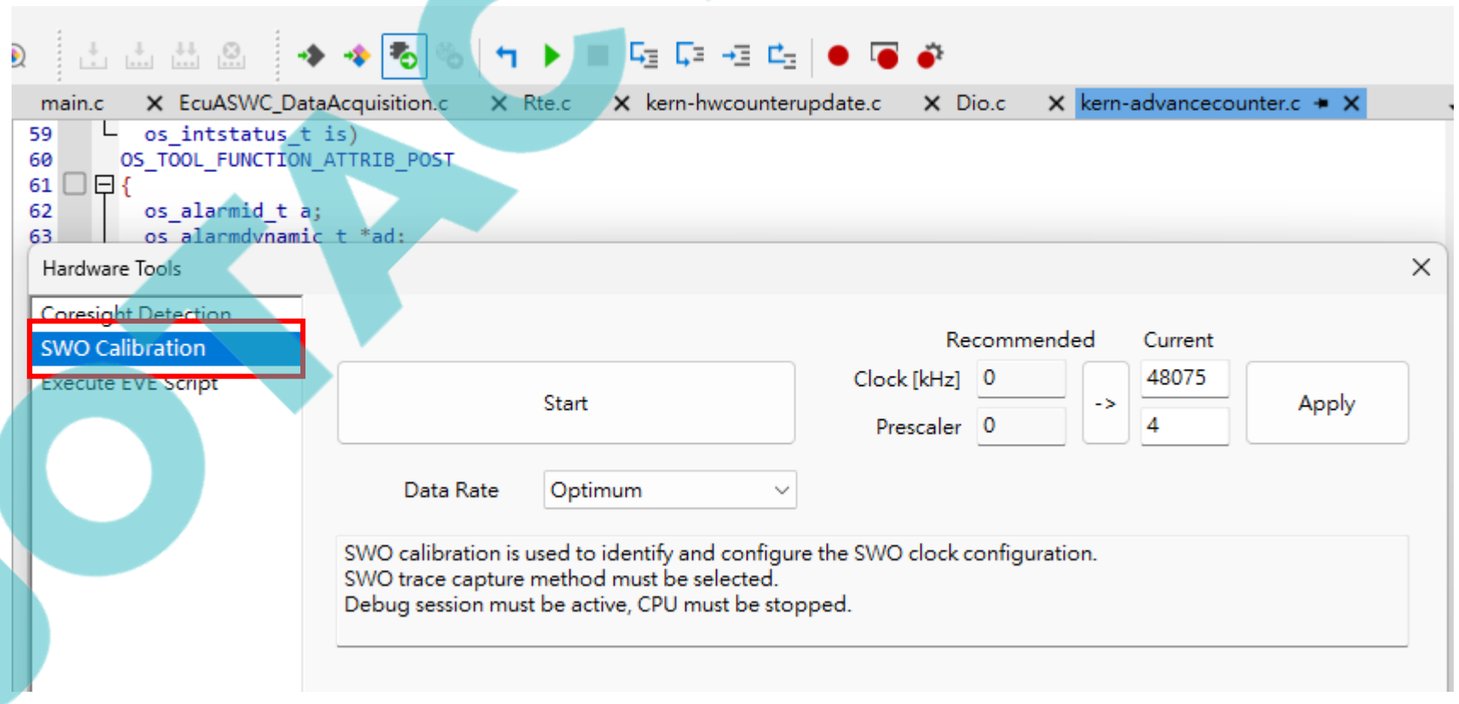
不同晶片核心架構支持的介面會不一樣，因此介面也會有所差異。
此處需要根據晶片手冊、ECU板子，並參考winIDEA手冊來做調整

SWO的部分winIDEA內建調教工具，請參考下一頁

JOTACTIC

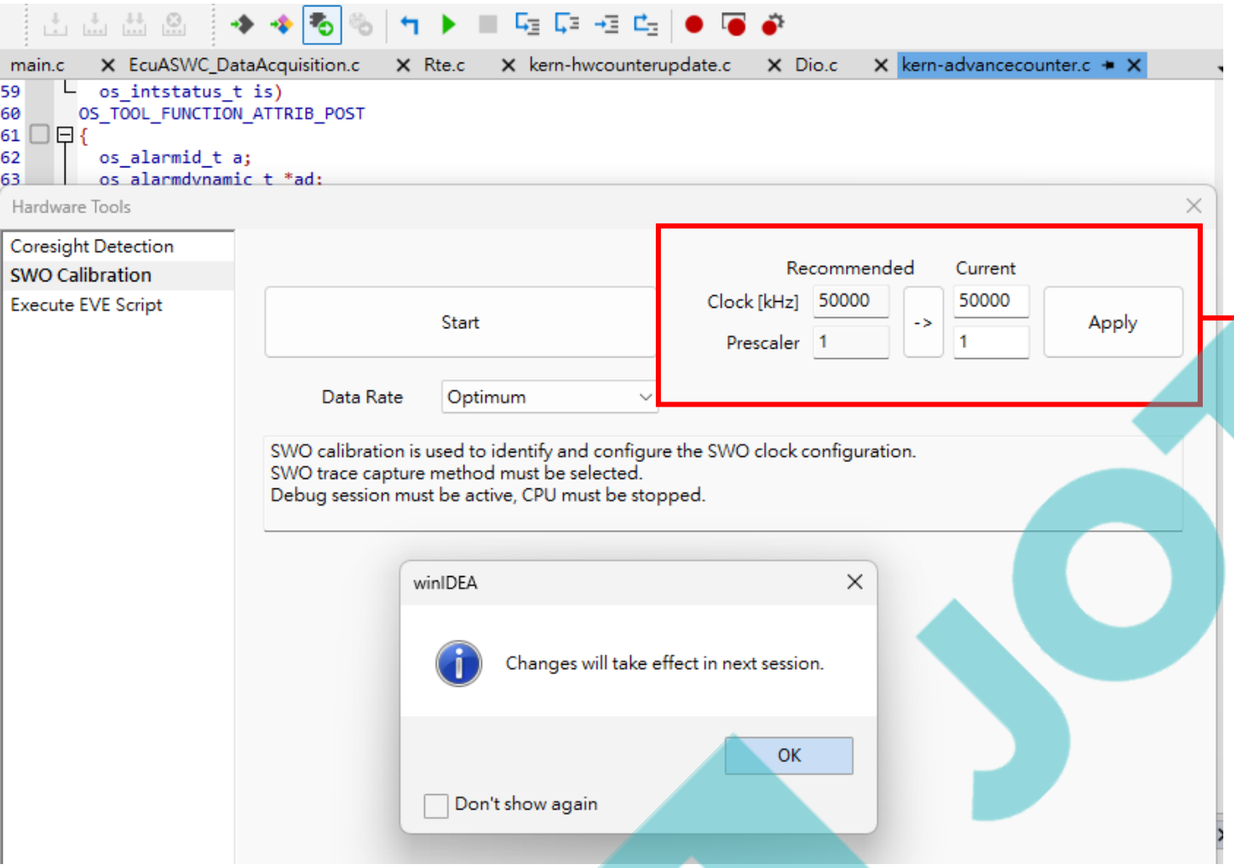
Debug 介面設定

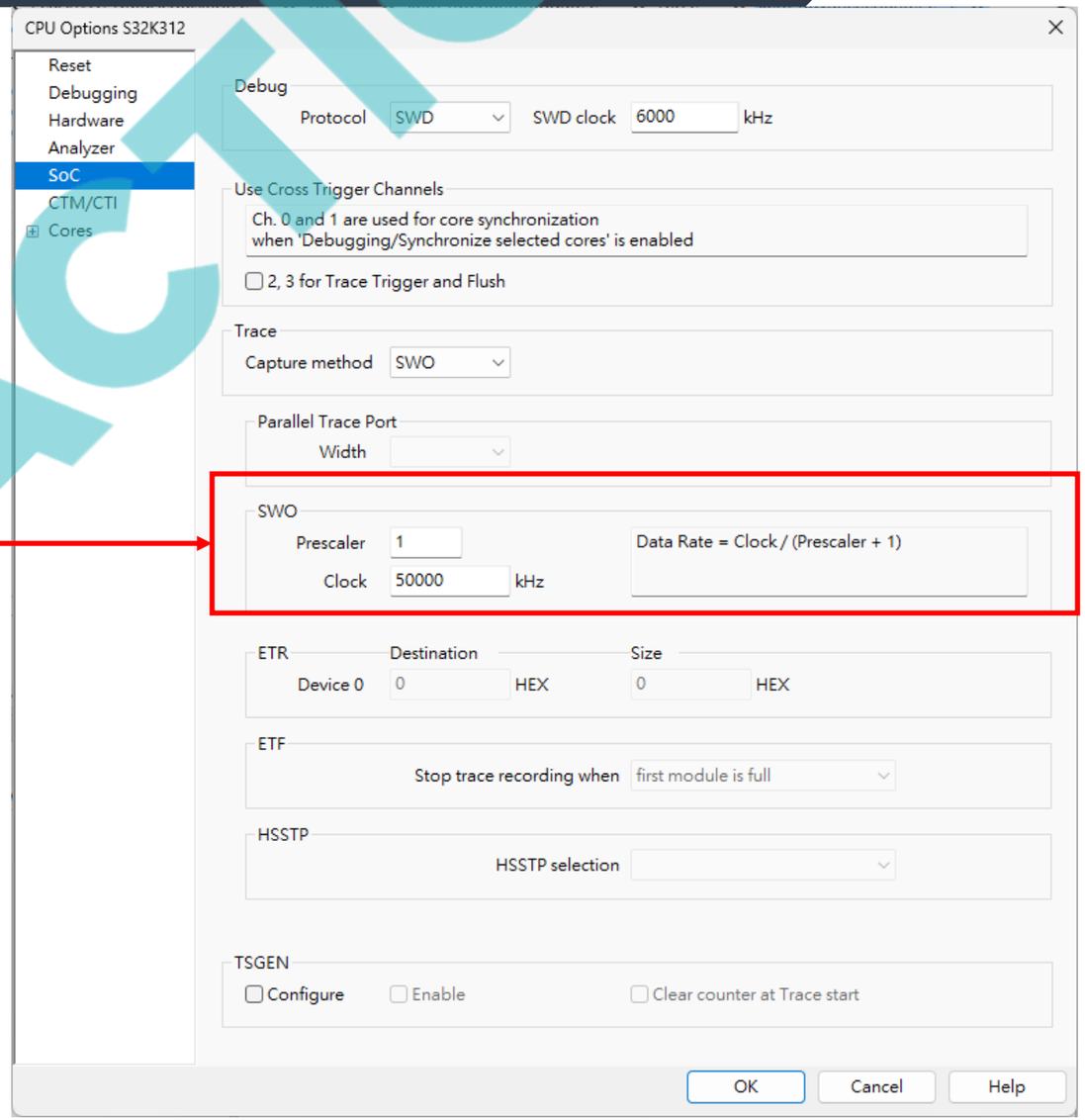




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Debug 介面設定





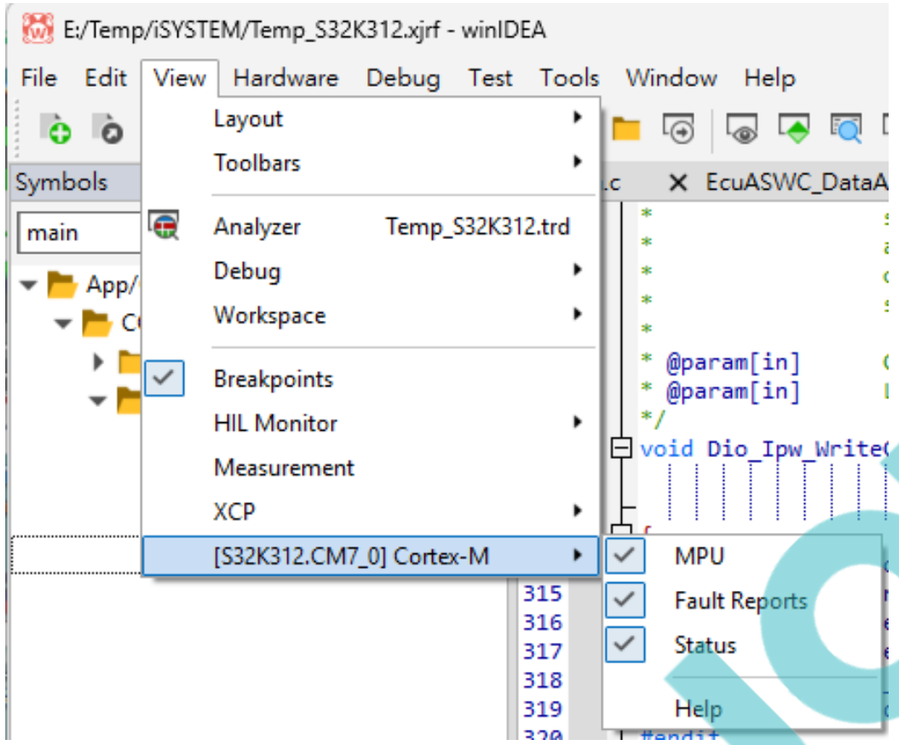
JOTACTIC

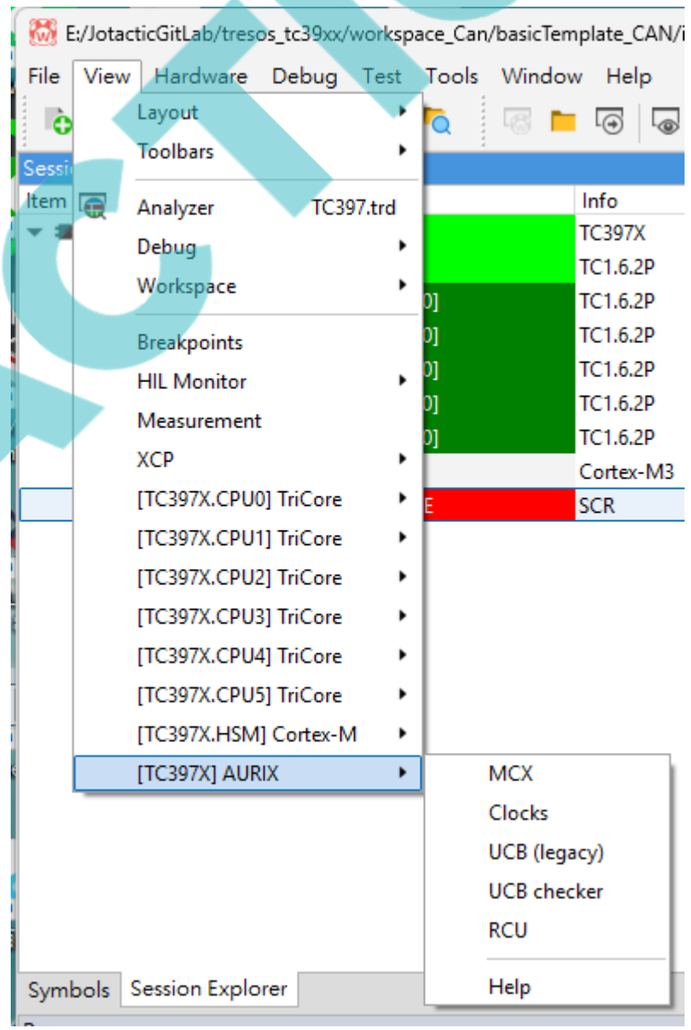


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JOTACTIC

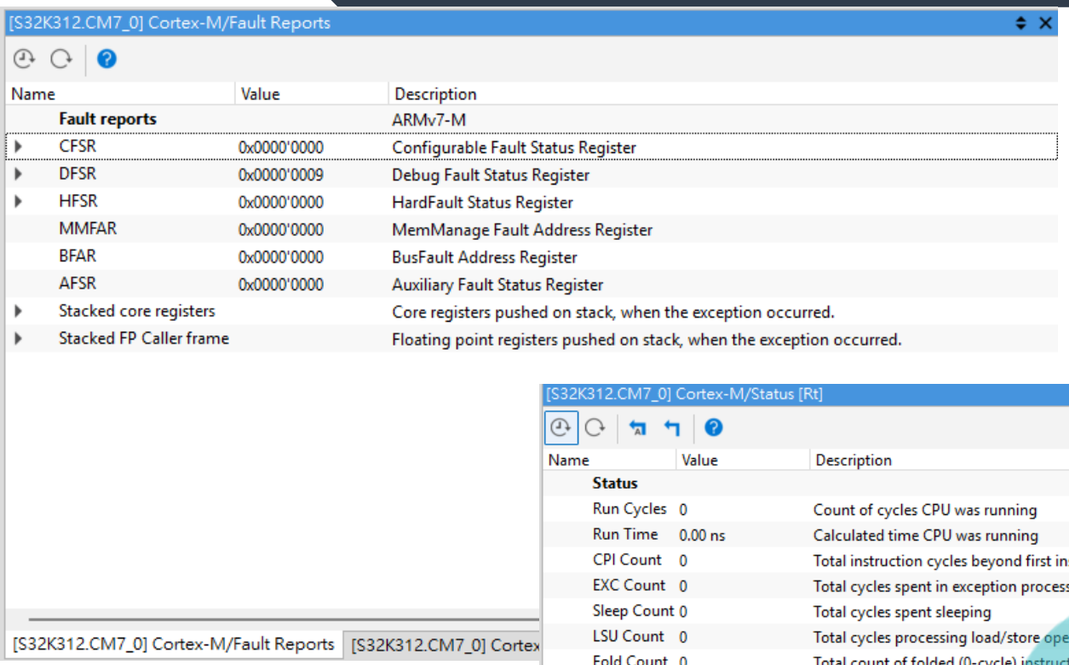
Architecture-specific plugins

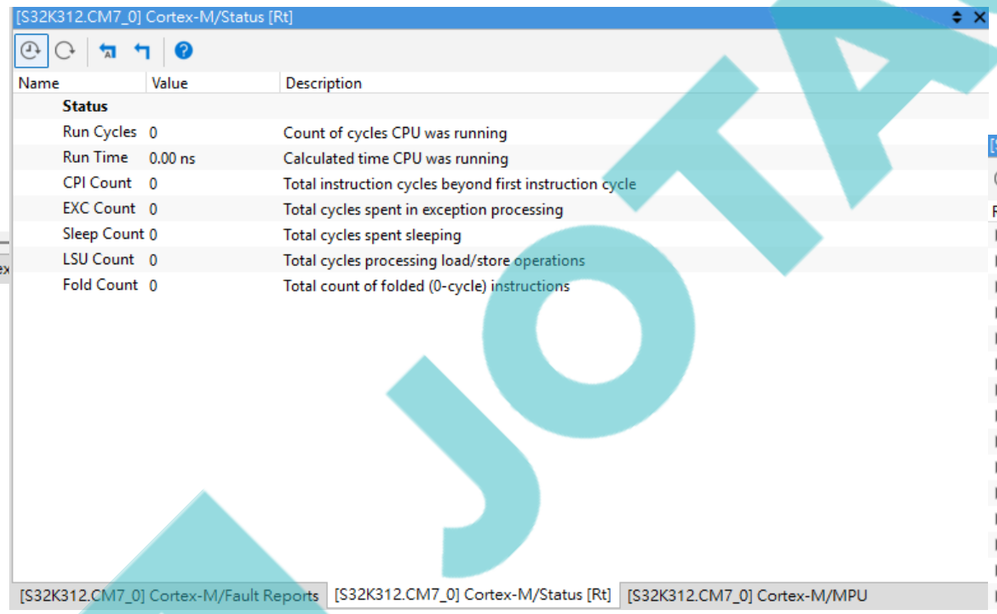


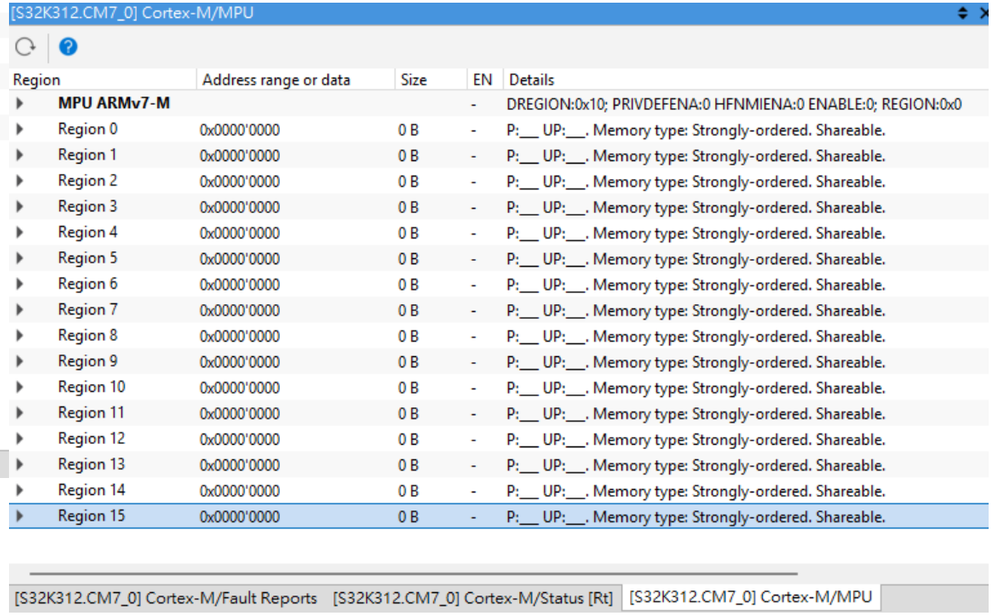


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Architecture-specific plugins

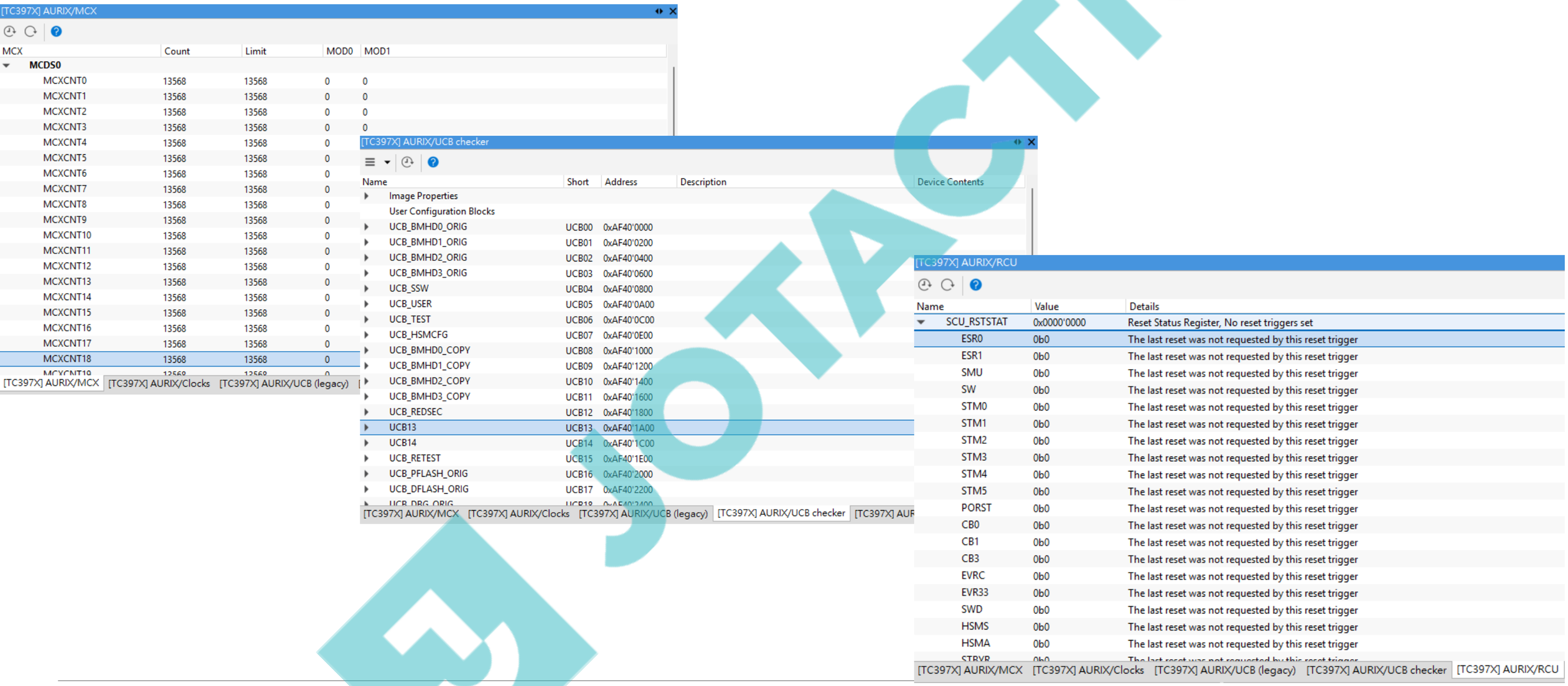






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Architecture-specific plugins



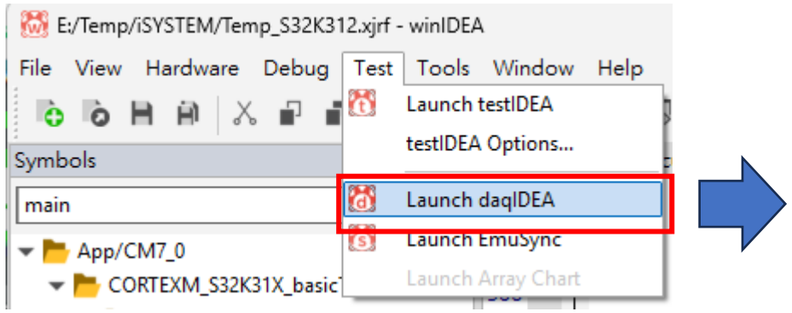
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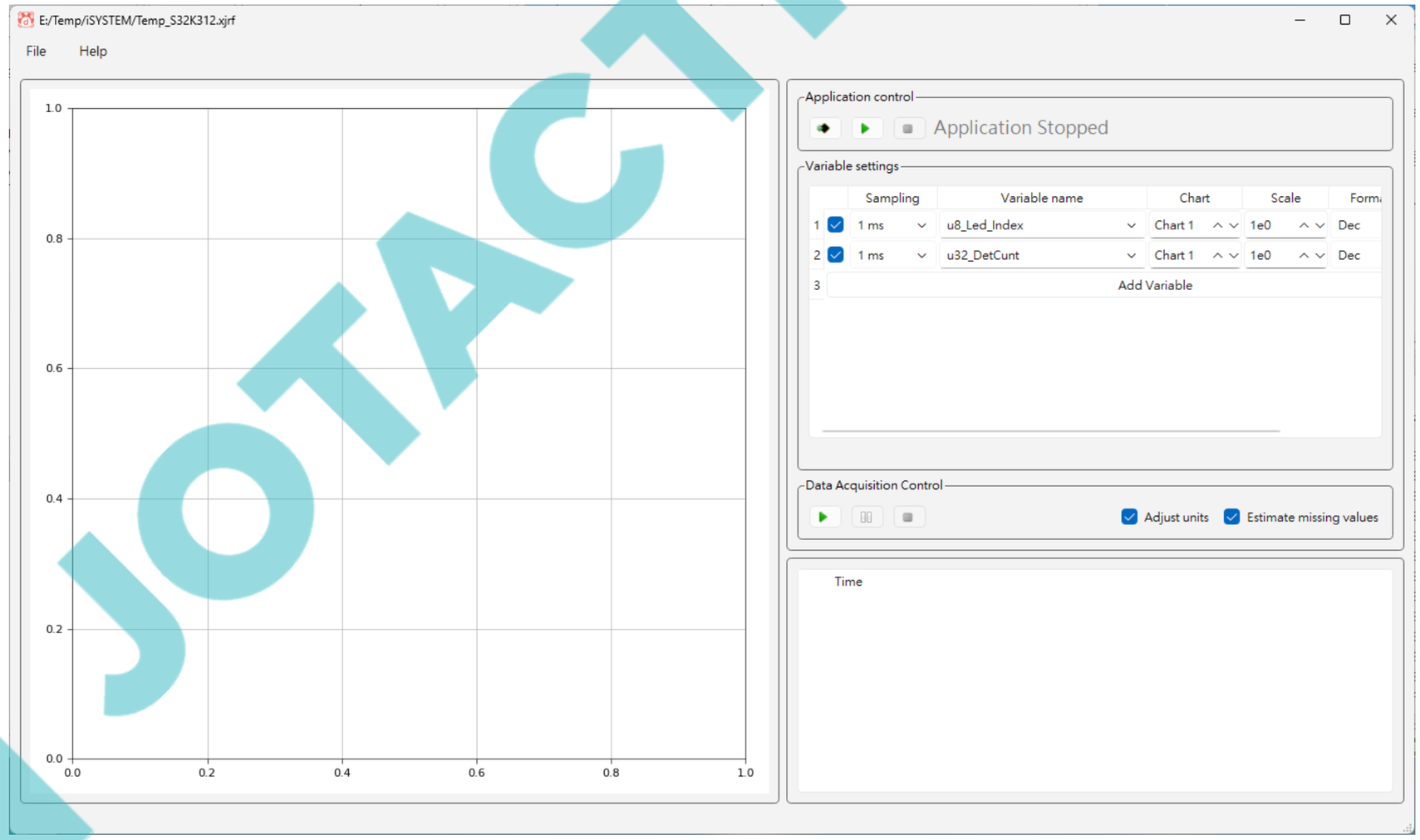


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daqIDEA



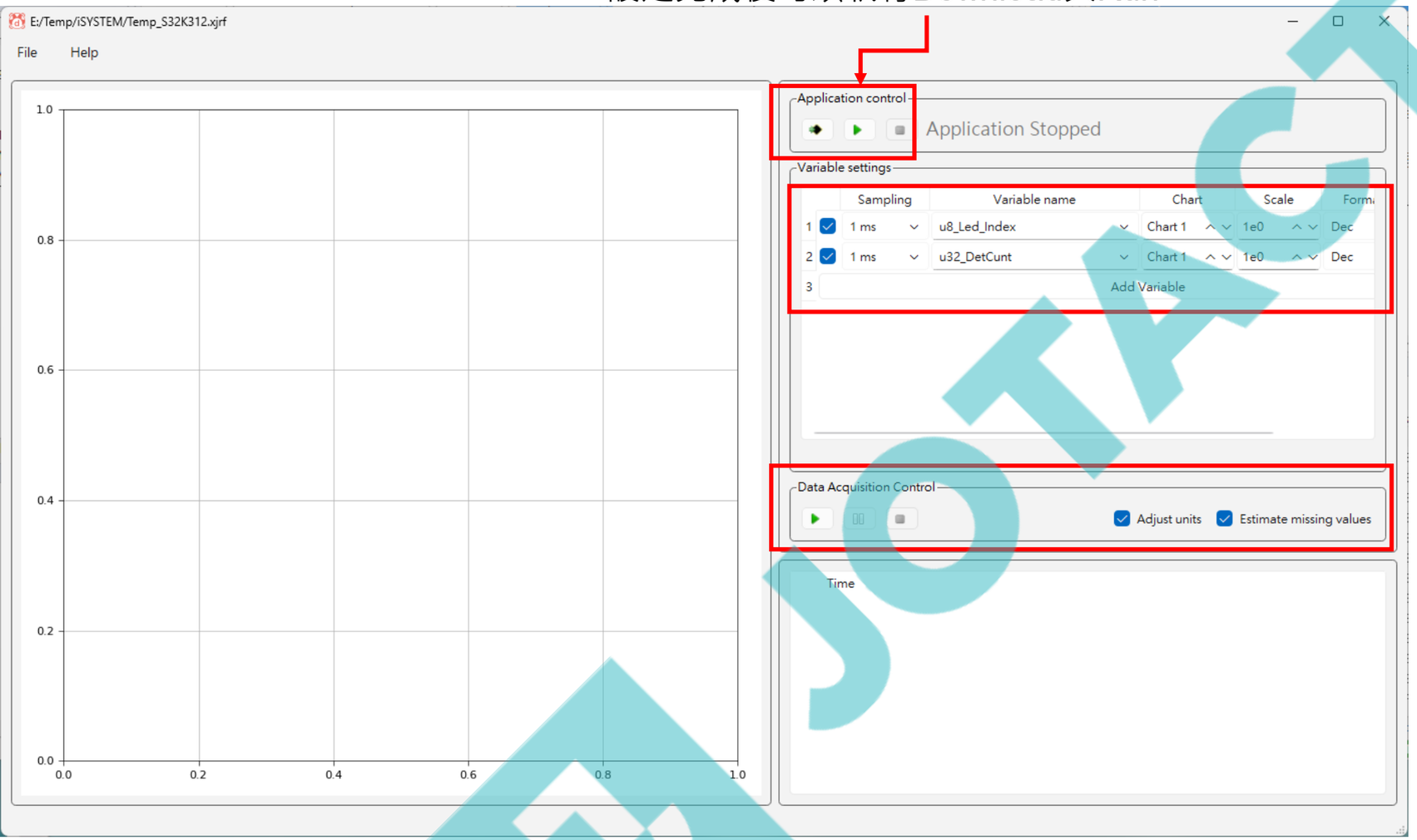


可以用來觀察變數轉跳的過程，對於有狀態定義的變數可以更直覺看出當前和過往的狀態的變化。

JOTACTIC

daqIDEA

設定完成後可以執行Download與Run

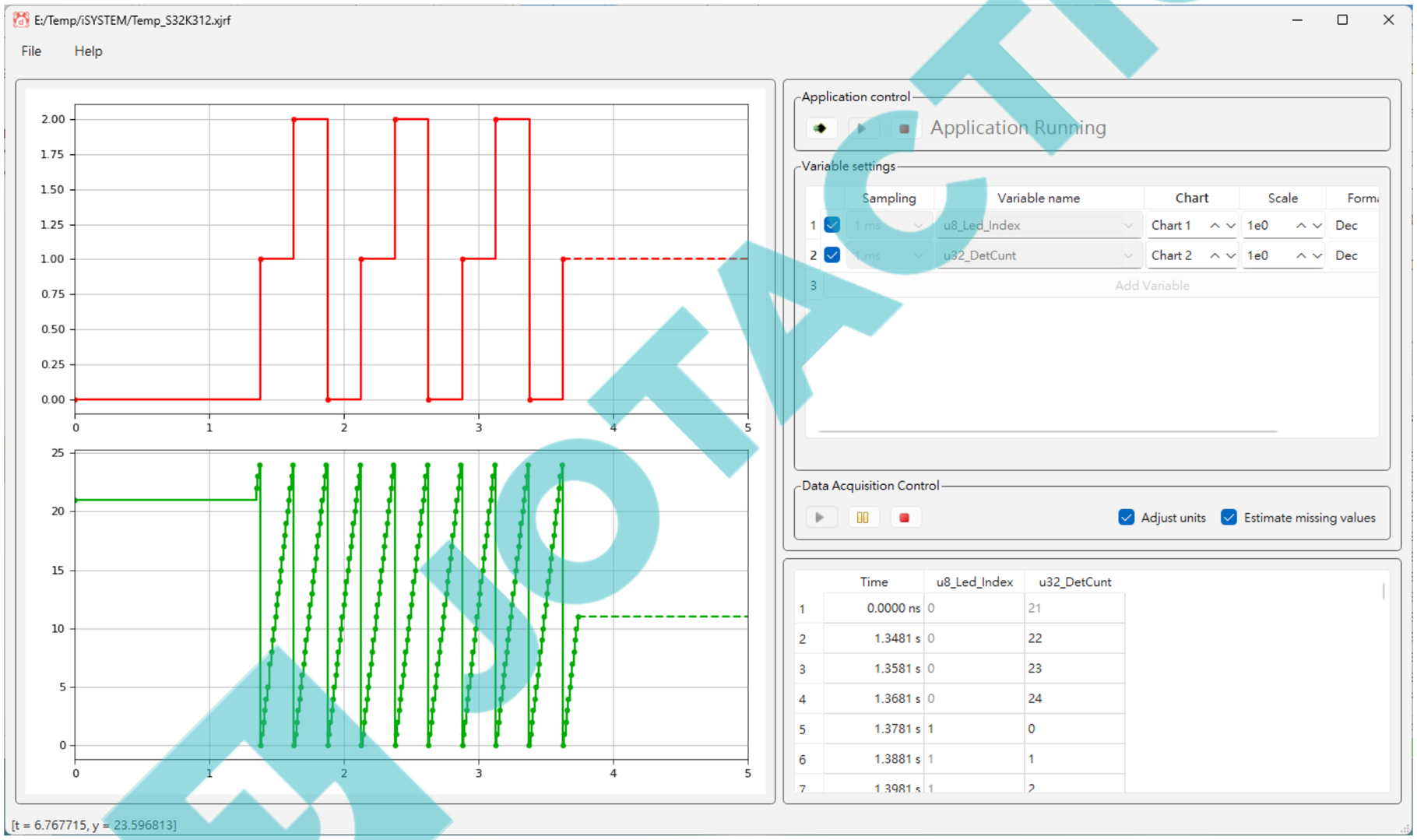


根據需求設定Sampling速度、選擇觀察的變數、變數顯示通道等參數。

如需要紀錄資訊，記得點擊Run ，最後要記得保存。

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daqIDEA

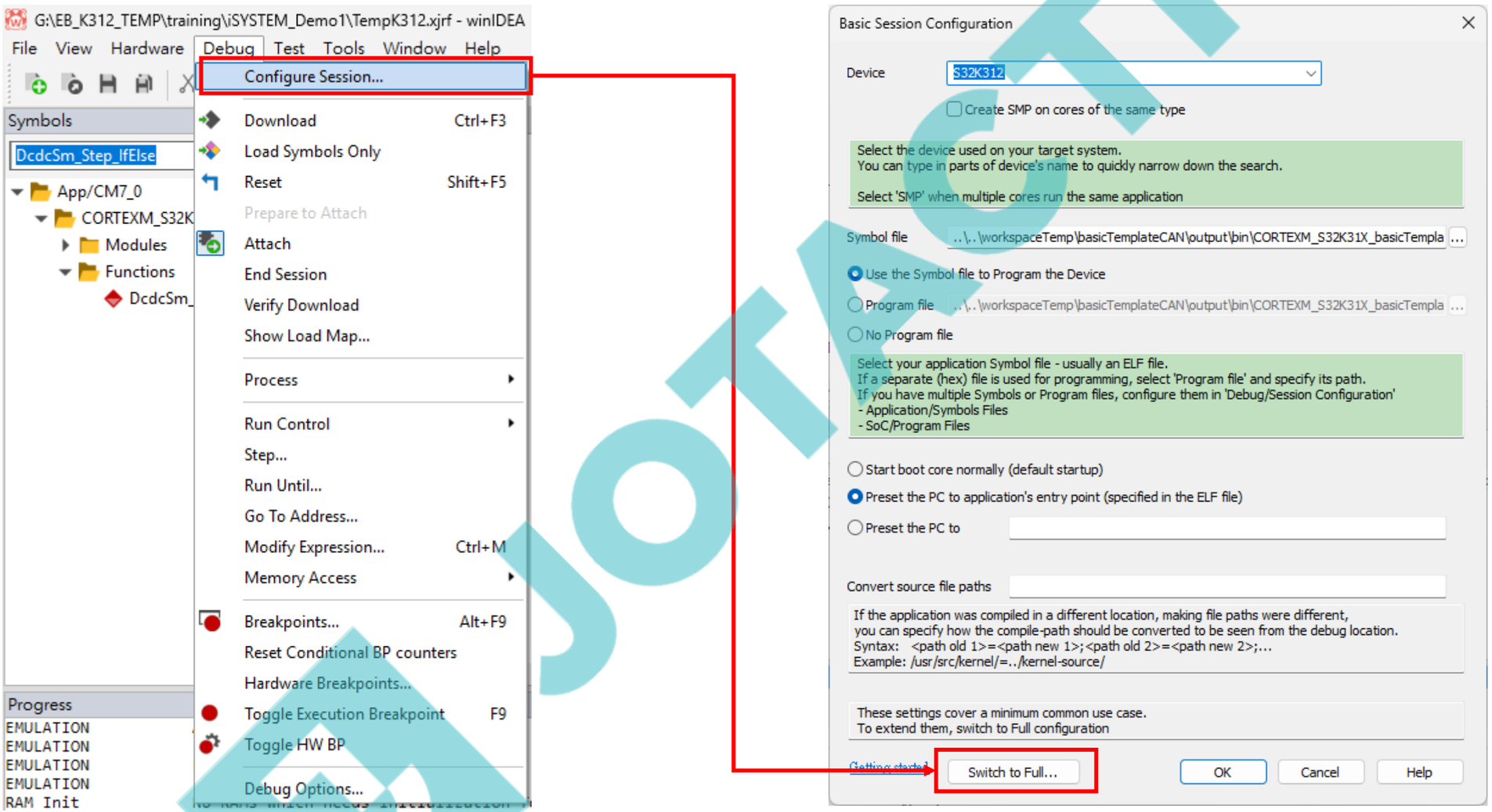


JOTACTIC

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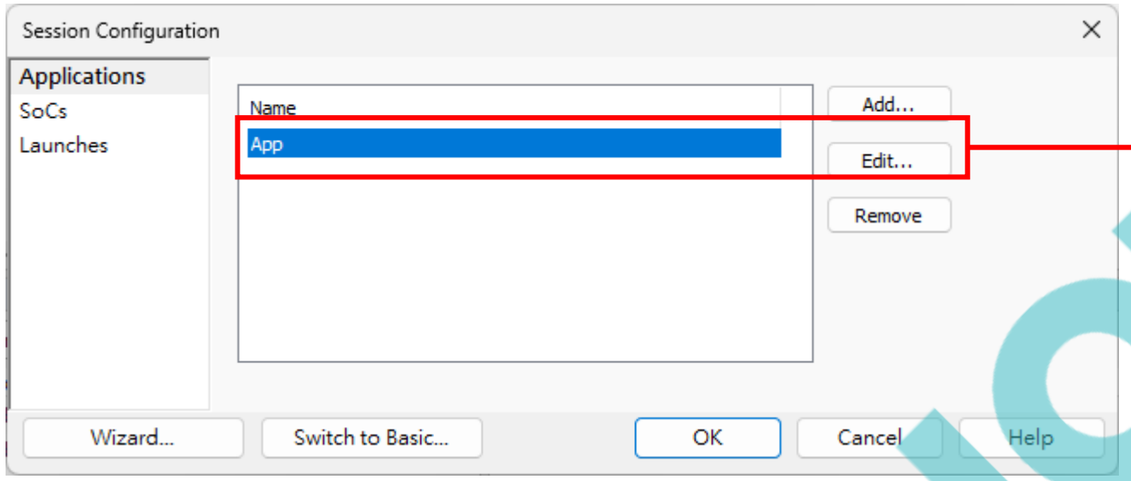
Tasks Window

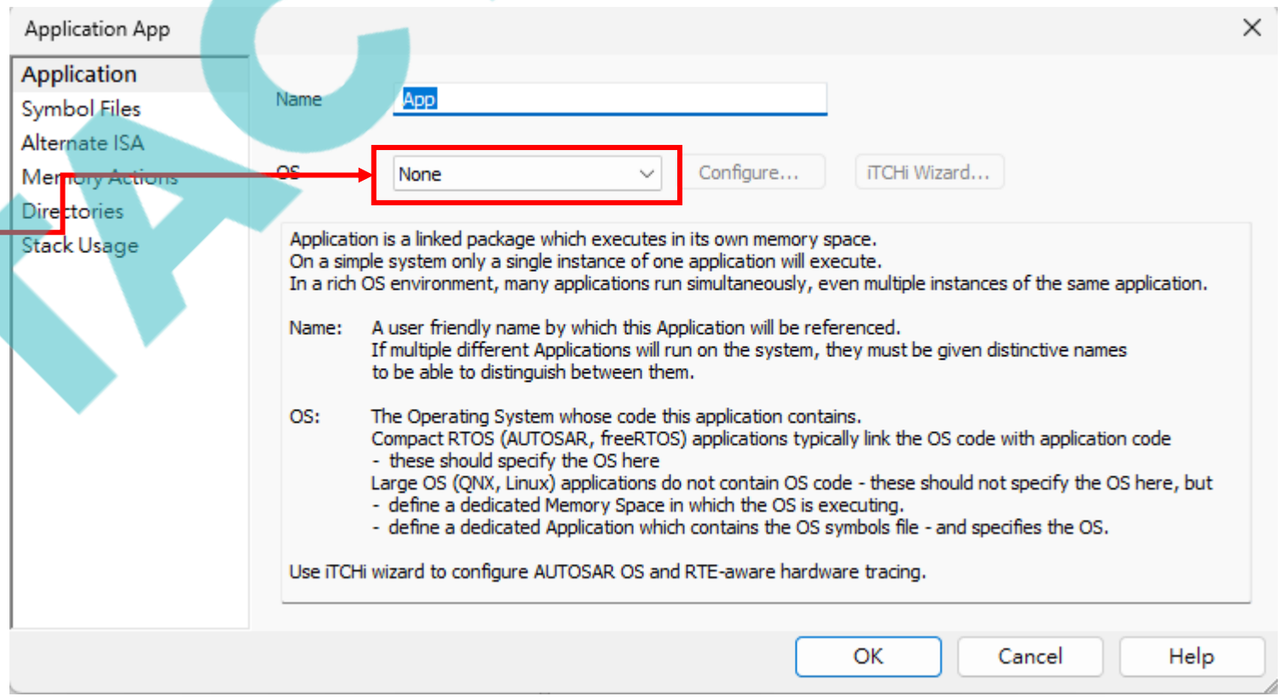


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Tasks Window

選擇App, 點擊Edite

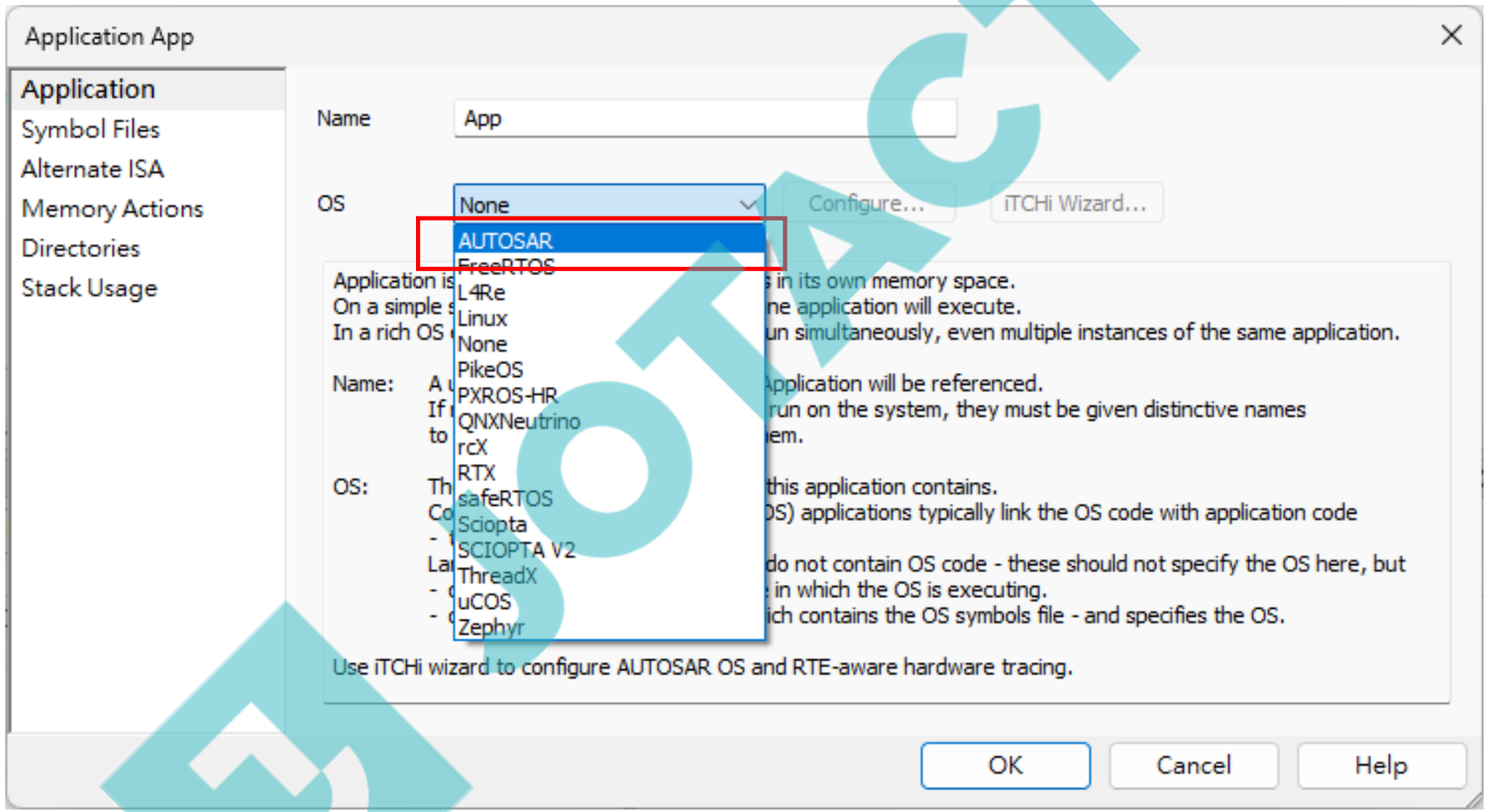




JOTACTIC

Tasks Window

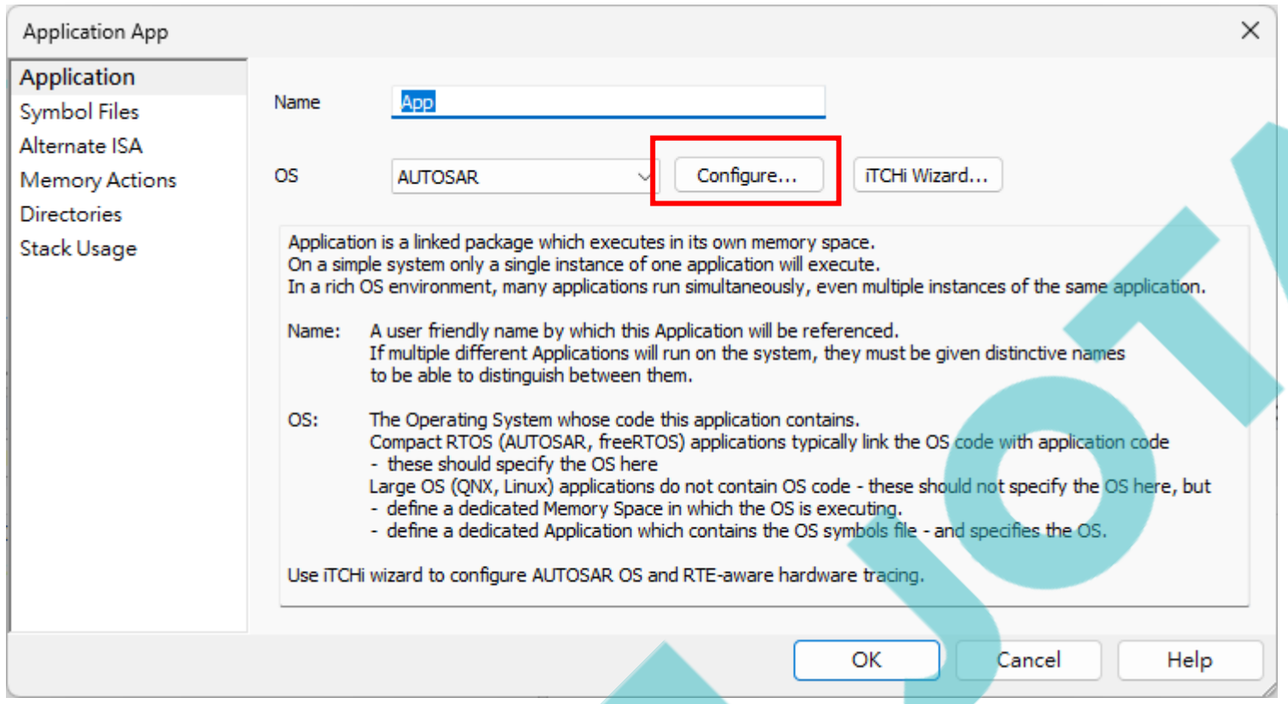
根據您運行在MCU上的系統來選擇對應的參數

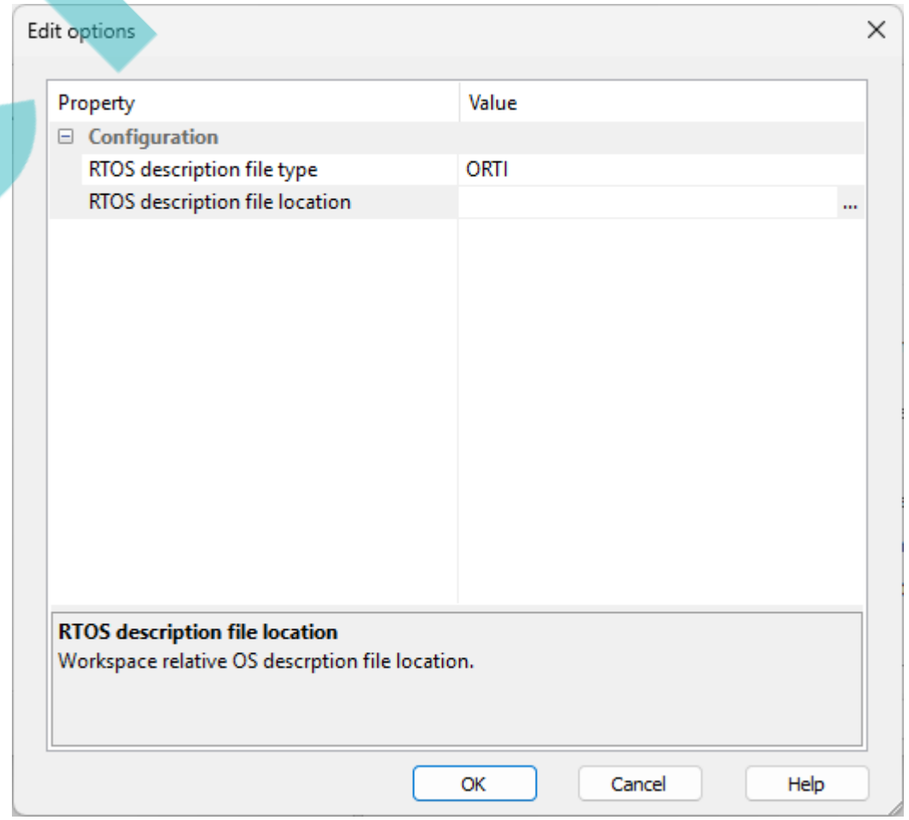


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Tasks Window

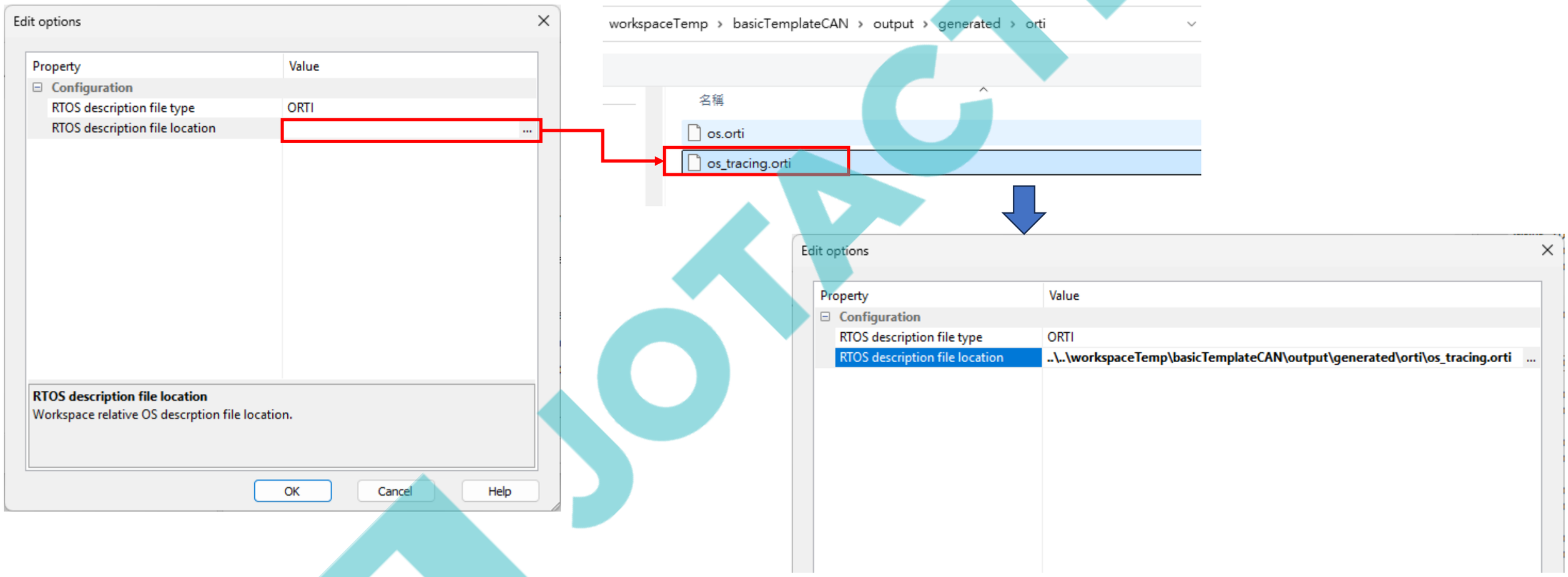
此處展示的範例是選用AUTOSAR系統，不同的系同配置可能會有所差異
請根據當前使用的系統並參考winIDEA手冊進行配置與調整





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Tasks Window

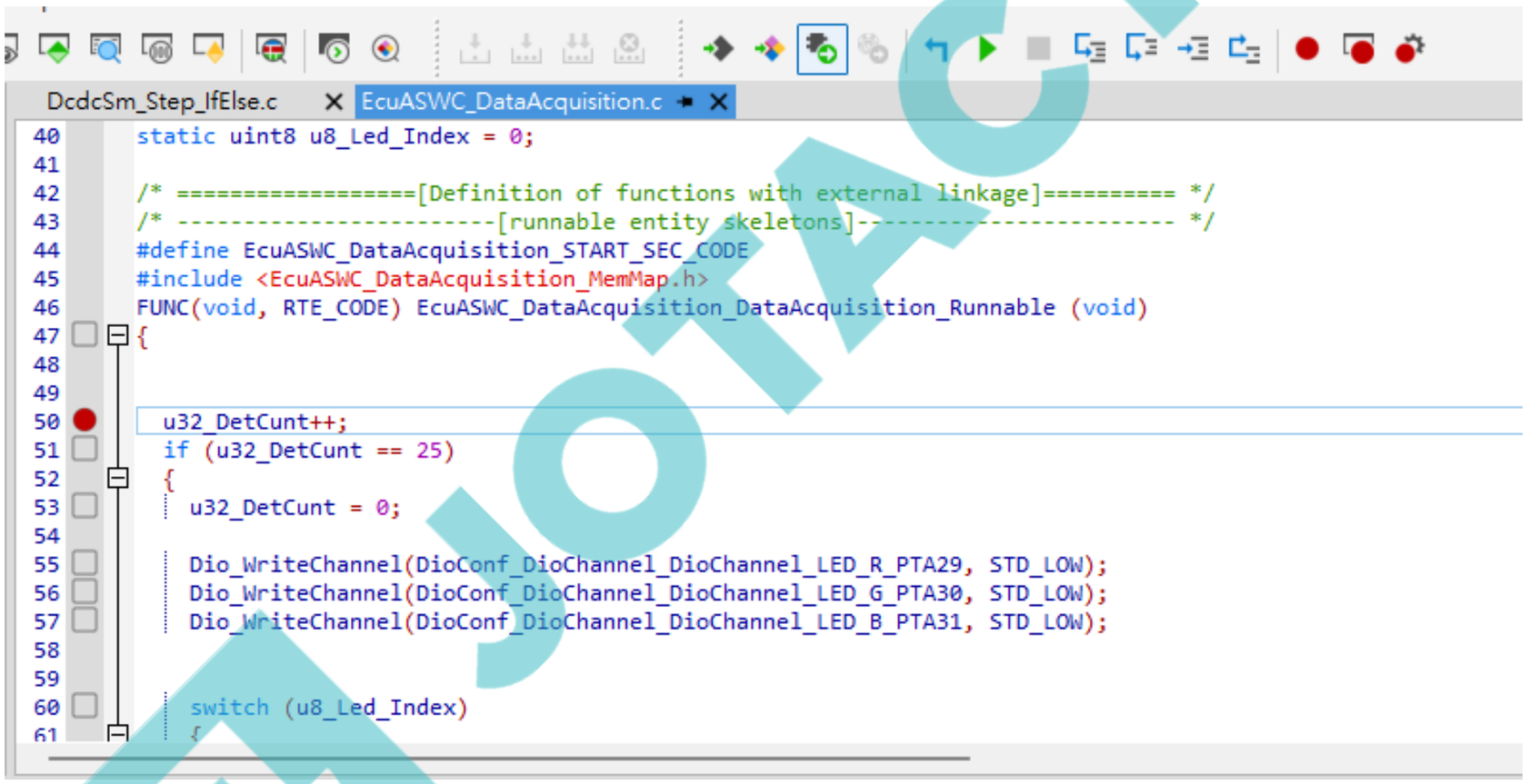


此處展示的範例是選用AUTOSAR系統，不同的系同配置可能會有所差異。
請根據當前使用的系統並參考winIDEA手冊進行配置與調整

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Tasks Window

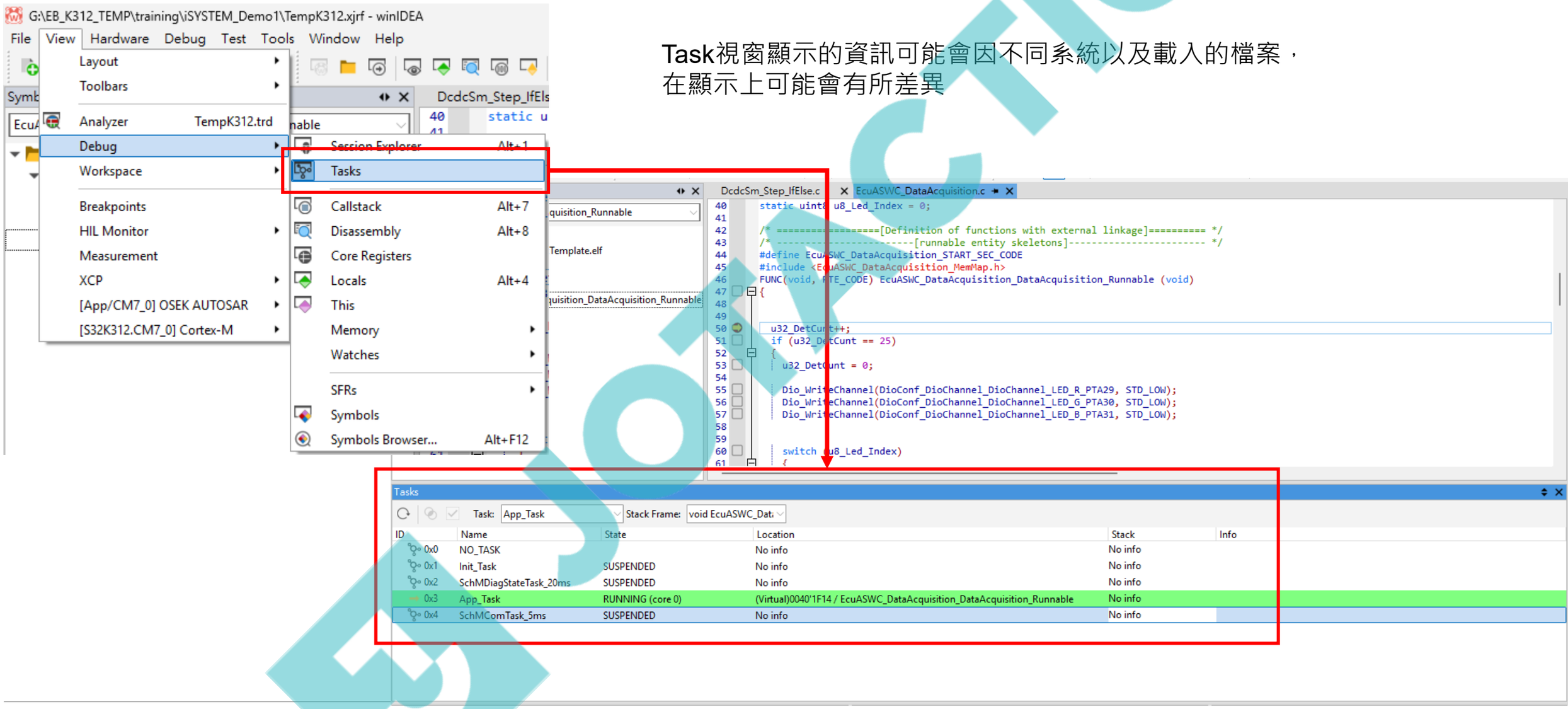
在想要觀察的地方設置中斷點，並運行系統



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Tasks Window

Task視窗顯示的資訊可能會因不同系統以及載入的檔案，
在顯示上可能會有所差異



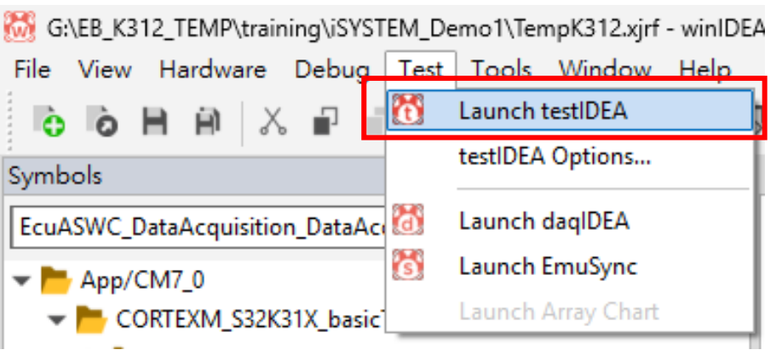
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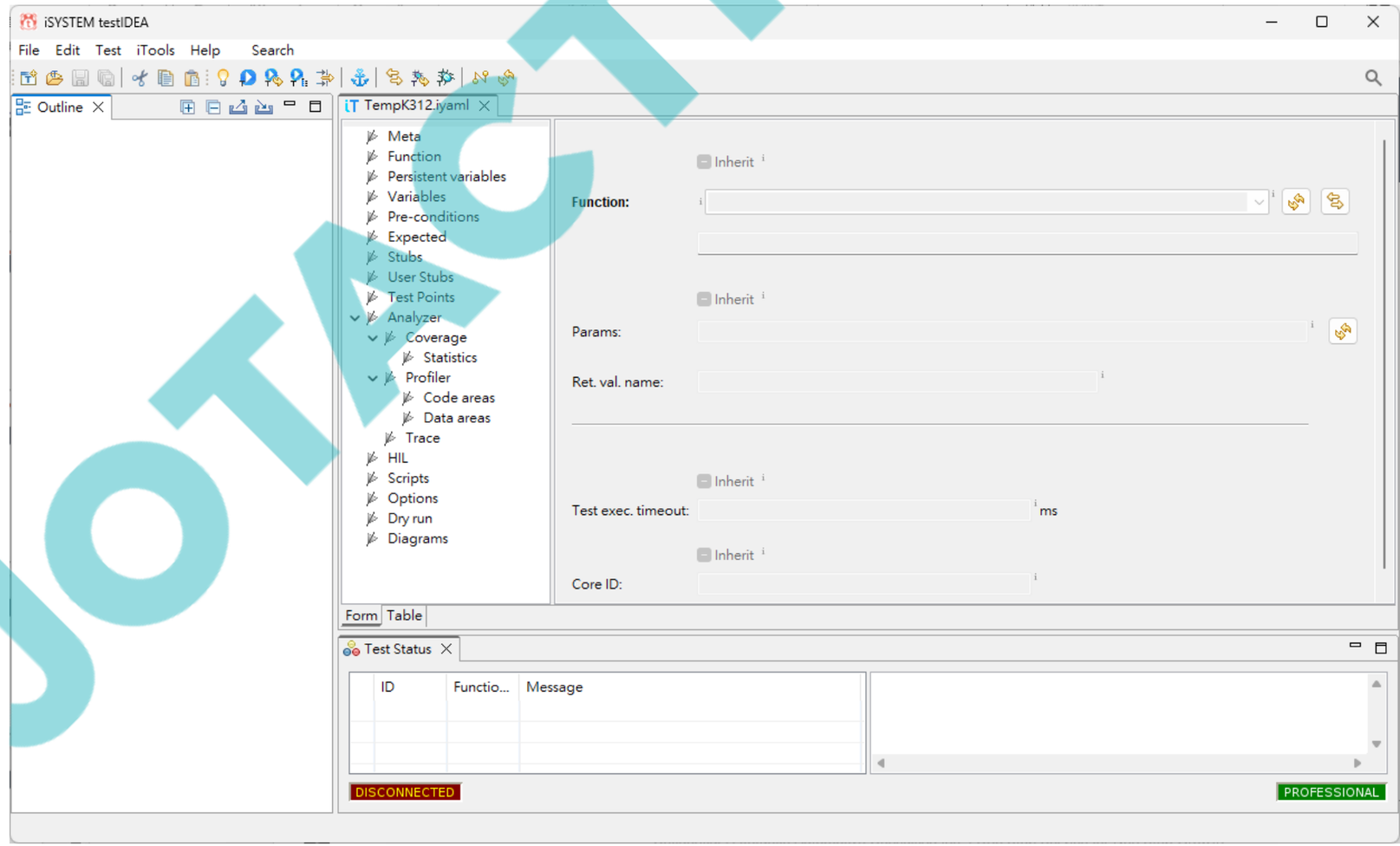


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JOTACTIC

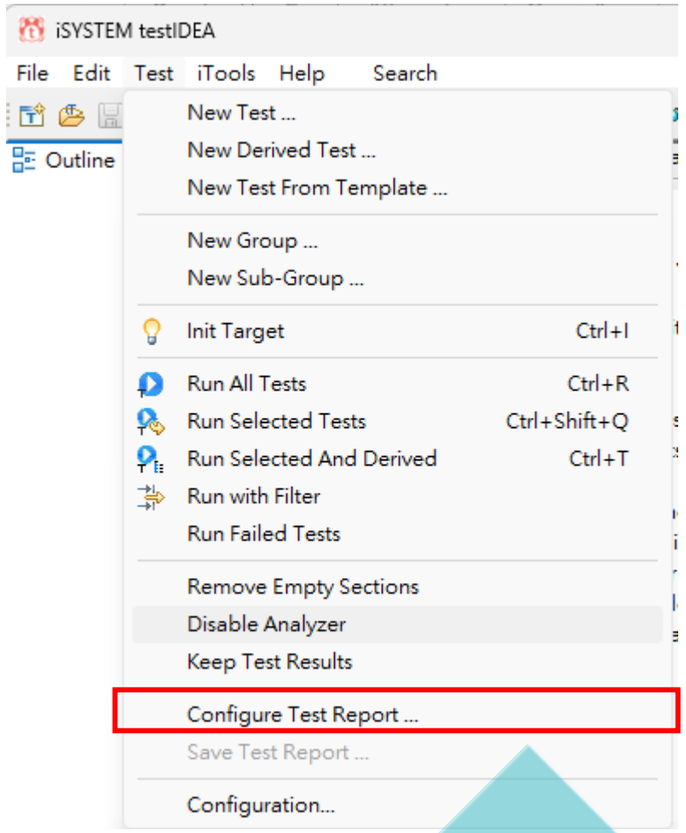
testIDEA

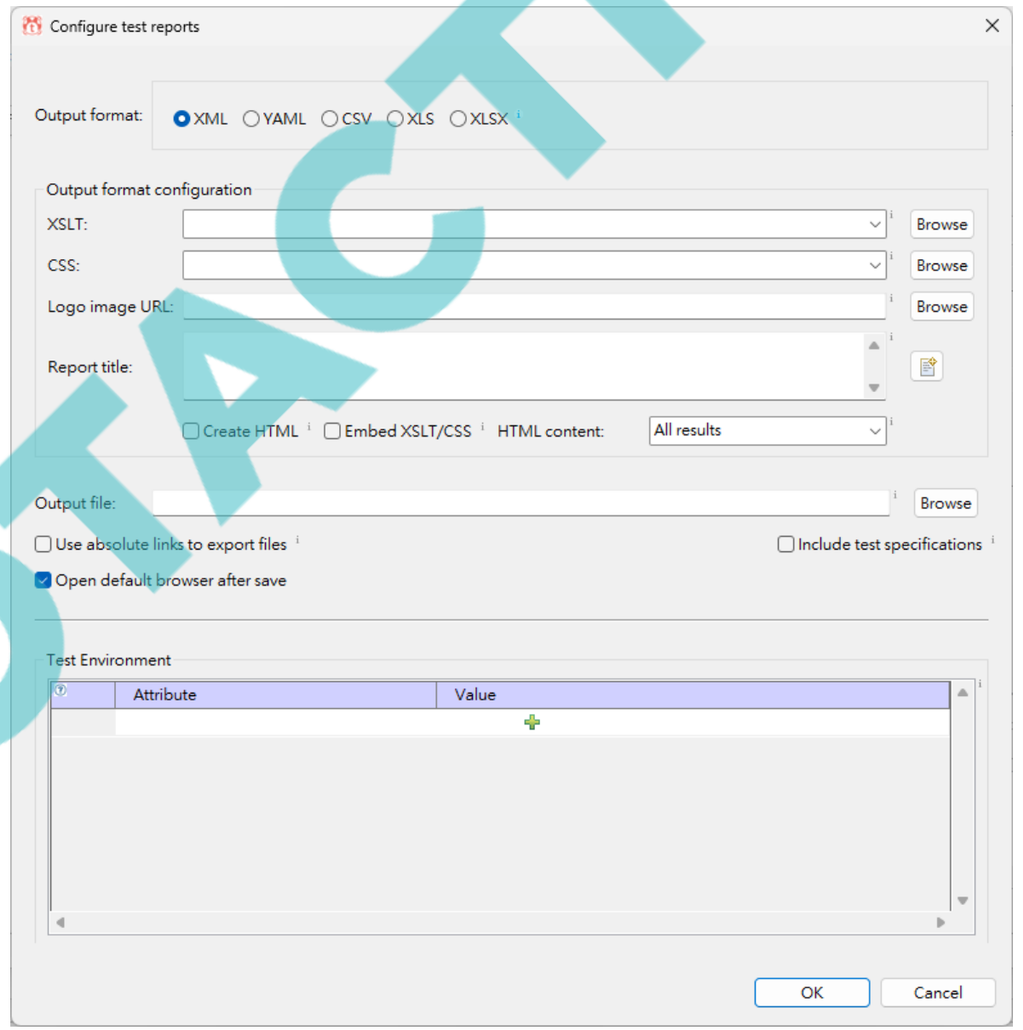




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testIDEA – 配置測試報告模板

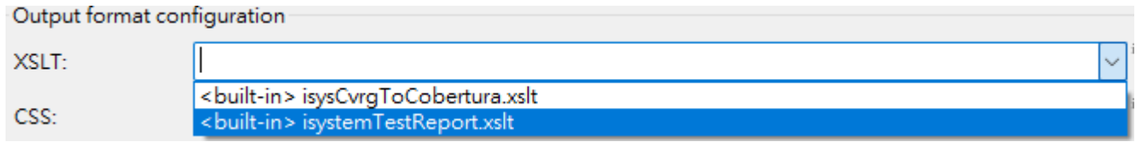




JOTACTIC

testIDEA – 配置測試報告模板

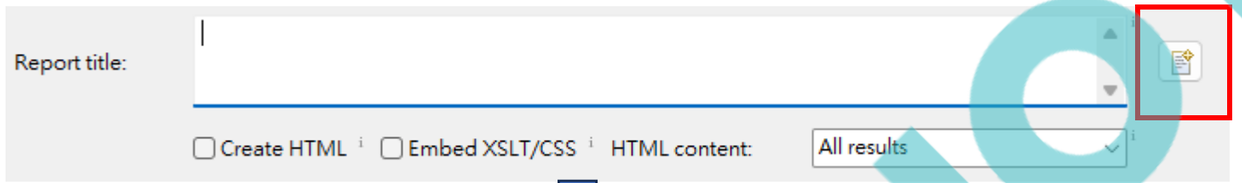
1.

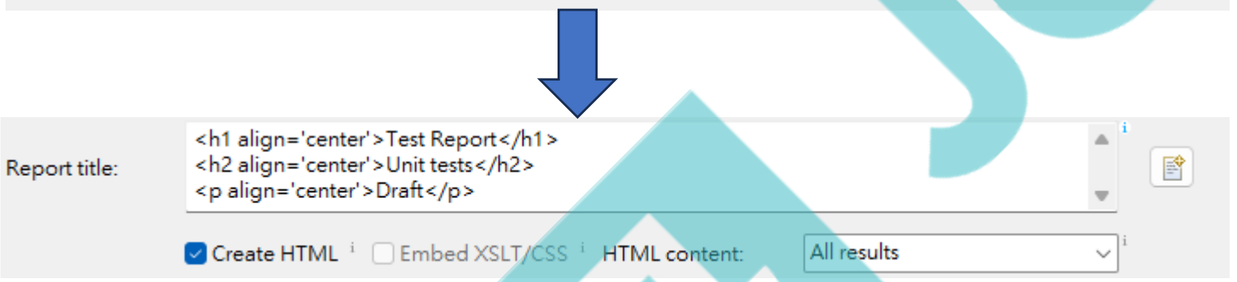


2.



3.





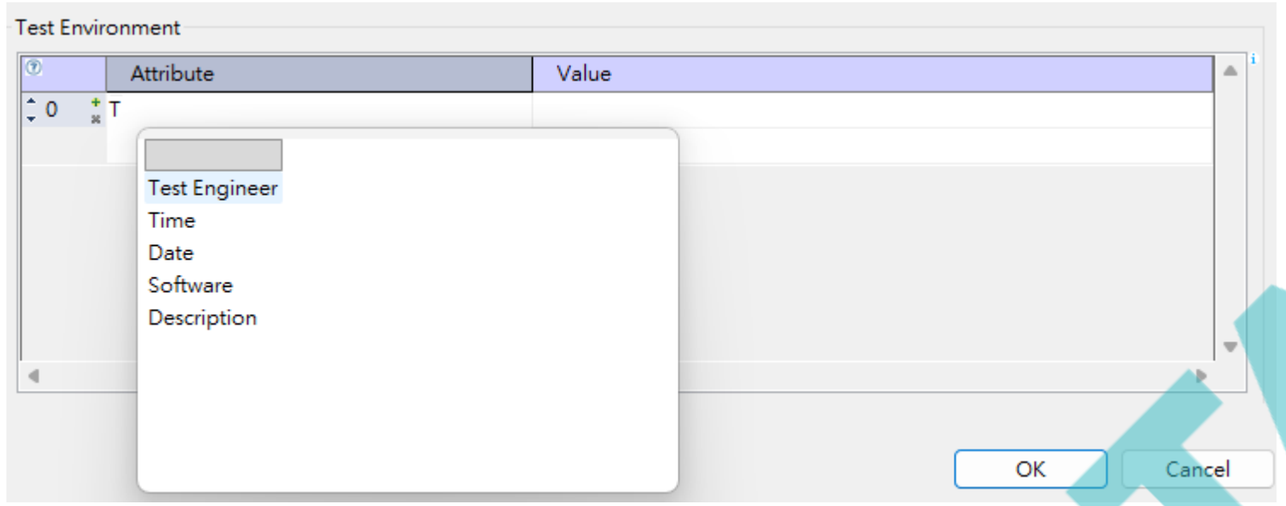
4.

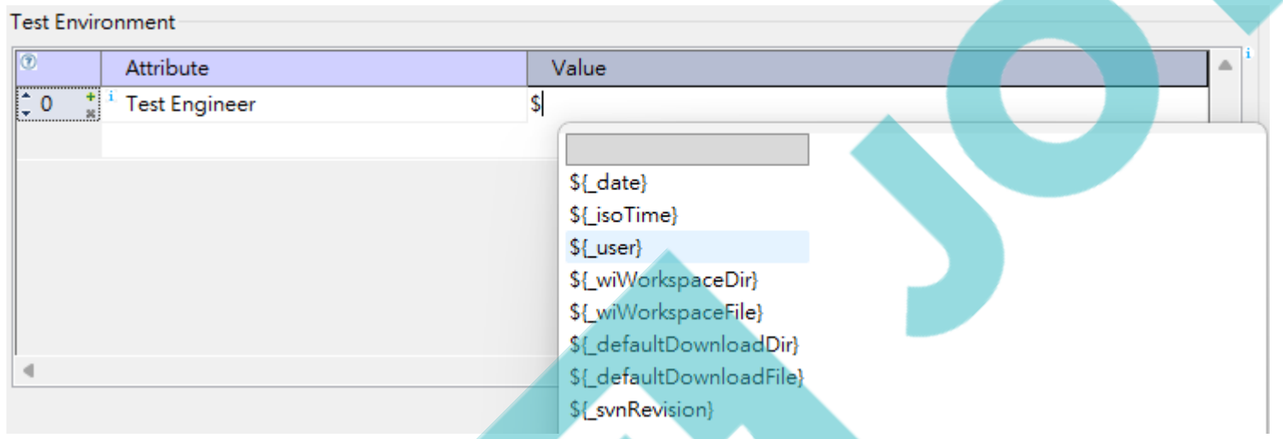


此頁的配置僅供參考，相關的欄位參數配置請根據需求調整並參考testIDEA手冊

JOTACTIC

testIDEA – 配置測試報告模板

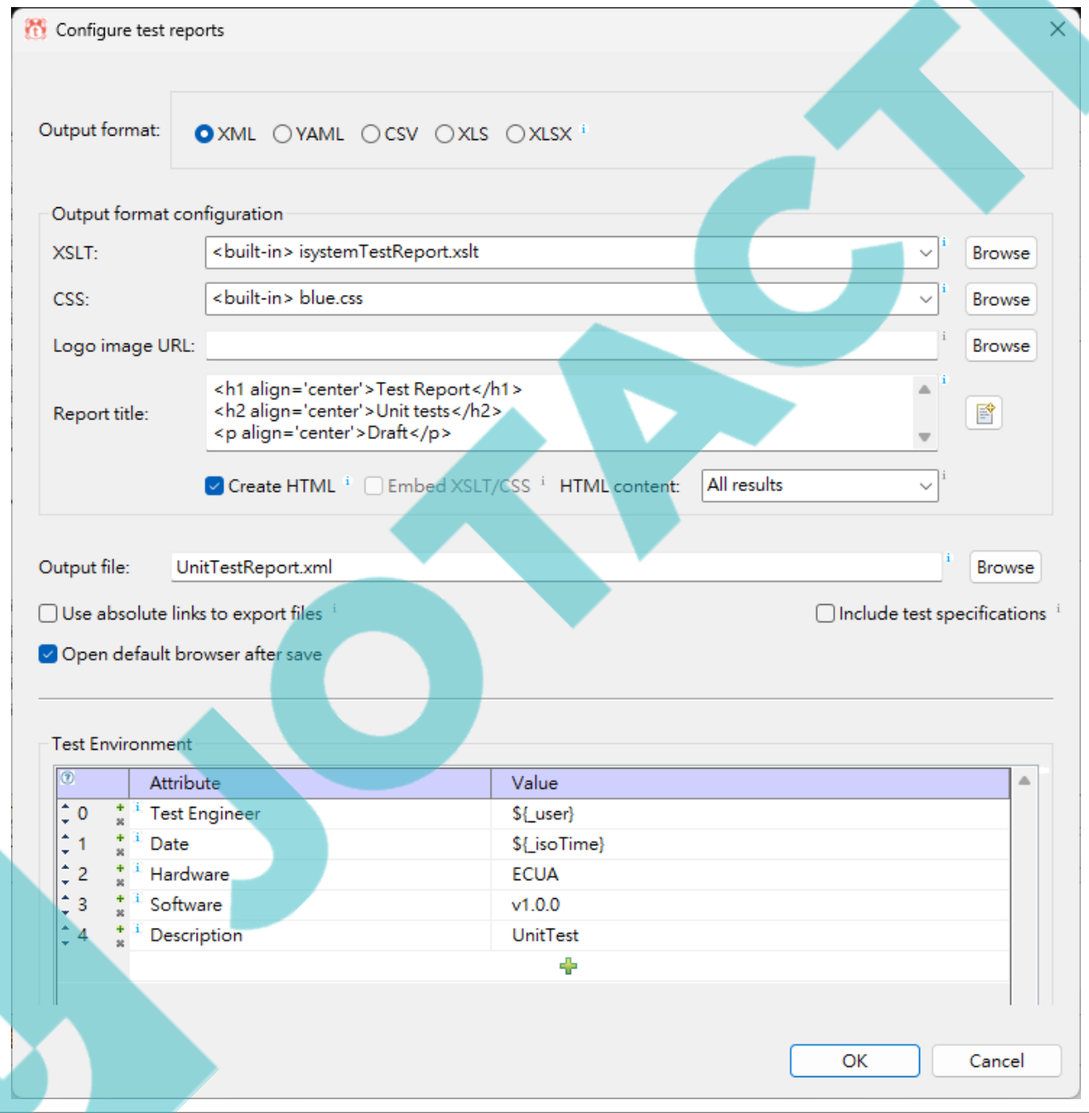




此頁的配置僅供參考，相關的欄位參數配置請根據需求調整並參考testIDEA手冊

JOTACTIC

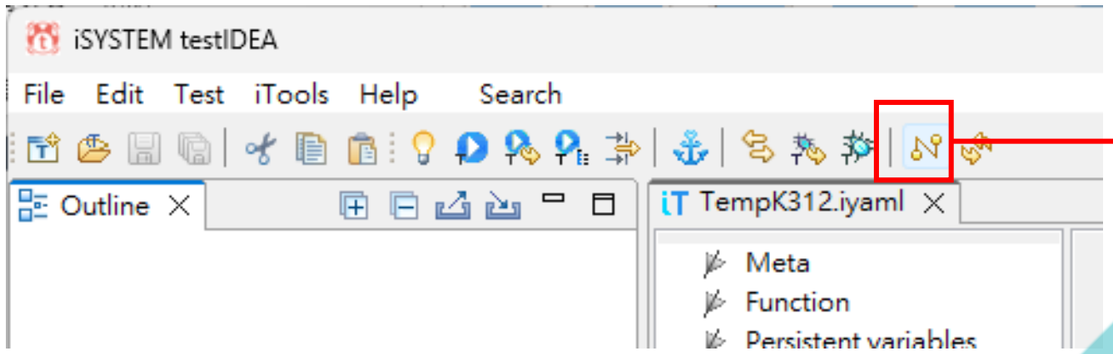
testIDEA – 配置測試報告模板

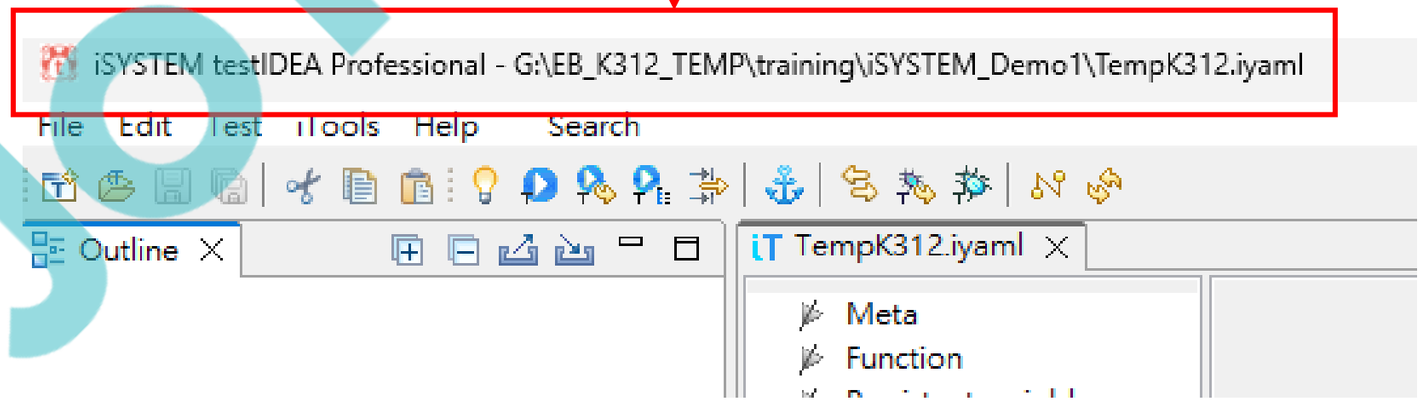


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testIDEA – 測試環境設定

testIDEA需要先與winIDEA環境做連接，來獲取相關的待測目標資訊

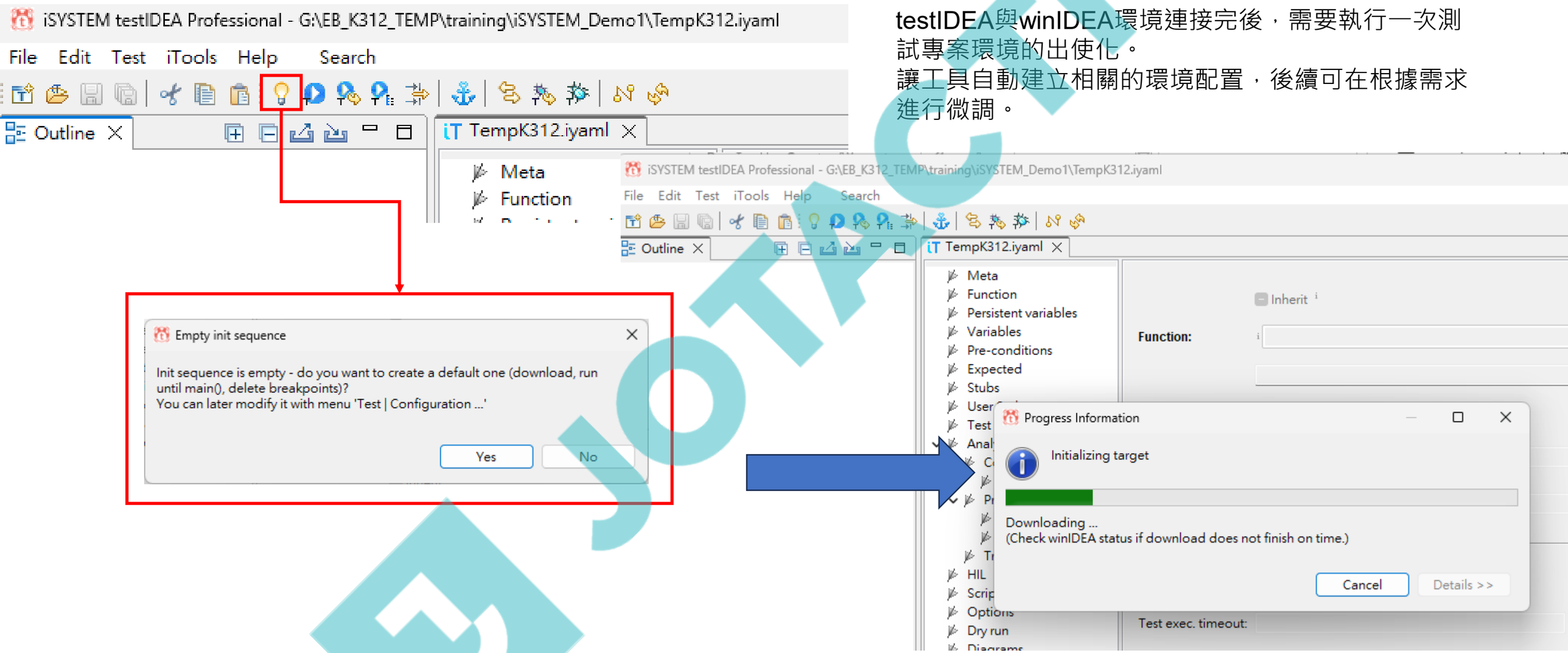




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testIDEA – 測試環境設定

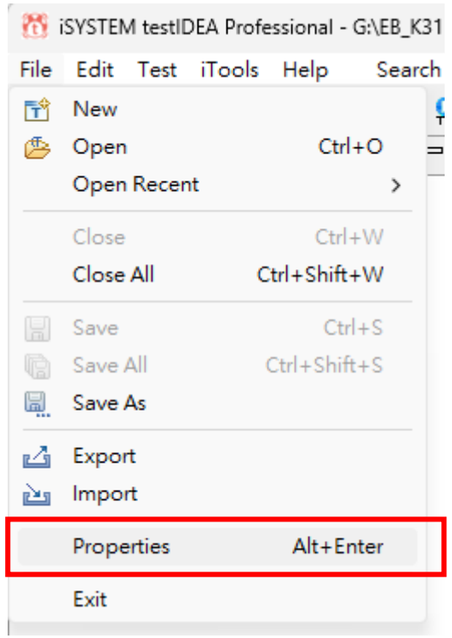
testIDEA與winIDEA環境連接完後，需要執行一次測試專案環境的出使化。
讓工具自動建立相關的環境配置，後續可在根據需求進行微調。

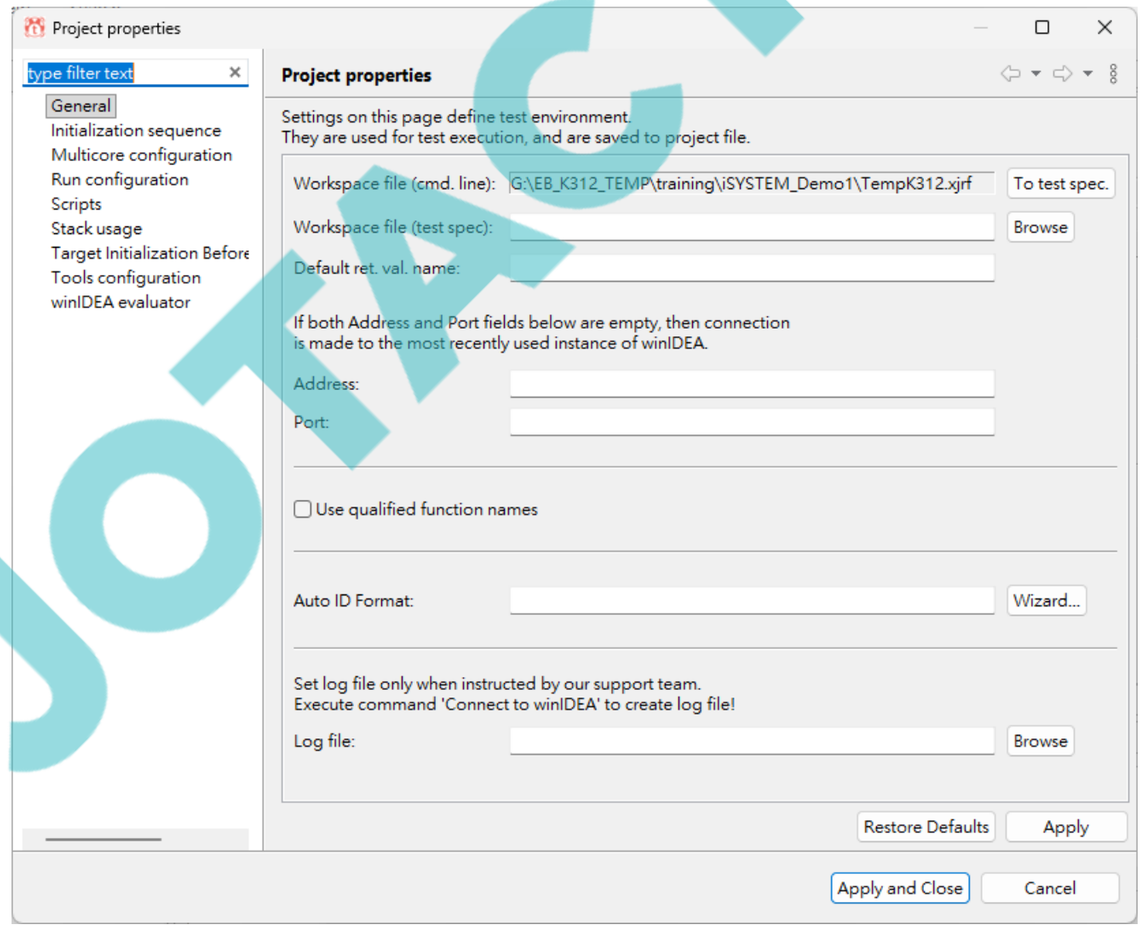


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testIDEA – 測試環境設定

專案的環境參數配置請參考testIDEA手冊並根據需求進行調整

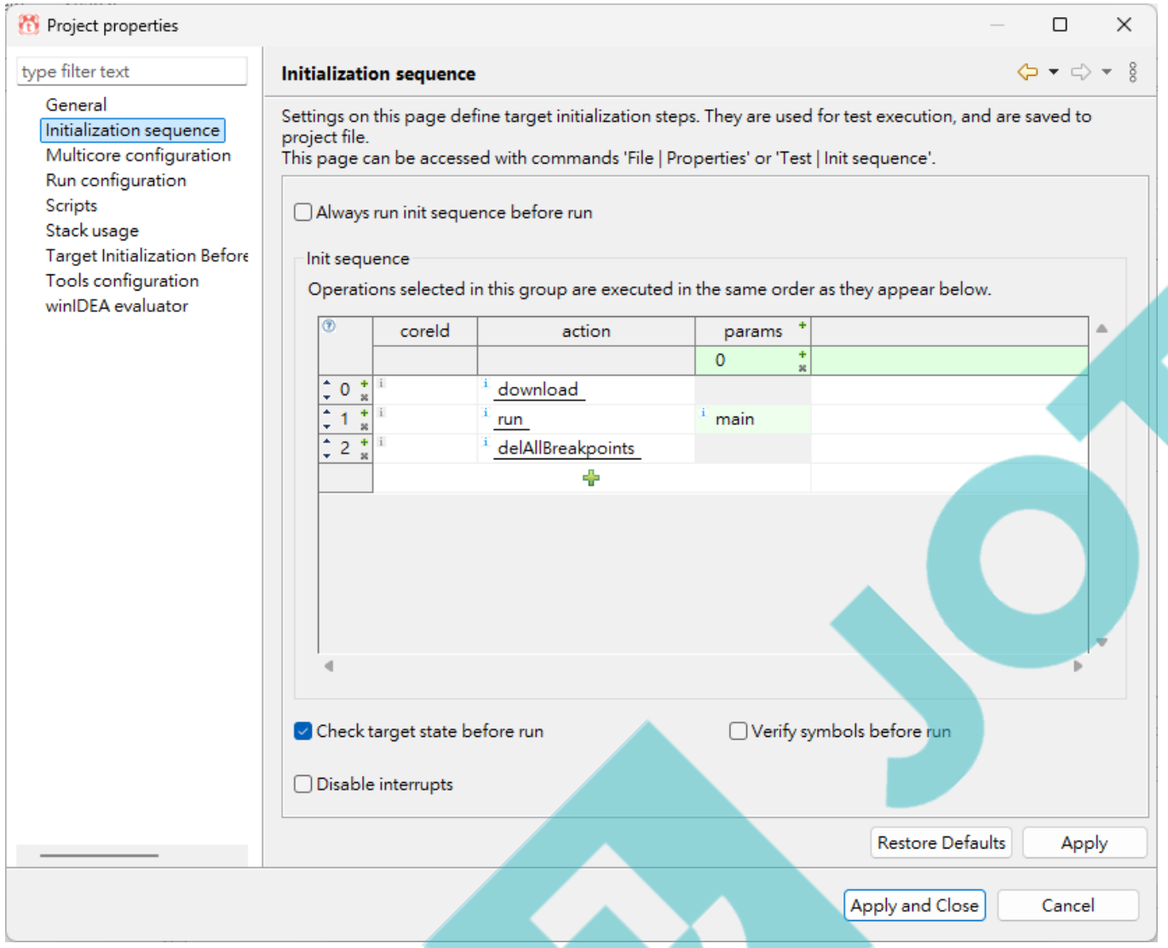


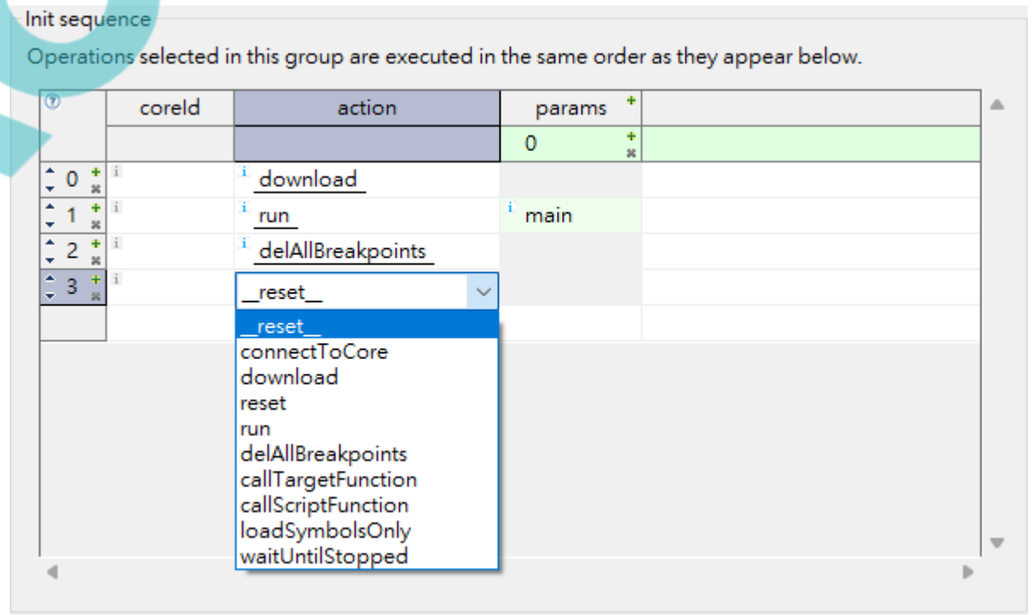


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testIDEA – 測試環境設定

根據需求在每次執行測試時執行相關的出使化的流程

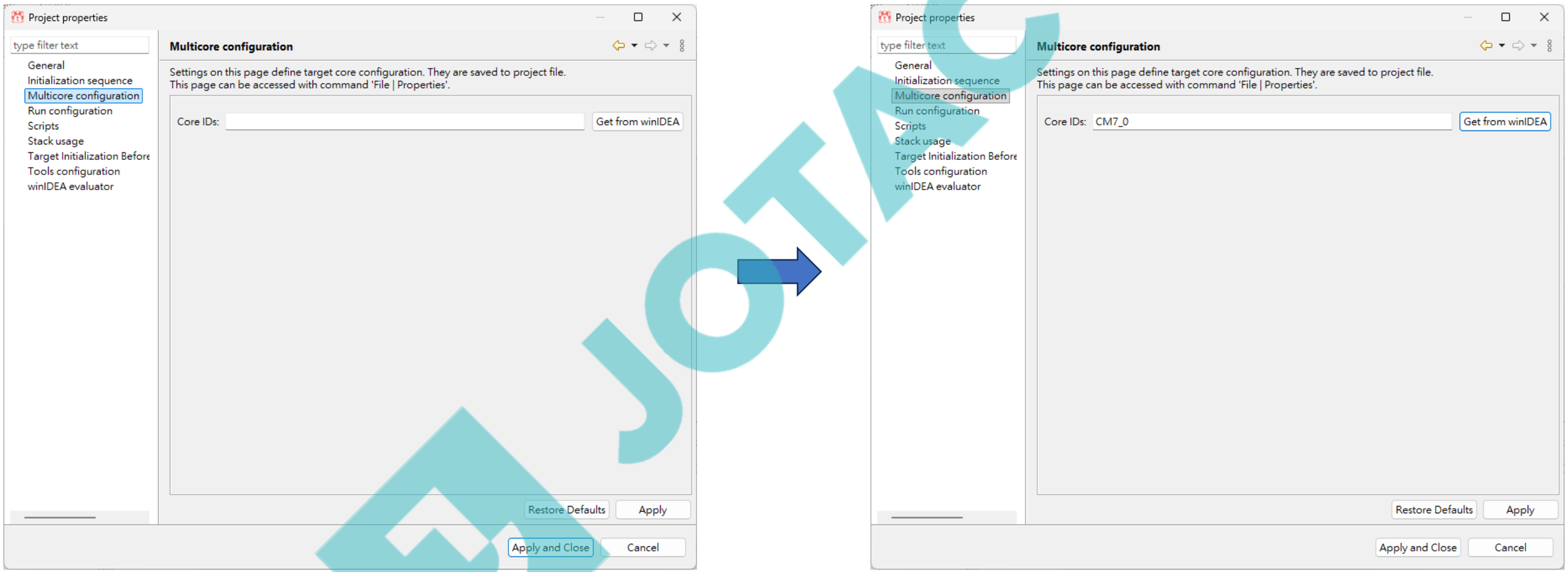




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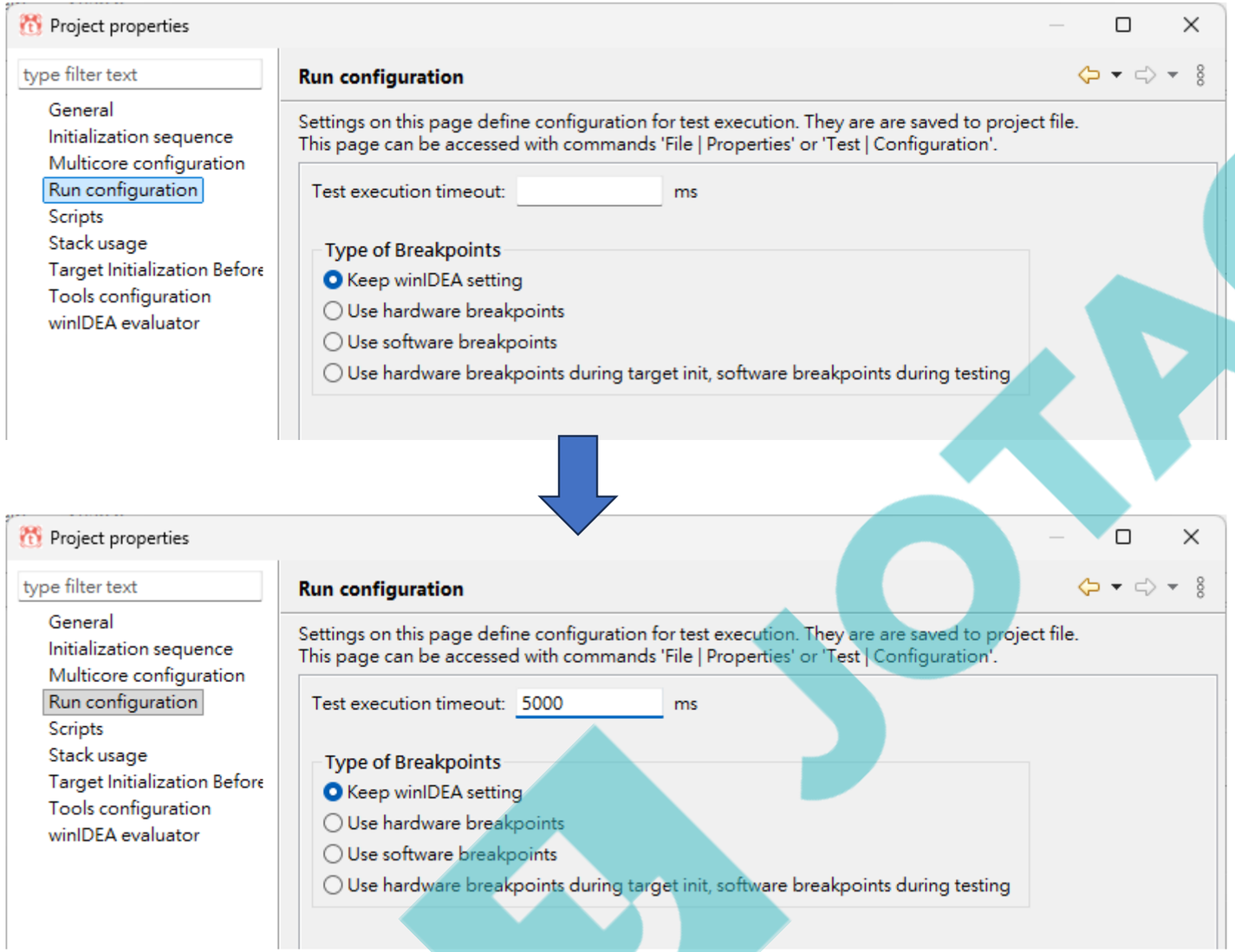
testIDEA – 測試環境設定

核心配置可能會因為目標晶片而有所不同



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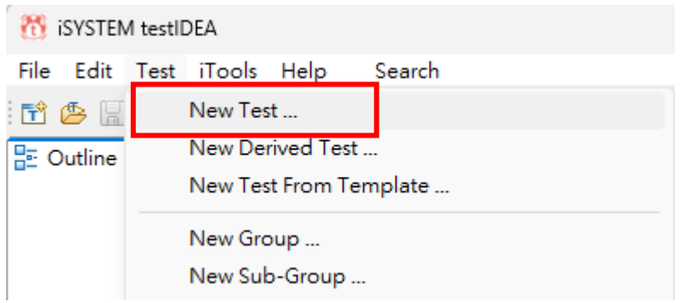
testIDEA – 測試環境設定

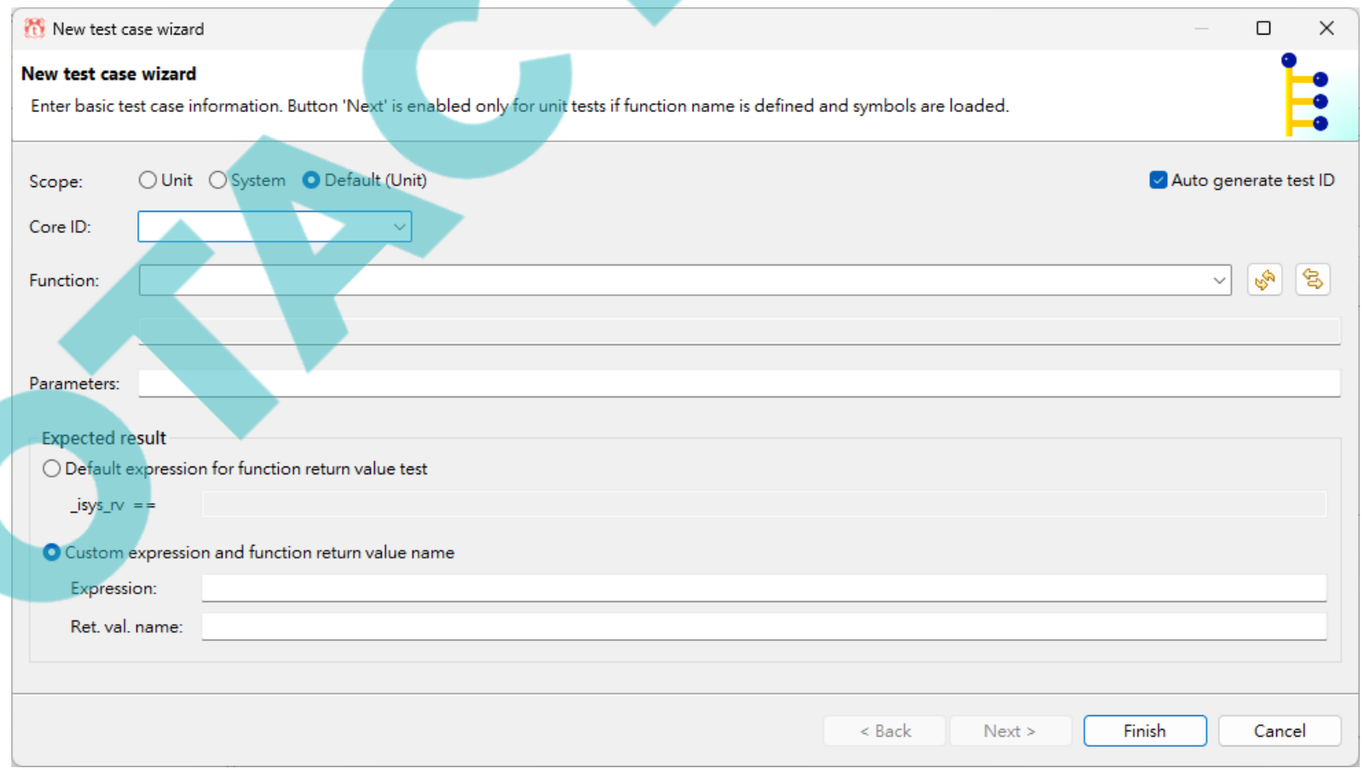


可設置測試案例執行時的 TimeOut 時間，
也可以在每一個測試案例上單獨設定 TimeOut 時間

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testIDEA – 創建測試案例

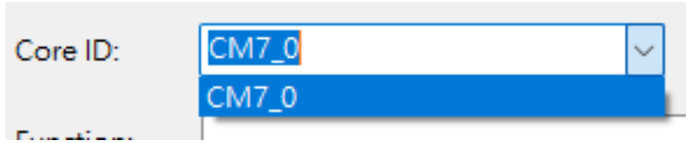




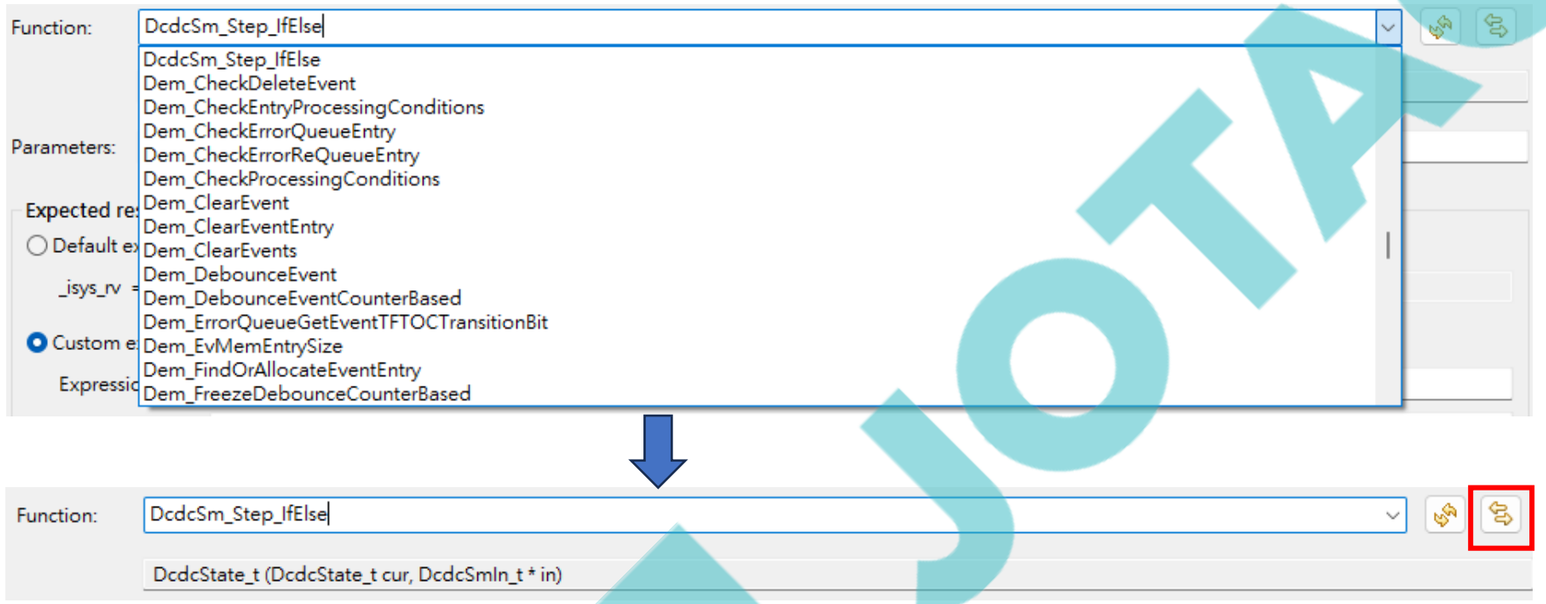
JOTACTIC

testIDEA – 創建測試案例

1.



2.



點擊此按鈕winIDEA會自動轉跳目標Function

```c
DcdcSm_Step_IfElse.c
#include "DcdcSm.h"

DcdcState_t DcdcSm_Step_IfElse(DcdcState_t cur, const DcdcSmIn_t* in)
{
  if (in == 0) { return cur; }

  /* 全域最高優先權：故障 */
  if (in->fault)
  {
    return DCDC_FAULT;
  }

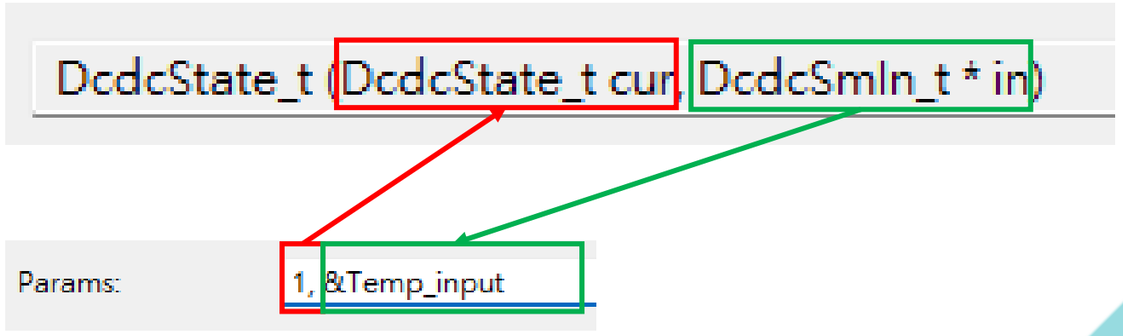
  /* 從 FAULT 離開的條件 */
  if (cur == DCDC_FAULT)
  {
    if ((in->fault == 0) && (in->fault_clr))
    {
      return DCDC_IDLE;
    }
    return DCDC_FAULT;
  }

  if (cur == DCDC_SLEEP)
  {
    if (in->wakeup && in->enable)
    {
      return DCDC_IDLE;
    }
    return DCDC_SLEEP;
  }
  else if (cur == DCDC_IDLE)
  {
```

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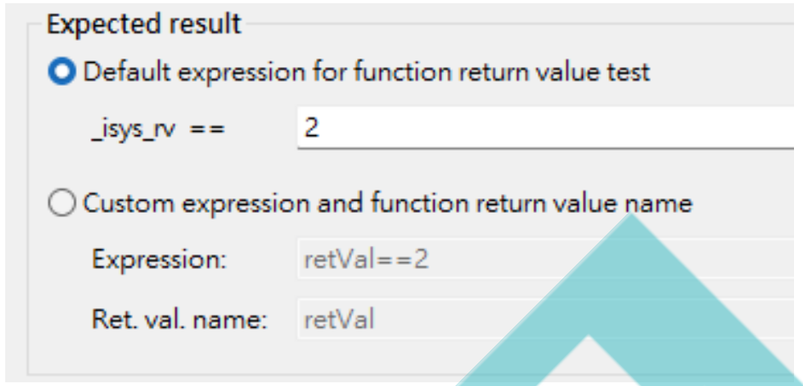
testIDEA – 創建測試案例

3.

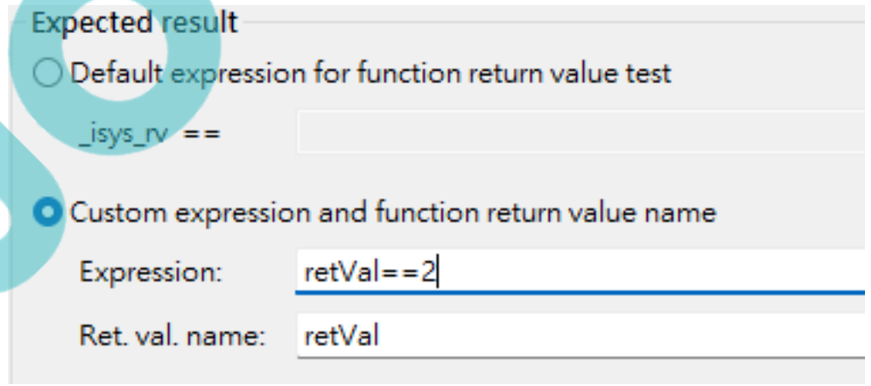


Params 則是根據待測 Function 傳入值來做設定，
有多個傳入值請用 “,”分隔，如傳入值為Point形式則使用”&”

4.



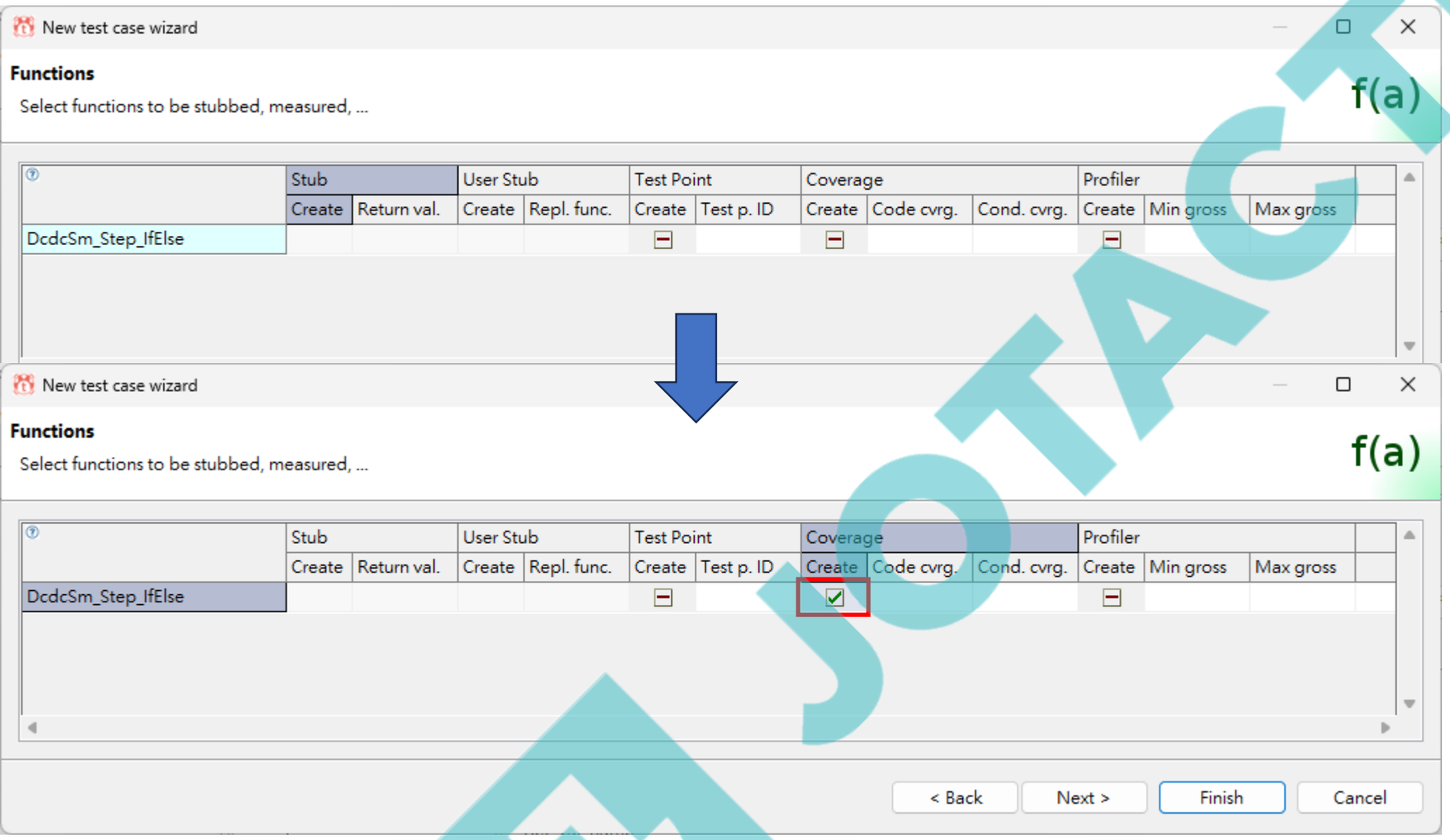
or



設定Function的返回期望值以及返回值的名稱，
也可使用testIDEA的default返回名稱

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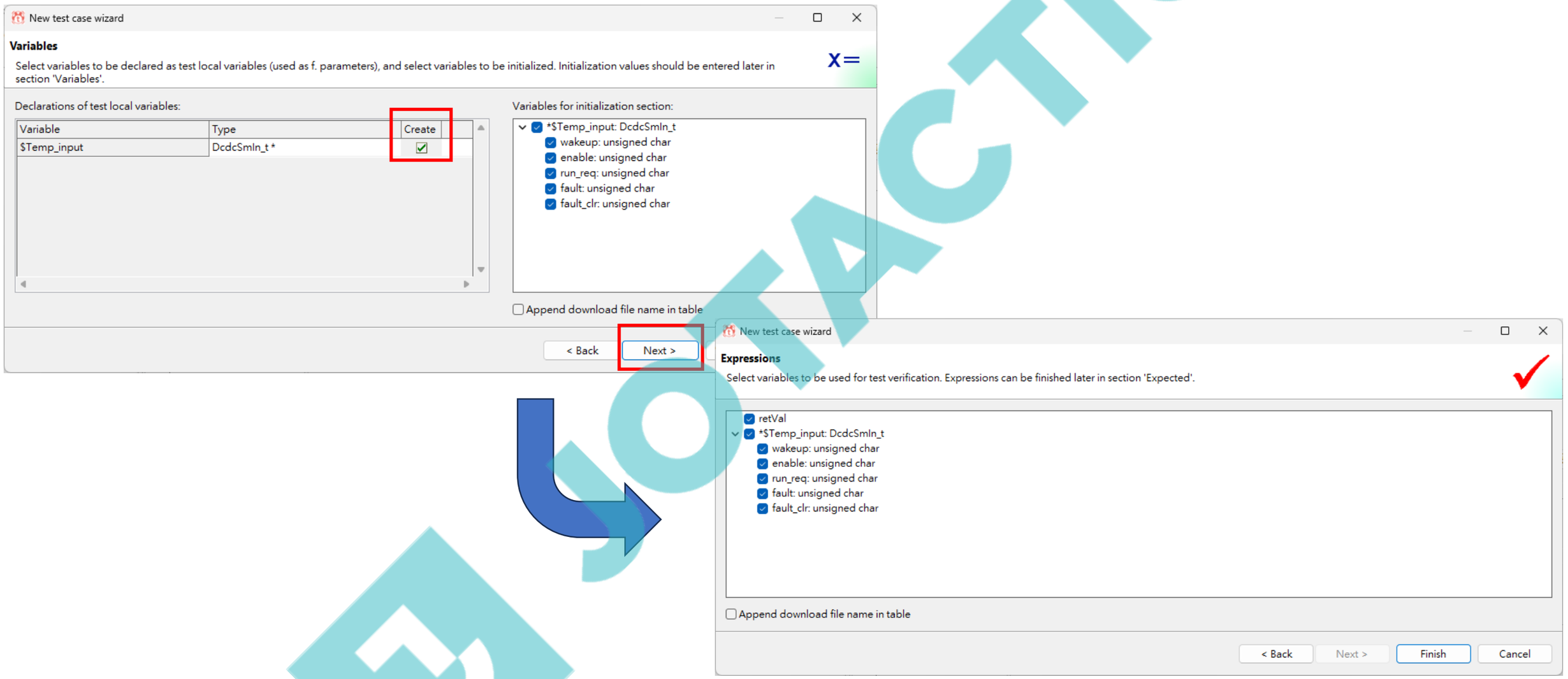
testIDEA – 創建測試案例



此頁可配置項目會因待測Function而有所不同，請根據需求並參考winIDEA、testIDEA進行調整

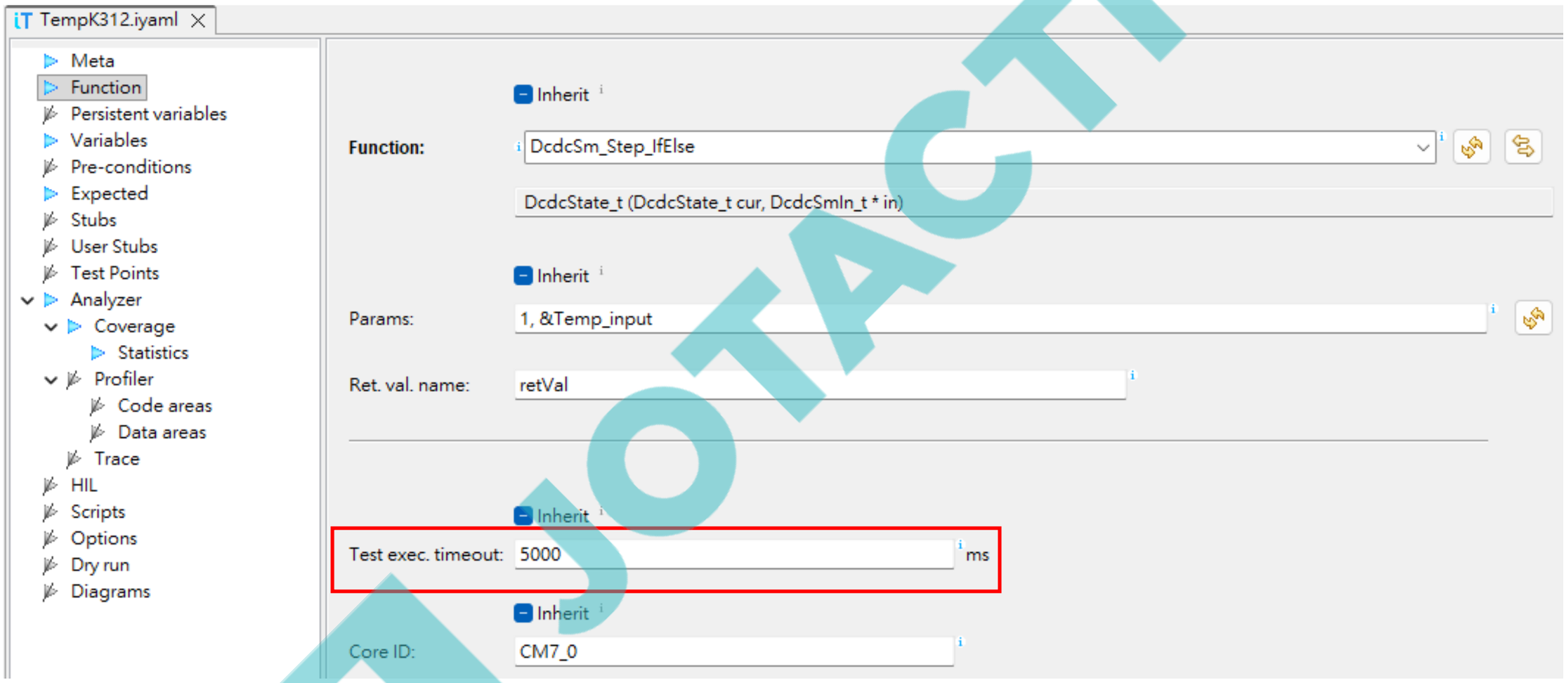
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testIDEA – 創建測試案例



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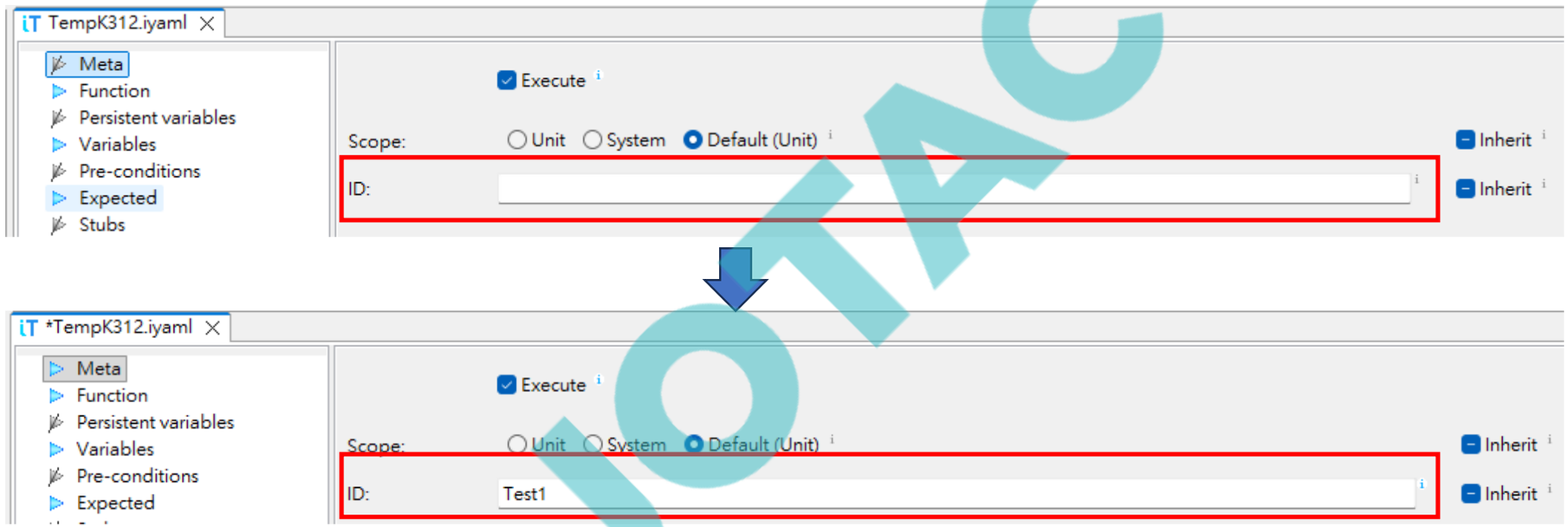
testIDEA – 創建測試案例



※ 設定測試案例執行的TimeOut時間

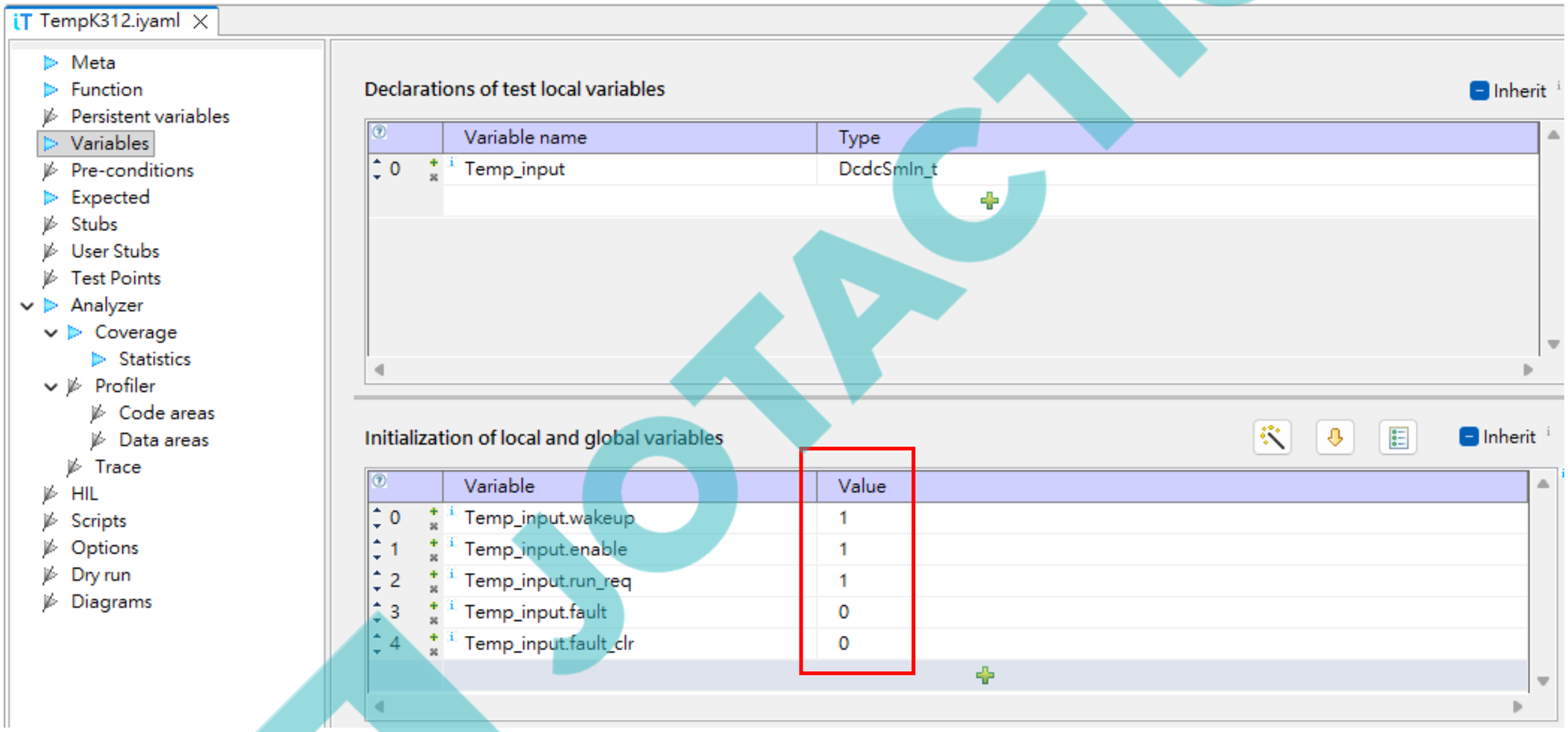
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testIDEA – 創建測試案例



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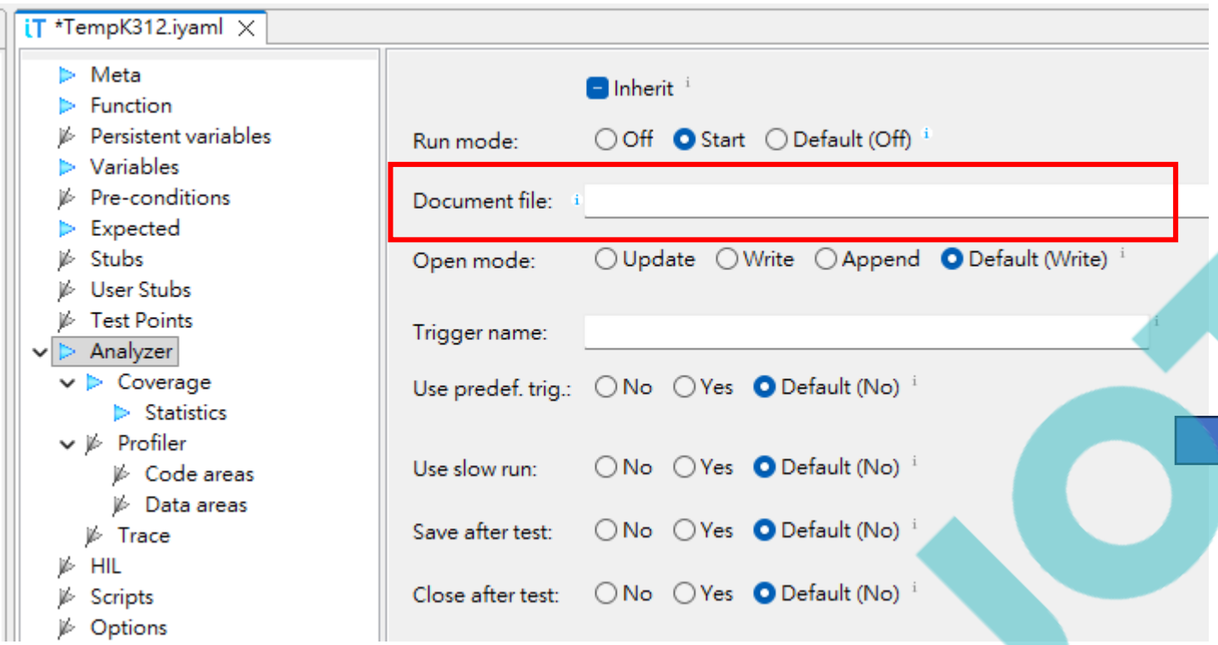
testIDEA – 創建測試案例

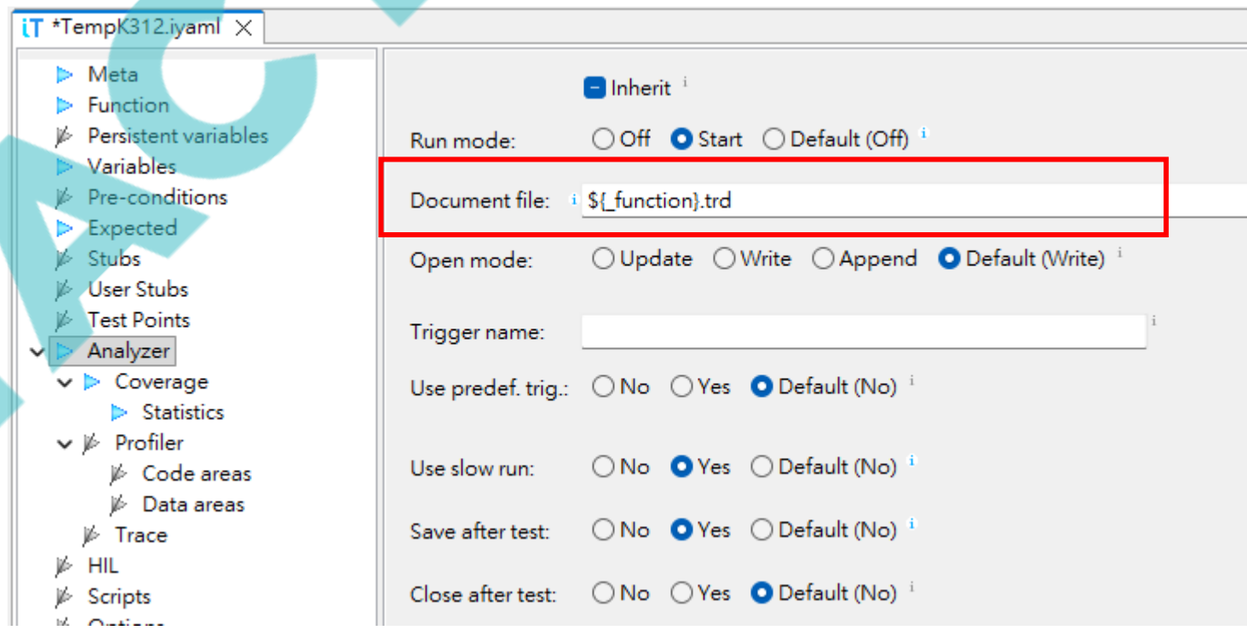


※ 請記得調整對待測function要注入的測試數值，對於前面自動創建流程的Point變數需要到此設定初始化數值

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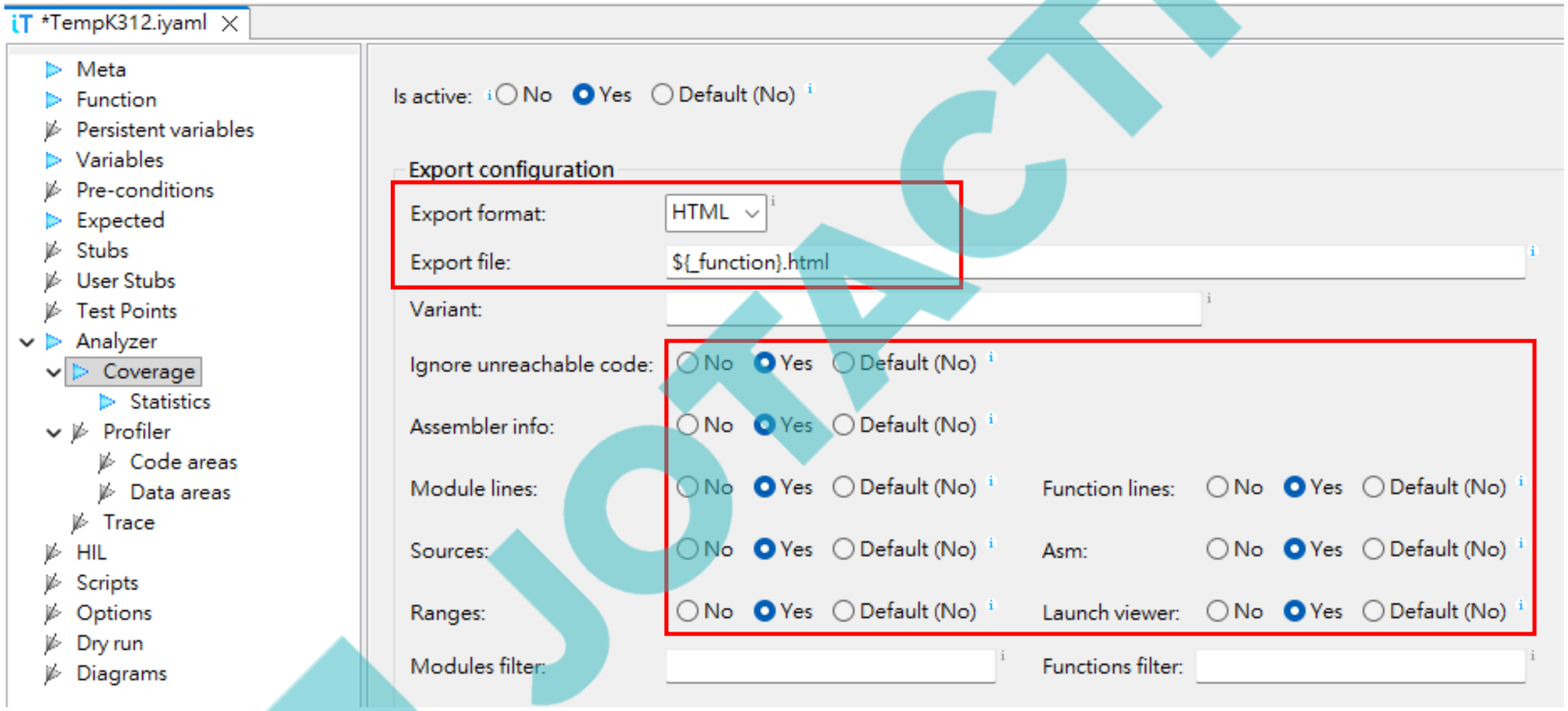
testIDEA – 創建測試案例





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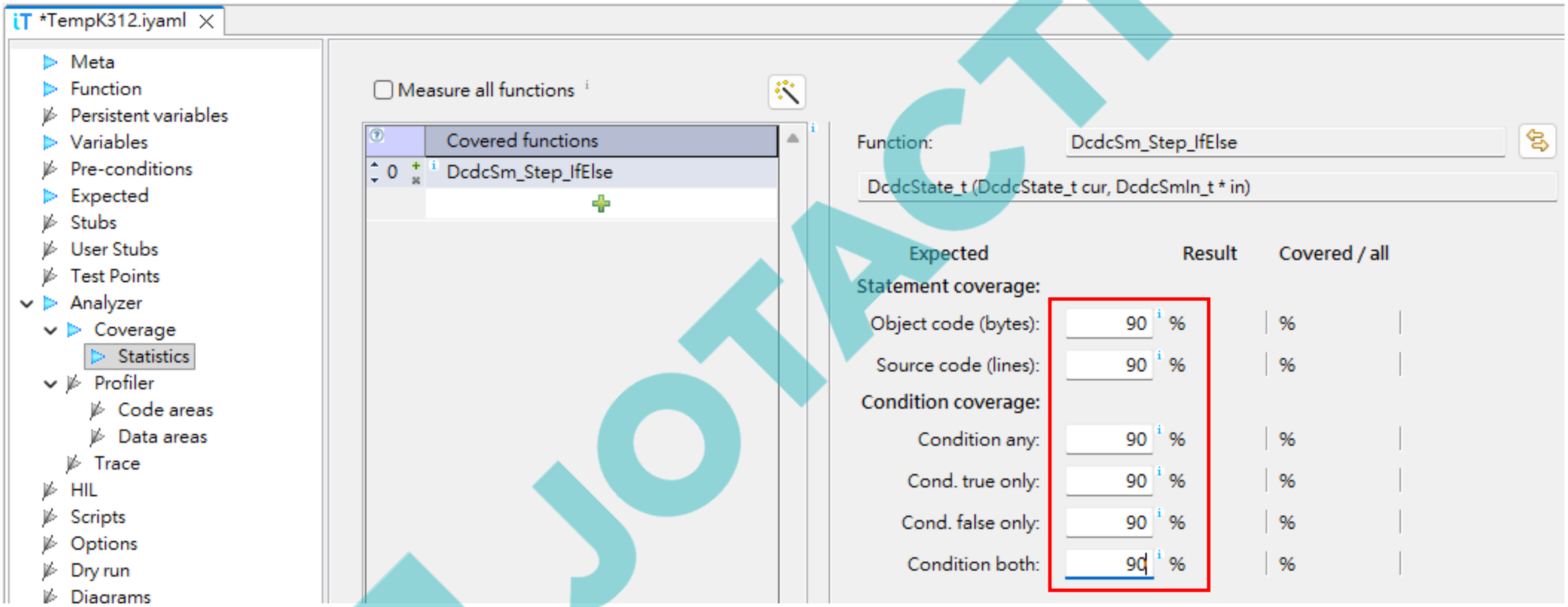
testIDEA – 創建測試案例



※ 設定Coverage導出得檔案格式與檔名
可調整Coverage驗證的相關參數，詳細請參考winIDEA、testIDEA手冊並根據需求進行調整

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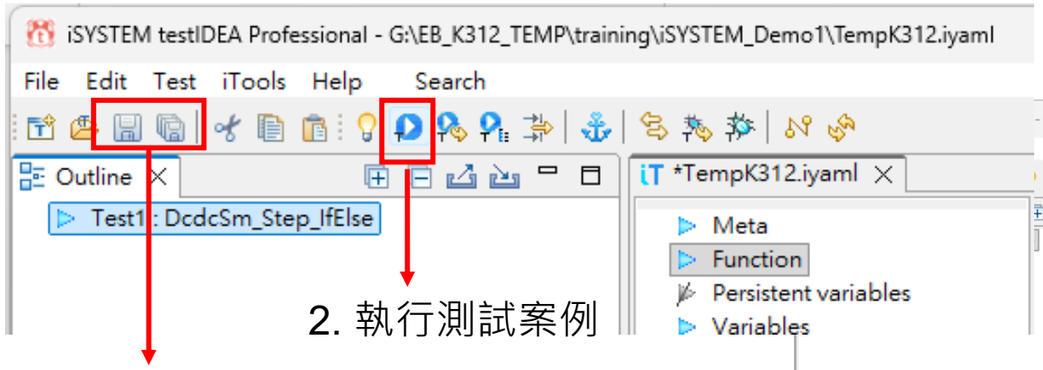
testIDEA – 創建測試案例



※ 可設定Coverage通過率，詳細請參考winIDEA、testIDEA手冊並根據需求進行調整

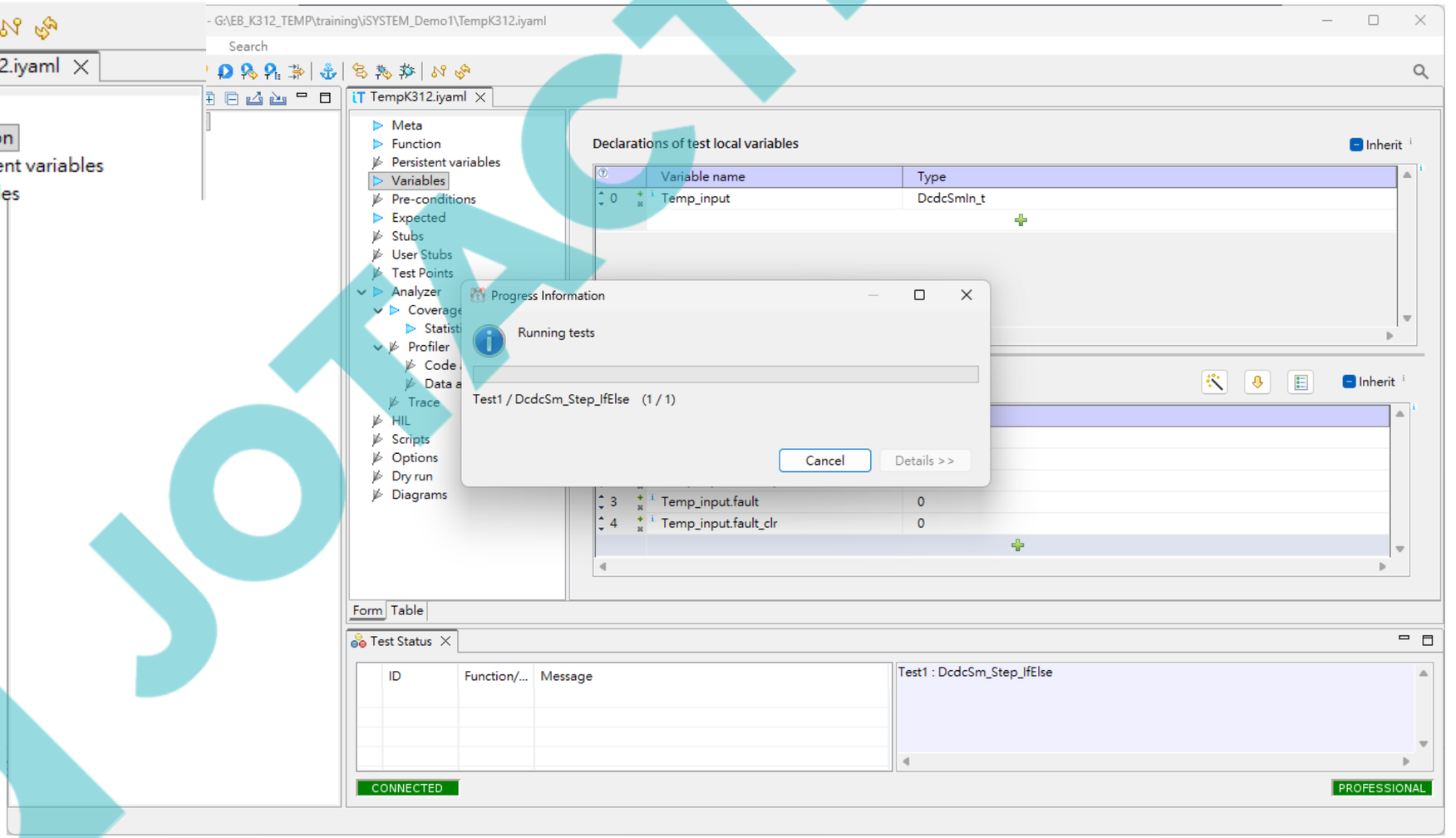
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testIDEA – 執行測試案例



2. 執行測試案例

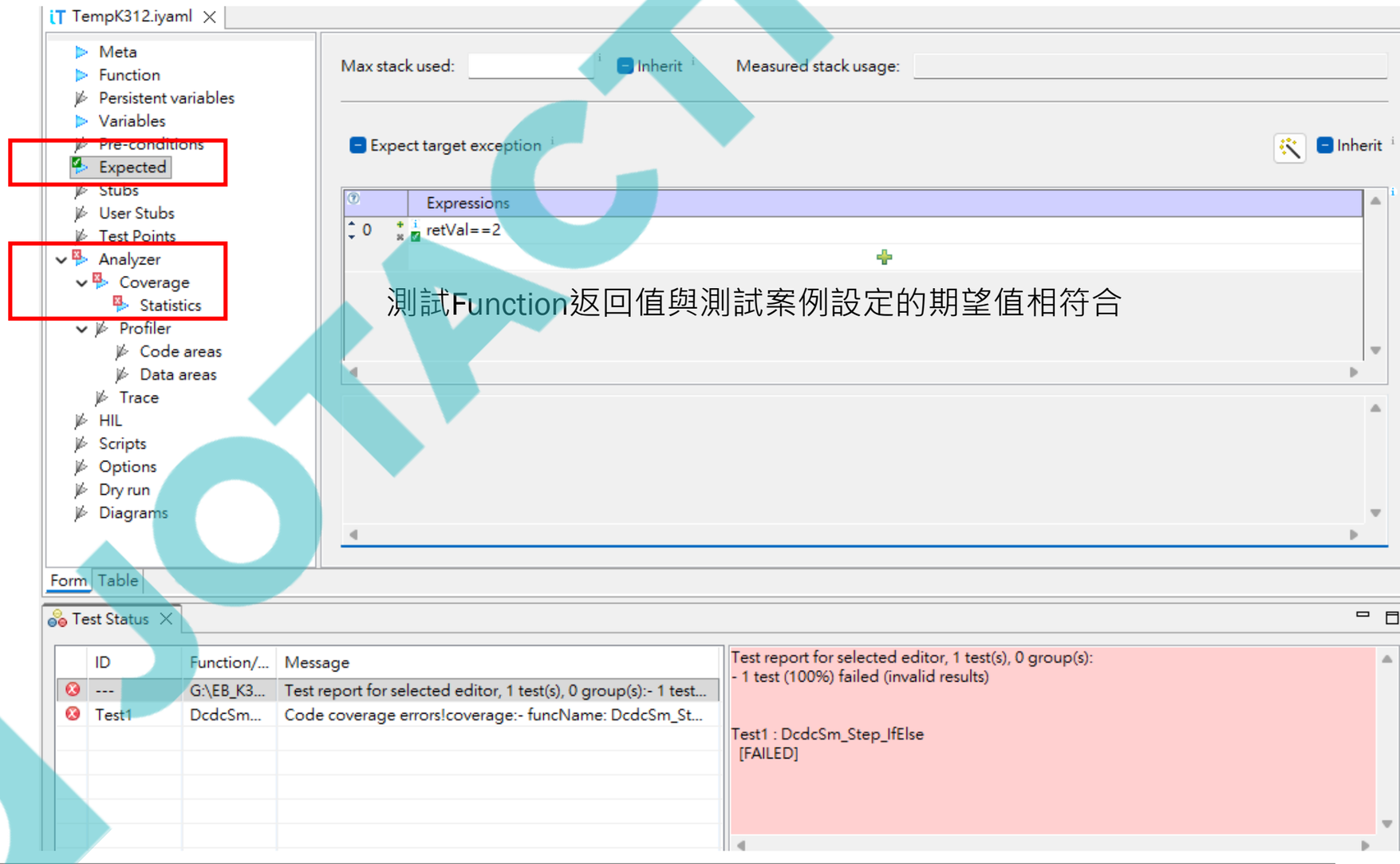
1. 建議做測試案例的保存



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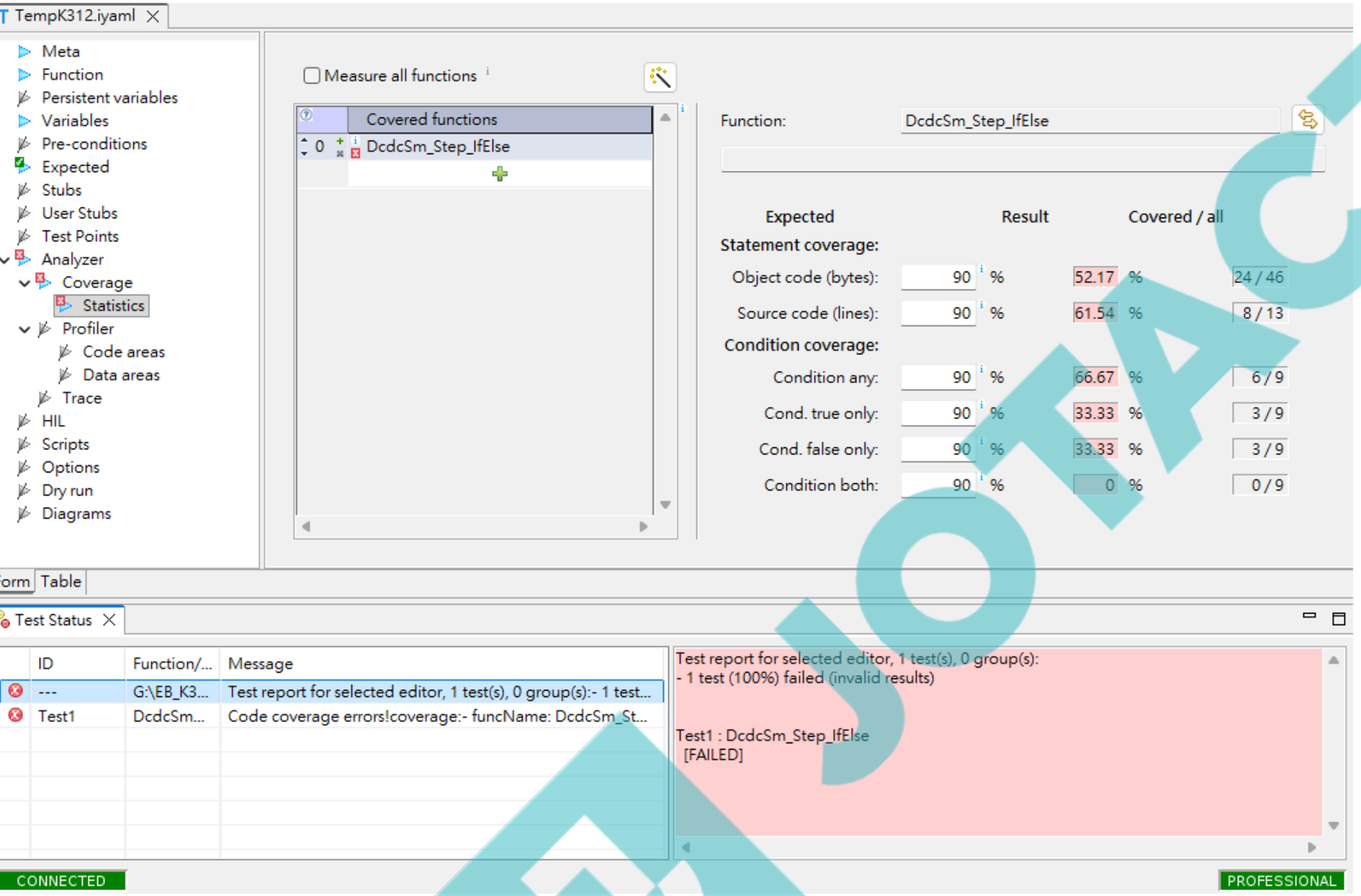
testIDEA – 創建測試案例

當測試案例執行完後，
項目中有出現綠色的代表通過設定條件。
紅色的代表有錯誤，相關錯誤需要查看
下方TestSatus視窗。



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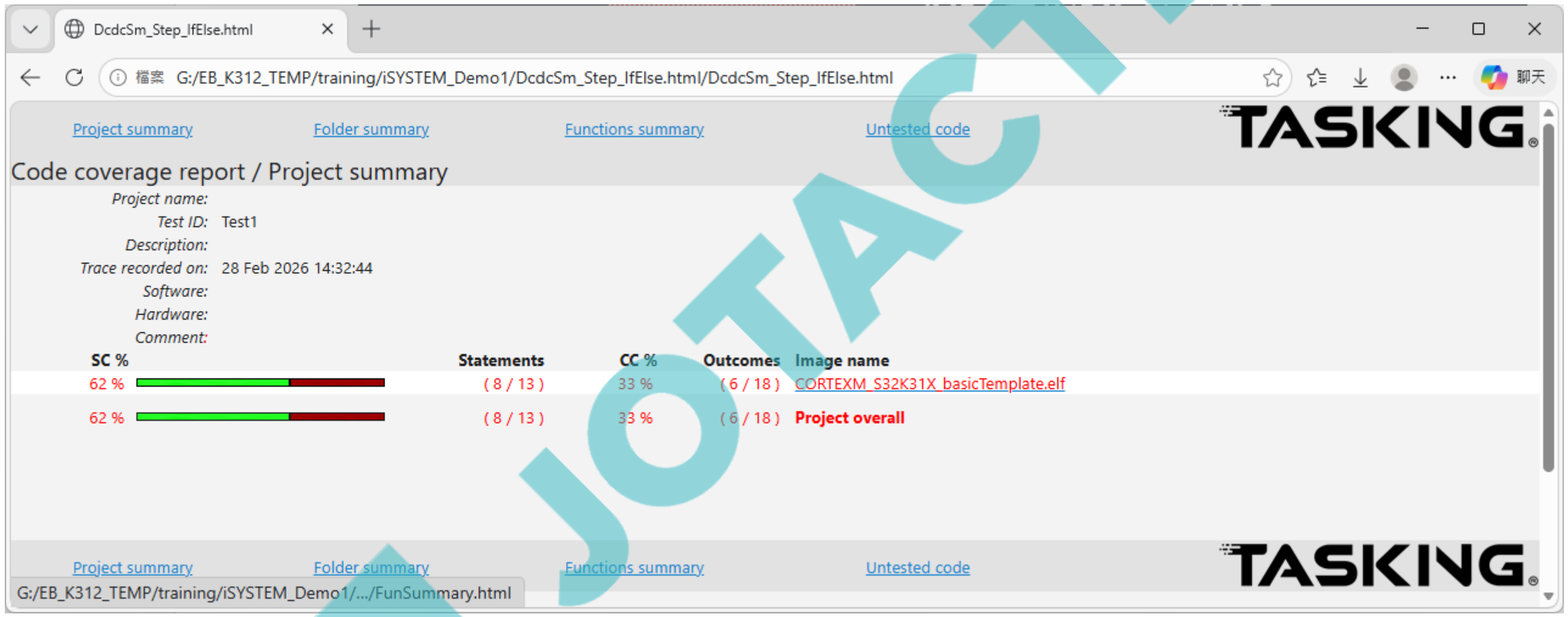
testIDEA – 創建測試案例



測試Function與測試案例設定的期望值相符合

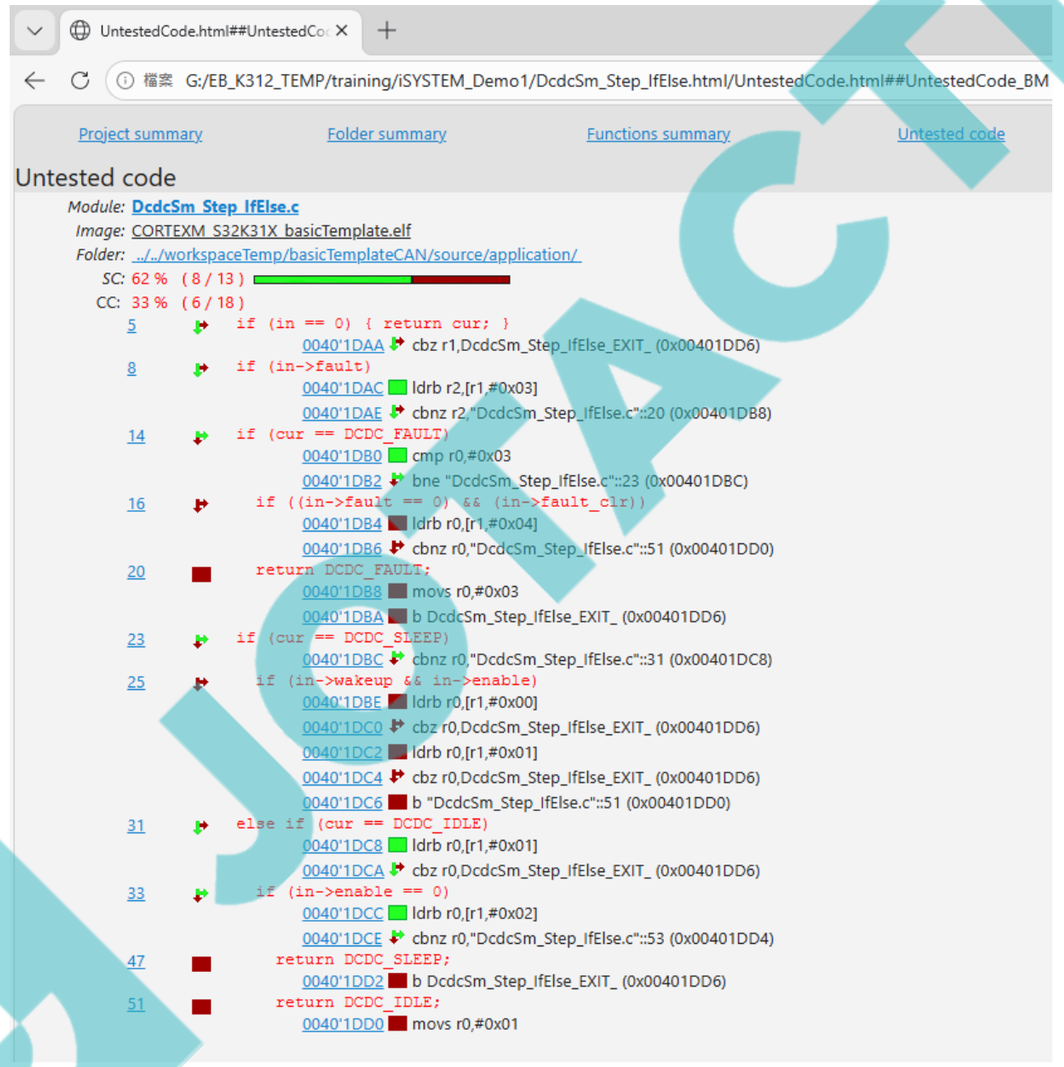
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testIDEA – 創建測試案例

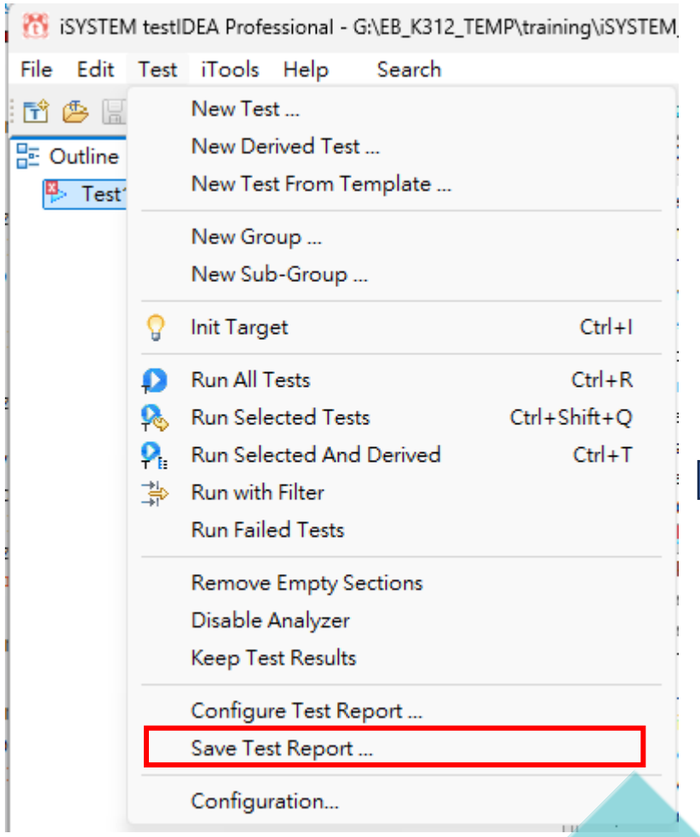


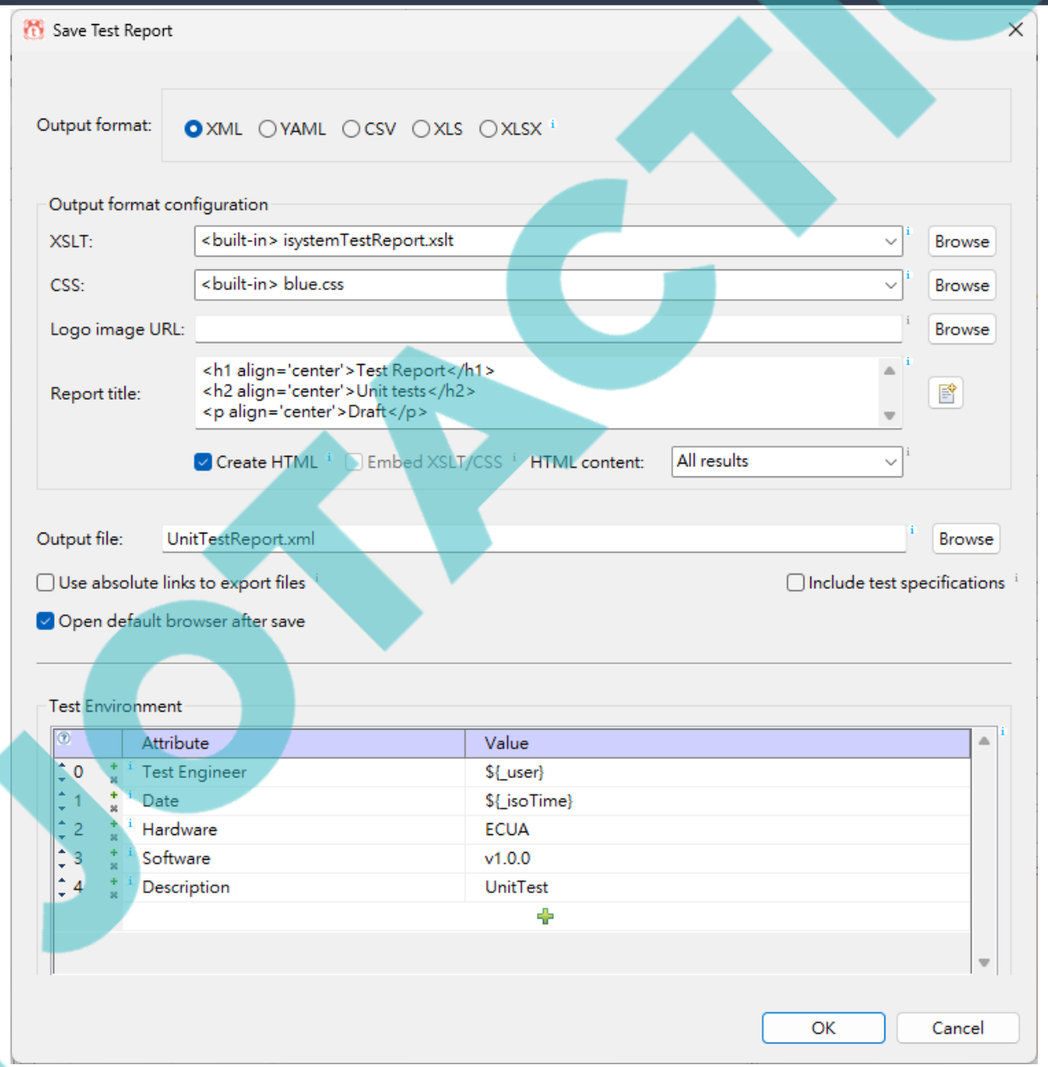
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testIDEA – 創建測試案例



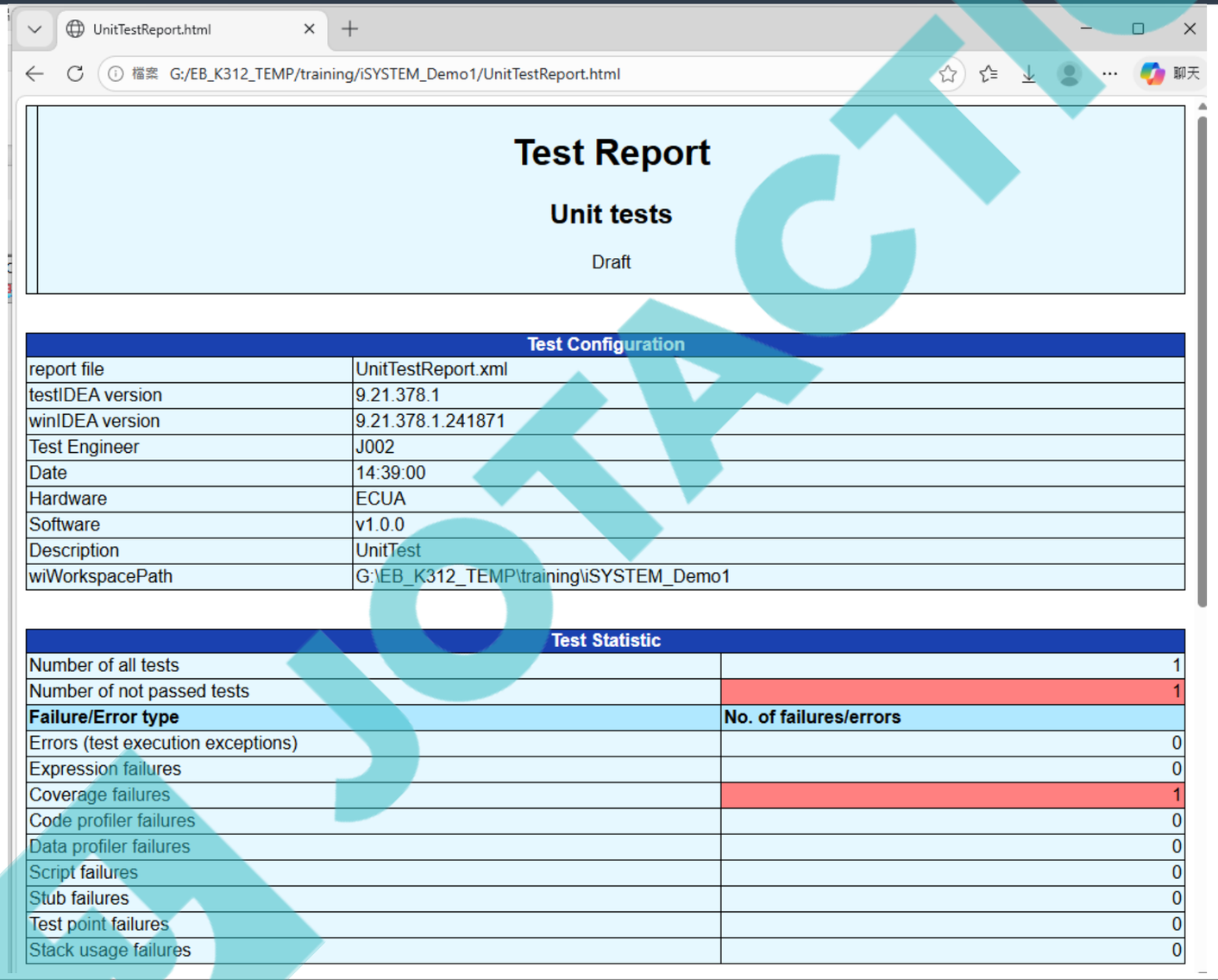
testIDEA – 導出測試報告





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testIDEA – 導出測試報告



Test Report

Unit tests

Draft

Test Configuration	
report file	UnitTestReport.xml
testIDEA version	9.21.378.1
winIDEA version	9.21.378.1.241871
Test Engineer	J002
Date	14:39:00
Hardware	ECUA
Software	v1.0.0
Description	UnitTest
wiWorkspacePath	G:\EB_K312_TEMP\training\iSYSTEM_Demo1

Test Statistic	
Number of all tests	1
Number of not passed tests	1
Failure/Error type	**No. of failures/errors**
Errors (test execution exceptions)	0
Expression failures	0
Coverage failures	1
Code profiler failures	0
Data profiler failures	0
Script failures	0
Stub failures	0
Test point failures	0
Stack usage failures	0

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testIDEA – 導出測試報告

Test Cases With Failures and Errors		
Test ID	**Function**	**Failure/Error**
Test1	DcdcSm_Step_IfElse	Coverage,

Test ID	Function	Result
Test1	DcdcSm_Step_IfElse	Fail
Assert expressions		
Expression	Sub-expressions	
retVal==2	retVal = 0x00000002 (2)	

Coverage

Document	DcdcSm_Step_IfElse.trd
Export file	DcdcSm_Step_IfElse.html\Index.html

Function	Obj. code all	Src. lines all	Cond. all	CC (Outcomes)	Obj. code executed measured (exp., abs.)	Src. lines executed measured (exp., abs.)	Conditions any measured (exp., abs.)	Cond. true only measured (exp., abs.)	Cond. false only measured (exp., abs.)	Conditions both measured (exp., abs.)
DcdcSm_Step_IfElse	46	13	9	33.3% (6/18)	**52.2%** (90.0%, 24)	**61.5%** (90.0%, 8)	**66.7%** (90.0%, 6)	**33.3%** (90.0%, 3)	**33.3%** (90.0%, 3)	**0.0%** (90.0%, 0)

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- 創建 iSYSTEM 專案
- Debug 功能介紹
- Debug 介面設定
- Architecture-specific plugins
- daqIDEA
- Tasks Window
- testIDEA
- Other – coverage 結果疊加

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Other – coverage 結果疊加

測試案例從Test001一路直行到Test003，帶測目標Function的Coverage會根據測項進入的路徑，coverage覆蓋率也會有所上升。
藉此達到客戶所要求的coverage的整體通過率。

Test ID	Function	Result
Test001	DcdcSm_Step_IfElse	Pass

Assert expressions	
Expression	Sub-expressions
retval==2	retval = 0x00000002 (2)

Coverage										
Document	DcdcSm_Step_IfElse.trd									
Export file	DcdcSm_Step_IfElse.html\Index.html									
Function	Obj. code all	Src. lines all	Cond. all	CC (Outcomes)	Obj. code executed measured (exp., abs.)	Src. lines executed measured (exp., abs.)	Conditions any measured (exp., abs.)	Cond. true only measured (exp., abs.)	Cond. false only measured (exp., abs.)	Conditions both measured (exp., abs.)
DcdcSm_Step_IfElse	46	13	9	33.3% (6/18)	**52.2%** (0.0%, 24)	**61.5%** (0.0%, 8)	**66.7%** (0.0%, 6)	**33.3%** (0.0%, 3)	**33.3%** (0.0%, 3)	**0.0%** (0.0%, 0)

Test ID	Function	Result
Test002	DcdcSm_Step_IfElse	Pass

Tags	Base tests
	/Test001

Assert expressions	
Expression	Sub-expressions
retval==3	retval = 0x00000003 (3)

Coverage										
Document	DcdcSm_Step_IfElse.trd									
Export file	DcdcSm_Step_IfElse.html\Index.html									
Function	Obj. code all	Src. lines all	Cond. all	CC (Outcomes)	Obj. code executed measured (exp., abs.)	Src. lines executed measured (exp., abs.)	Conditions any measured (exp., abs.)	Cond. true only measured (exp., abs.)	Cond. false only measured (exp., abs.)	Conditions both measured (exp., abs.)
DcdcSm_Step_IfElse	46	13	9	38.9% (7/18)	**60.9%** (0.0%, 28)	**69.2%** (0.0%, 9)	**66.7%** (0.0%, 6)	**33.3%** (0.0%, 3)	**22.2%** (0.0%, 2)	**11.1%** (0.0%, 1)

Test ID	Function	Result
Test003	DcdcSm_Step_IfElse	Pass

Tags	Base tests
	/Test001

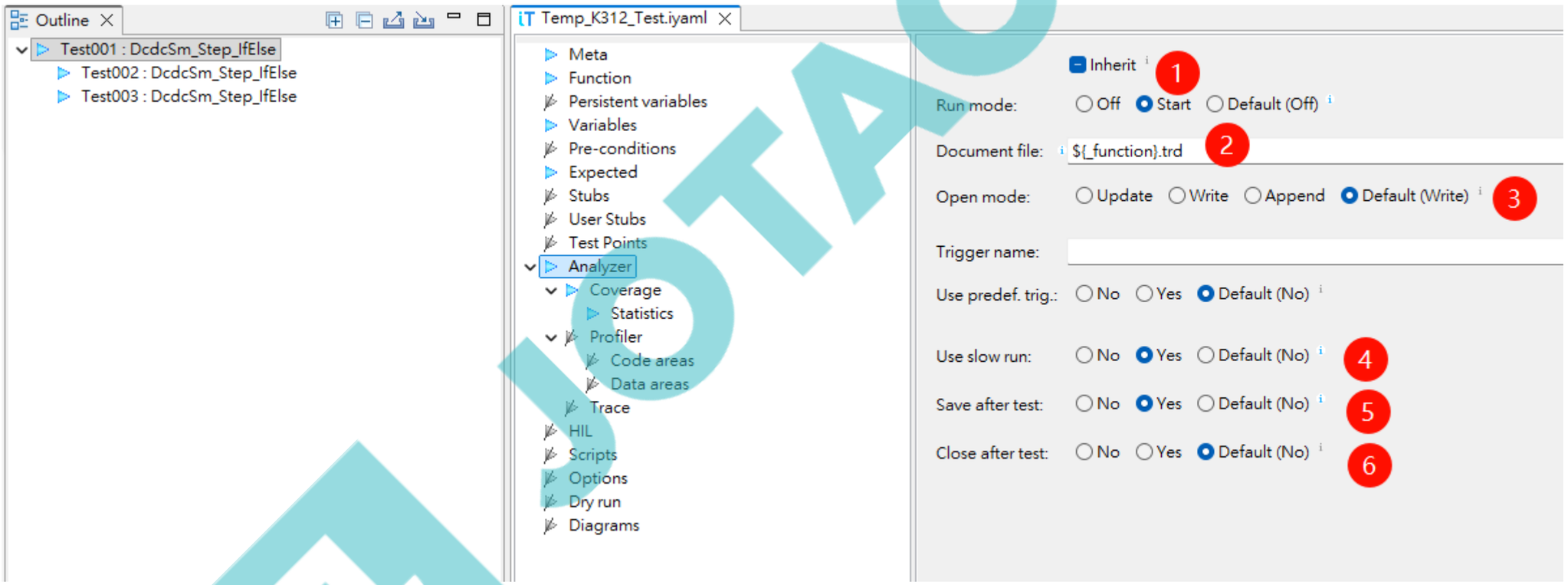
Assert expressions	
Expression	Sub-expressions
retval==1	retval = 0x00000001 (1)

Coverage										
Document	DcdcSm_Step_IfElse.trd									
Export file	DcdcSm_Step_IfElse.html\Index.html									
Function	Obj. code all	Src. lines all	Cond. all	CC (Outcomes)	Obj. code executed measured (exp., abs.)	Src. lines executed measured (exp., abs.)	Conditions any measured (exp., abs.)	Cond. true only measured (exp., abs.)	Cond. false only measured (exp., abs.)	Conditions both measured (exp., abs.)
DcdcSm_Step_IfElse	46	13	9	44.4% (8/18)	**69.6%** (0.0%, 32)	**84.6%** (0.0%, 11)	**66.7%** (0.0%, 6)	**22.2%** (0.0%, 2)	**22.2%** (0.0%, 2)	**22.2%** (0.0%, 2)

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Other – coverage 結果疊加

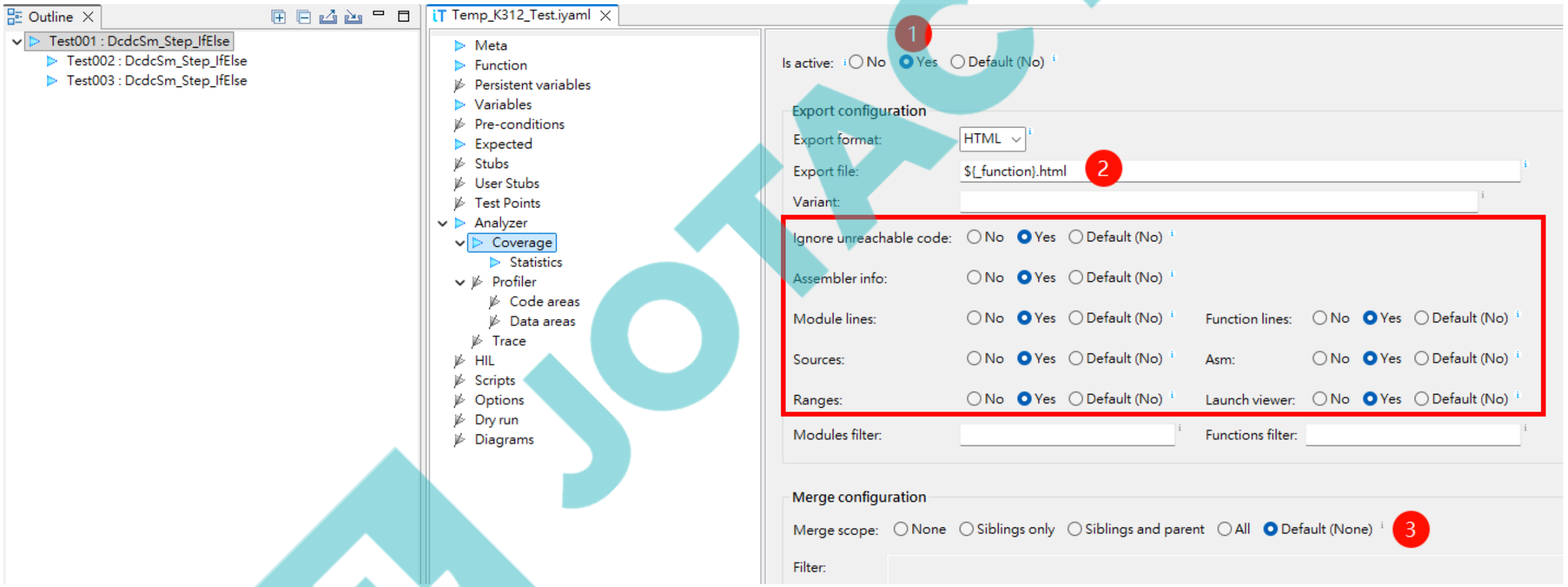
第一個測試案例在Analyzer，請依照下列標記進行配置。
Open mode必須設定write，當trd檔案不存在時工具會自動創建，而當檔案存在時會覆蓋成最新的資訊。



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Other – coverage 結果疊加

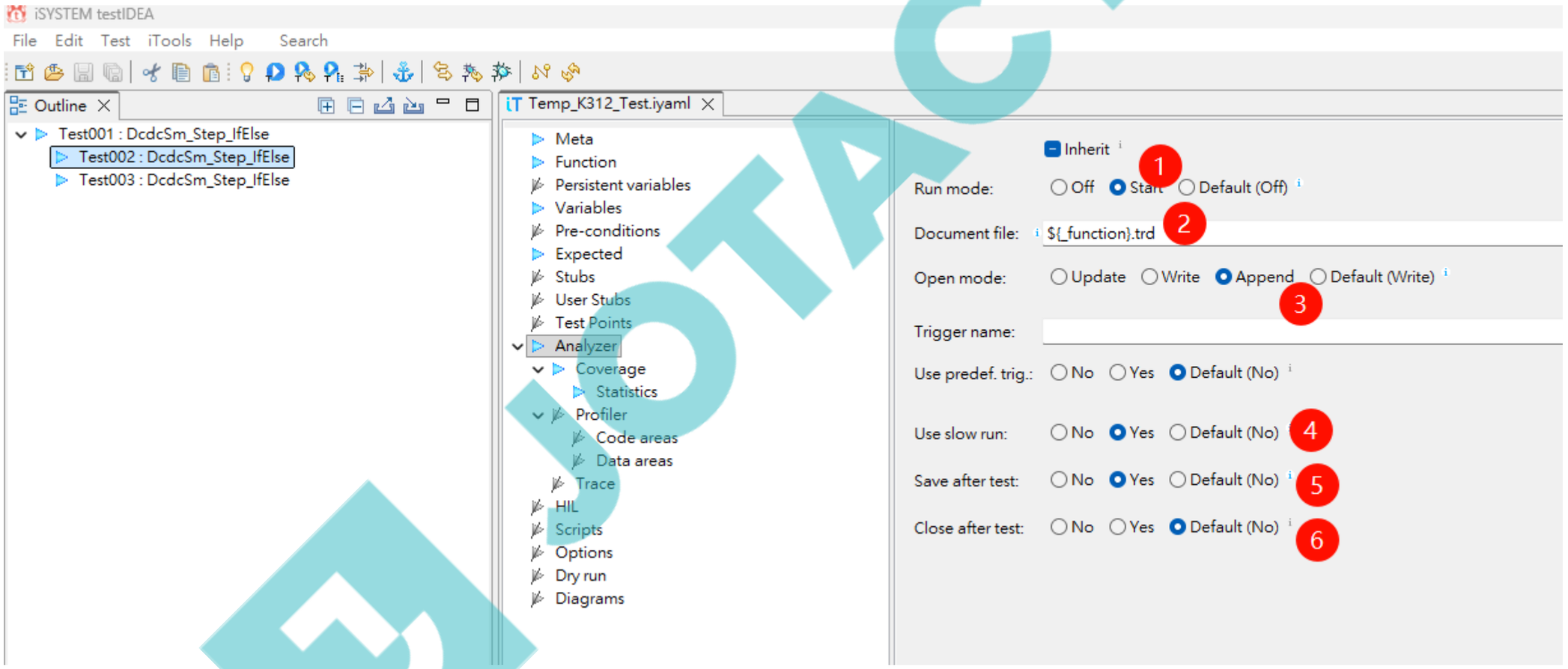
第一個測試案例在Coverage，請依照下列標記進行配置。
注意Export當案類型、檔名請設定一樣，coverage驗證項目也請設定相同。



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Other – coverage 結果疊加

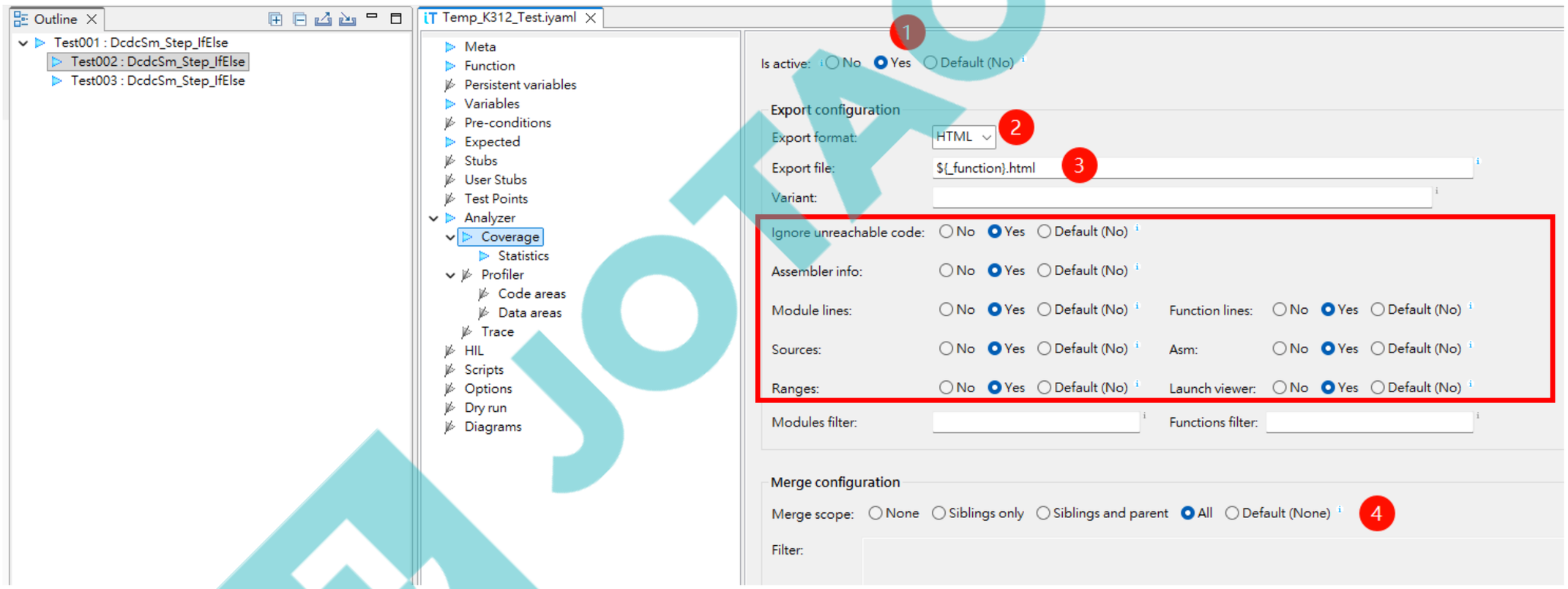
第N個測試案例在Analyzer，請依照下列標記進行配置。
Open mode必須設定Append，將當前執行的測試案例內容基於先前的內容往上疊加。



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Other – coverage 結果疊加

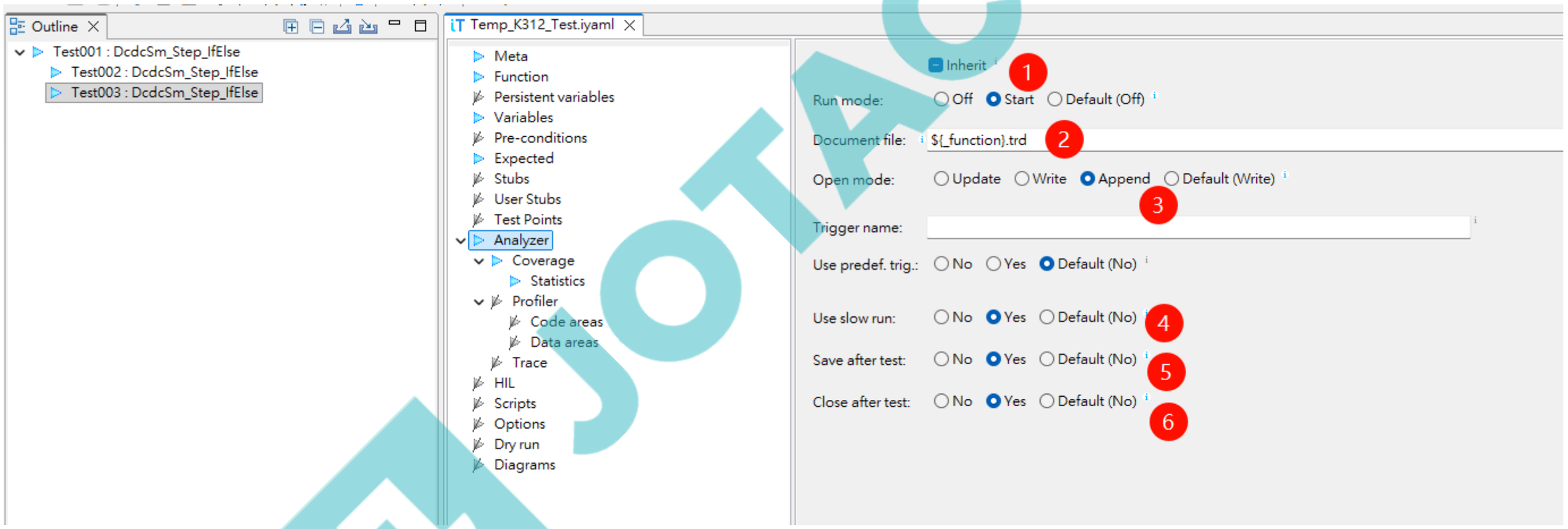
第N個測試案例在Coverage，請依照下列標記進行配置。
注意Export當案類型、檔名請設定一樣，coverage驗證項目也請設定相同。
Merage Configuration請務必設定成 “All”，將當前執行完的結果與先前的結果一起整合。



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Other – coverage 結果疊加

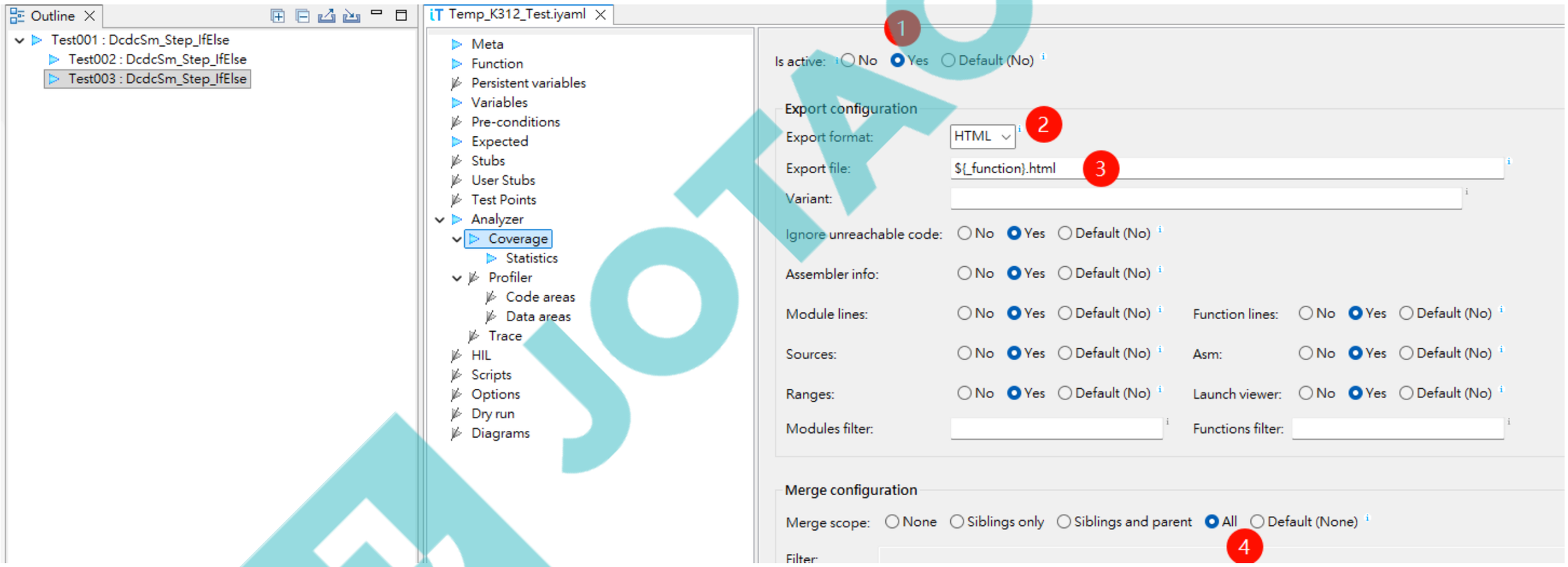
最後一個測試案例在Analyzer，請依照下列標記進行配置。
Open mode必須設定Append，將當前執行的測試案例內容基於先前的內容往上疊加。



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Other – coverage 結果疊加

最後一個測試案例在Coverage，請依照下列標記進行配置。
注意Export當案類型、檔名請設定一樣，coverage驗證項目也請設定相同。
Merage Configuration請務必設定成 “All”，將當前執行完的結果與先前的結果一起整合。



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Thank You

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